

A TELEVISION/COMPUTER SYSTEM FOR
HUMAN LOCOMOTION ANALYSIS

Thesis presented for the degree of Doctor of Philosophy

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The literature review, Chapter One, is in three parts. The first part concerns the development of locomotion analysis, placing particular emphasis on the methods of measurement used. The second part is a review of kinematic measurement systems in which the attributes and limitations of each method are fully discussed. The final part of the review describes other television/computer systems that have been used or developed for locomotion measurement and discusses the limitations of these systems.

Chapter Two discusses methods of detecting the spatial position of body segments using a television camera and justifies the use of a passive marker system to indicate anatomical landmarks. A suitable material for markers and its response is described. Methods of identifying the signals produced by the markers in the camera video output are suggested, and the circuits for the marker detectors used are presented.

The principles of operation of a television/computer interface designed to generate the spatial co-ordinates of markers are described in Chapter Three. A simple scheme to obtain these co-ordinates is first of all presented and the limitations of this method are shown and used to justify the development of a more complex digital system. A description is presented of the functional elements of this system, which generates the co-ordinates of markers detected in the video signals from up to six television cameras. The basic principles of television referred to in this chapter will be found in Appendix A1; similarly those aspects of the PDP 12 computer system which directly concern the interface are described in Appendix A2.

Chapter Four presents the logic of the interface in the form of a description and a set of logic drawings. The nomenclature used to describe the logic is first of all discussed and then a detailed description with the aid of timing diagrams and tables is presented for each drawing.

Computer programming of the interface is described in Chapter Five. The instruction set created to control the interface is listed, and two programming examples are provided to show how the instruction set may be used. A method of decoding the interface data, to relate the co-ordinates to the camera

which generated them, is also presented. A summary of the computer software written and listed in Appendix A3 is given.

Chapter Six describes how the system was tested in all aspects of its design and in its suitability for human locomotion measurement. Errors and methods of calibrating the data are discussed. The co-ordinates generated by the interface for the trajectories of markers placed on a walking subject are presented and a means of identifying markers from co-ordinate data is described. Some modifications to the basic design are suggested to give improved performance. The performance of the system is summarised and comparisons are made with other methods. Clinical and other applications of the system are discussed and recommendations for future work are given.

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CHAPTER I

REVIEW OF THE LITERATURE

- 1.1 Introduction
- 1.2 Development of Locomotion Analysis
- 1.3 Kinematic Measuring Systems
 - 1.3.1 Photographic Systems
 - 1.3.2 Non Photographic Optical Methods
 - 1.3.3 Contacting Methods
 - 1.3.4 Other Techniques
- 1.4 Television Systems

1.1 Introduction

It is the purpose of this chapter to review the literature concerning the development of locomotion analysis, the different types of kinematic measurement systems and television/computer systems.

1.2 Development of Locomotion Analysis

Scientific enquiry into the nature of human locomotion has a history extending back to the time of Ancient Greece. Aristotle (384-322 BC) described the actions of muscles and subjected them to a geometrical analysis. Leonardo da Vinci (1452-1519) described the mechanics of the body in standing, walking up and down hill, rising from sitting, and jumping. William Harvey, in his notes "De Motu Locali Animalium" (Harvey (1627)), frequently refers to the work of Aristotle when discussing the relative motions of body segments. In these notes Harvey also stresses the function of the antagonistic action of muscles in modulating movement. Typical of these philosophers was the great range of their studies. Harvey was no exception, and his reference to the kinds of gait in man resembling that of a duck, a crane, a crow etc. were not facetious comments but typical of the way in which he drew his illustrations from every kind of animal life which he had studied. Borelli (1680) described forward motion as the projection of the centre of gravity of the body and consequent loss of stability which was regained by swinging the limb through. Borelli considered the skeleton as a system of levers and again did not confine his studies to humans, as Figure 1.1 shows.

The Weber brothers (1836) were perhaps the first to attempt some quantitative measurement of locomotion. They used measuring lines and evolved a theory which considered the swing phase of gait as a pure pendulum. This theory was later repudiated by Fischer (1895-1904). Carlet (1872) investigated human walking using a pneumatic system of recording displacements of parts of the body, foot pressure and periods of muscular activity. The test subjects were constrained to walk in a circle around the recording equipment.

It was not until the advent of photography that significant advances in quantifying locomotion were made. Muybridge (1882), in America, demonstrated

Figure 1.1. Aristotle's theory of motion.

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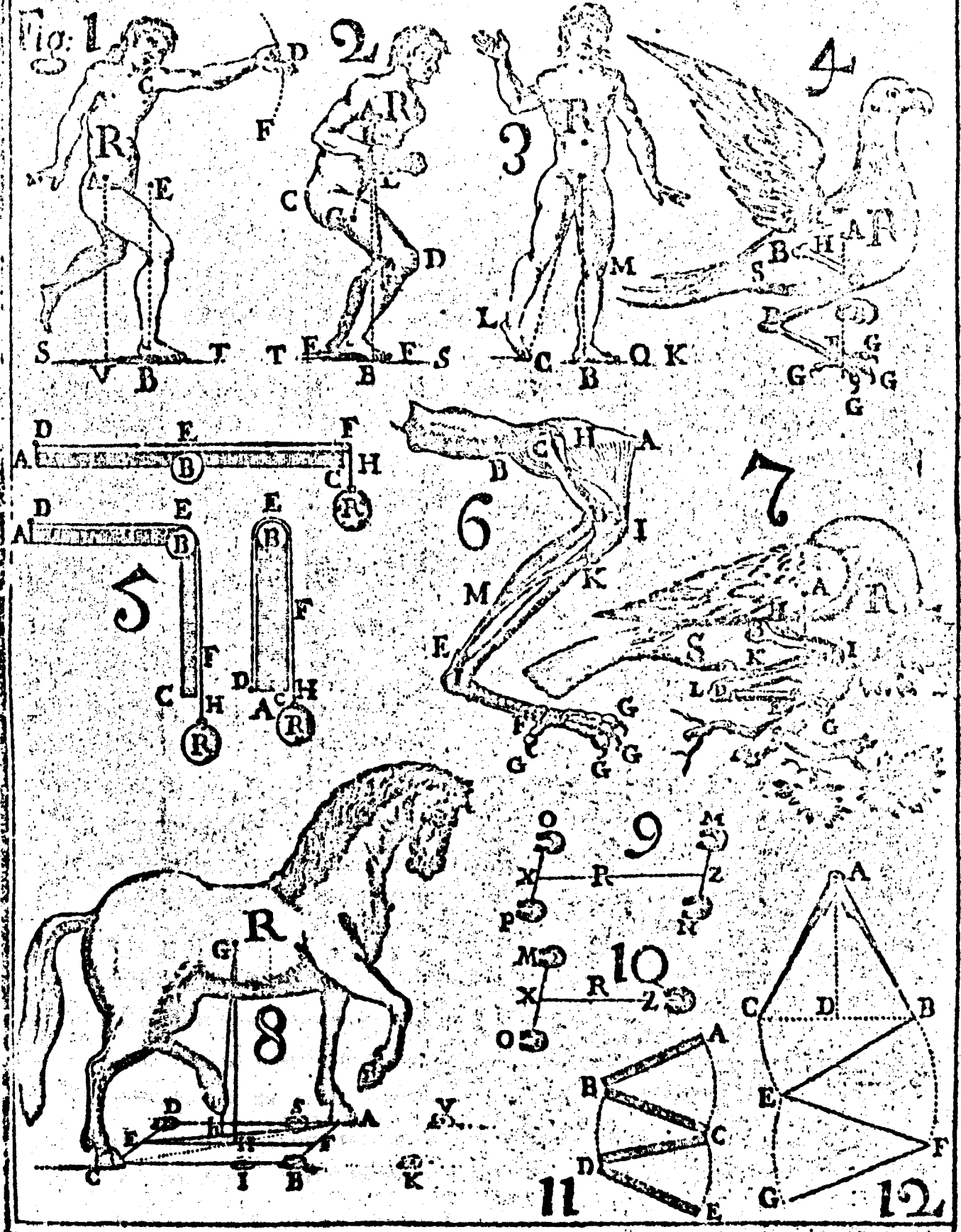


Figure 1.1. Plate from Borelli (1680).

the ability of the photographic system to arrest motion. He used 24 fixed cameras and two portable batteries of 12 cameras each to photograph animals and people in action. Investigations were performed on human subjects walking, running, rising from the sitting position and various other activities. The serial photographs were combined and used to show a slow motion moving picture of the recorded event. In some of the studies the background was divided into squares to allow a quantitative estimation to be made of the displacements of various parts of the body. The photographic technique was further adapted and improved by Marey (1895). His technique was to use a single plate camera which had a rotating disc with slits mounted in front of the lens. The subject was dressed in black and the limbs and joints were marked with shiny strips and bright buttons. The tests were carried out in bright sunlight and a "stick" diagram of the subjects motion was recorded on the plate, Figures 1.2 and 1.3. This technique, known as chronophotography, is still in use in various forms today.

Fischer (1895-1904) was the first to perform a comprehensive analysis of three dimensional movements of parts of the human body. In these investigations the subject was dressed in black and carried incandescent light tubes strapped to the various segments of the body. The subject walked in a darkened room in the view of four plate cameras; the plates being exposed at a constant cyclic rate. The exposed images of the tubes on the plate gave stick diagrams representing the successive positions of body segments. Measurements from these plates were corrected to eliminate errors due to perspective, which was complicated by the layout of the cameras. Two of the cameras were perpendicular to the axis of progression (one on each side) and the other two were positioned obliquely at 30° to the line of progression to obtain the frontal view. The information was analysed to obtain displacement, velocity and acceleration curves against time in three dimensions. This data together with data collected by Fischer on the mass properties of the body segments was used to calculate resultant forces acting on the centres of gravity of the segments. Fischer's work has been referred to by all subsequent investigators and his findings largely conditioned the mechanical design of external prostheses for the first half of this century.

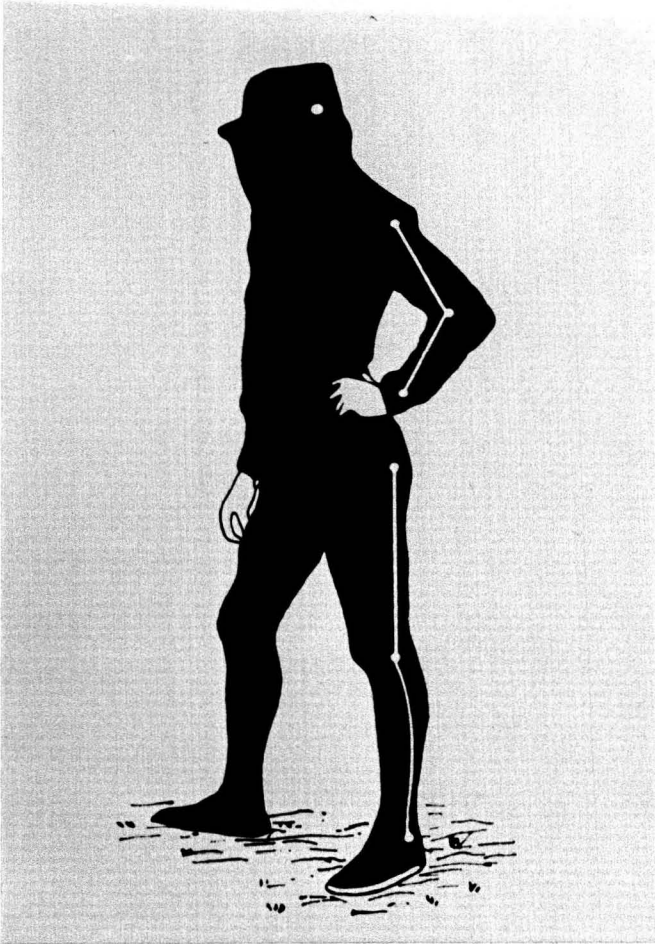


Figure 1.2. One of Marey's subjects dressed for photography
(From Bernstein (1967)).

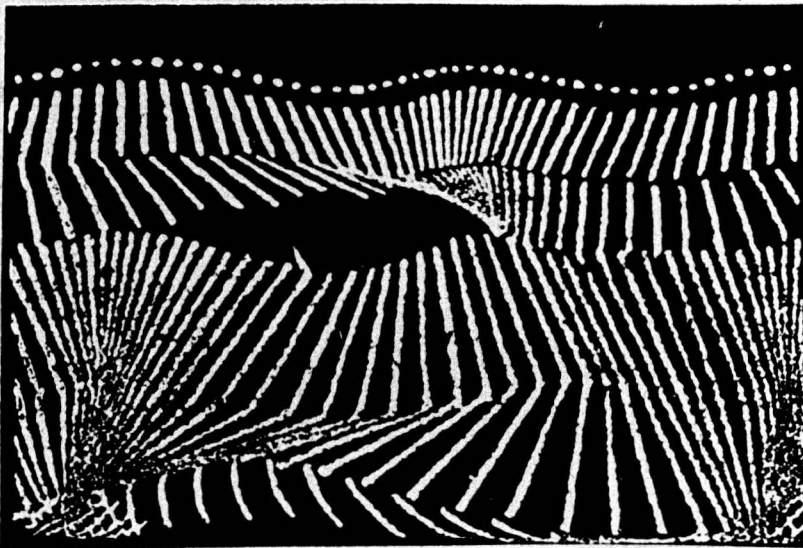


Figure 1.3. Stick diagram produced by Marey's technique
(From Bernstein (1967)).

Amar (1916) extended Fischer's work by developing an instrument which measured three components of ground to foot force. This "force plate" was of purely mechanical design, spring deflections indicating the applied forces. Investigations into the gait of subjects wearing external prostheses were made, and the results were used to suggest improvements in prosthetic design. Elftman (1938) also designed a mechanical force plate, in which the spring deflections were recorded by a high speed cine camera, which also recorded the position of the foot in one plane. Elftman (1939) extended his work to study the planar kinetics of the leg, using the force plate to measure stance phase forces and cine photography to measure displacements. As recording techniques improved investigators attempted to look for finer details in the recorded patterns of locomotion. Bernstein (1940) used chronophotography with repeated exposure rates from 60 to 190/sec and claimed that small details in the patterns of motion, in the derived curves of force etc., were common to all normals in locomotion. In the waveforms of the horizontal component of force acting on the centre of gravity of the thigh he claimed to observe, consistently, 10 distinct features in one cycle - Figure 1.4. Studies were made of the development of running technique in children using chronophotography, as well as many other types of movement. Data on the segment mass parameters of the body was also collected by Bernstein (1934) from studies of 150 live subjects. Most of Bernstein's work was carried out in two dimensions, but he did some tests in 3 dimensions by means of stereo photography (using a mirror to provide the second image).

A major landmark in locomotion analysis was the research carried out by the College of Engineering and the Medical School of the University of California, Berkeley. Their report, University of California (1947), covers a wide range of work, and provides much original information about the characteristics of human locomotion in the normal and in the amputee. Their studies provided data on the rotations of the lower limb segments about their long axes, Levens (1948). This data was obtained by making measurements of the spatial position of stainless steel pins which were inserted into bony prominences of the limb, from the recordings of three 35 mm cine cameras. Two dimensional

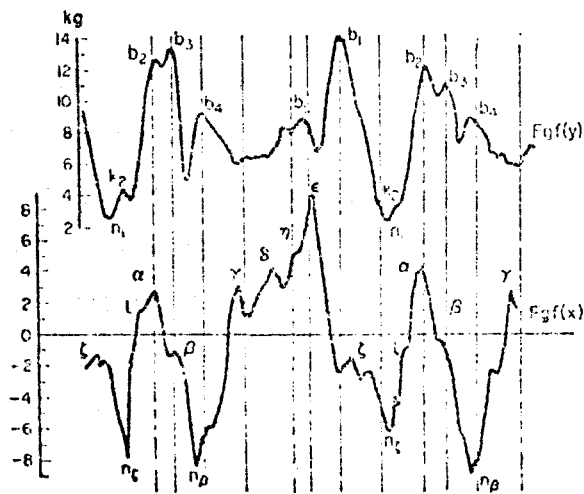


Figure 1.4. Force curves at the centre of gravity of the thigh in normal walking used by Bernstein to illustrate distinctive features during the walking cycle. Above: vertical components. Below: horizontal components. (From Bernstein (1967)).

studies of motion were made using interrupted light techniques, and three dimensional measurements were made with cine cameras. One of the principal advances made by the California group was the development of a force plate which could measure the six quantities necessary to define the resultant force actions transmitted between ground and foot during walking. The objectives of these studies were "1) to gain an accurate knowledge of the functions of components (of the body), so that repair techniques could be improved, 2) to form the basis of physiotherapy treatment, 3) to provide design parameters for external support devices and limb replacements". Figure 1.5 shows a part of the experimental set up.

Details of the labour involved in the University of California studies are contained in Bresler and Frankel (1950). In this paper it is revealed that to calculate the external forces and moments in the leg during normal level walking some 14,000 numerical calculations were made, 72 curves were plotted, and 24 curves were subjected to graphical differentiation for only one stride. The calculations were performed on desk calculators and the data processing for the first subject took 500 man hours, this was reduced to 250 man hours for the fourth (last) subject.

In a historical review of gait analysis Steindler (1953) claimed that "kinetic analysis of gait reveals and enlightens many situations concerning the practising physician....", also that it had "...become apparent that the kinetic analysis of gait has developed into a guide for clinical practice and an effective adviser in difficult and controversial situations....". The colossal manual effort involved in a proper kinetic analysis of gait certainly precluded its use in a clinical situation, and restricted the number of tests that could be made for research studies; hence the results of such studies needed to be treated with caution if they were to be applied to other subjects.

Saunders et al (1953) proposed, on the basis of the University of California's research, that locomotion is fundamentally the translation of the centre of gravity of the body through space along a pathway requiring the least expenditure of energy. Six factors which had a major influence on this motion were identified:- rotation, tilt and lateral displacement of the pelvis together with knee and hip flexion and knee/ankle interaction. Loss of any of these motions lead to

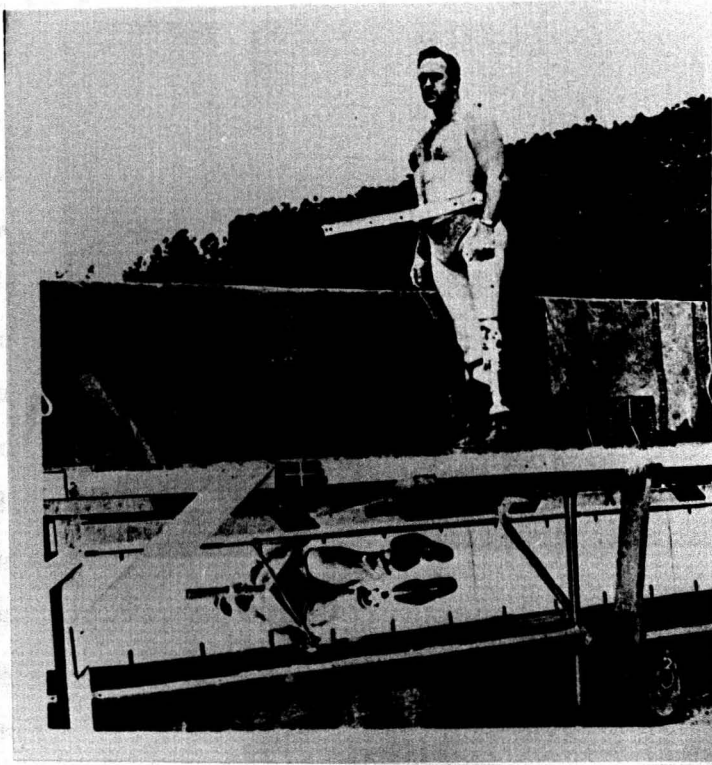


Figure 1.5. Part of the University of California's experimental set up - the glass walkway. (From University of California (1947)).

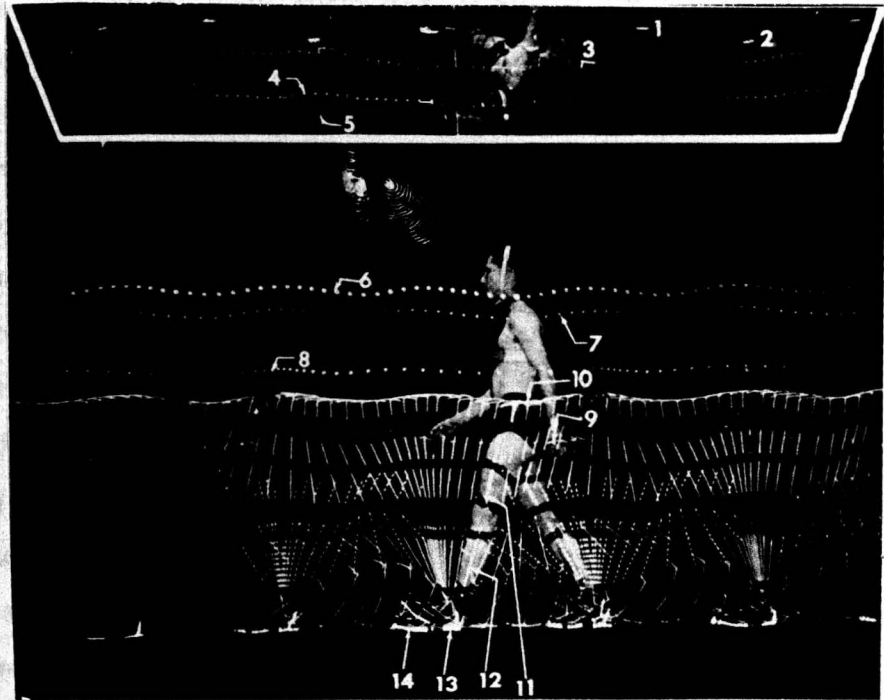


Figure 1.6. Photographic recording of locomotion as used by Murray (1964).

compensation by the others, which was reasonably effective if only one determinant was lost but the loss of two determinants lead to an increase in energy requirements of three times. The force plate and interrupted light photography were used in these particular studies, and the energies referred to were the mechanical energy requirements not the physiological energy requirements. The cadence of normal gait was compared with that of various pathological gaits by Drillis (1958). The cadence for normals was found from a sample of 936 pedestrians on a New York city street. Measurements of pathological gait were made in the laboratory using foot contact switches to record the times of the various phases of gait, and a tachograph was used to record the horizontal velocity of the trunk. More detailed studies were made using interrupted light photography at frequencies from 25 exposures/sec to 100 exposures/sec. Mention is also made in this paper of the use of 3 accelerometers to determine the acceleration of the subjects' centre of gravity, but no results are quoted.

Use was made of a stroboscope to obtain interrupted light stick diagrams by Murray (1964). 60 male subjects were analysed, measurements being made of 1) duration of the walk cycle and its phases, 2) length and width of steps and strides and foot angles, 3) saggital rotations of pelvis, hip, knee and ankle, 4) vertical, forward and lateral excursions of the head and neck, 5) transverse rotation of the pelvis and thorax, and 6) saggital excursions of the upper extremities. 14 markers were used and the subject was filmed in semi-darkness in one plane, but with an overhead mirror to provide the view of lateral movements. Figure 1.6 shows one of the photographs from which measurements were made, obviously a considerable amount of manual effort was required. Murray suggested that the results of these investigations showed the walking patterns of normal men, however although a fairly large sample was taken compared with other work the size of sample was still relatively small. The 60 subjects were split into 5 age groups, and the sample for each age group was split into three further groups by height.

The resultant force action at the human hip joint was studied by Hunt (1965) using cine cameras and a force plate similar in design to that at the University of California. During this work accelerometers were used, mounted

on the shank, to check the differentiation procedures used to obtain acceleration from the cine displacement/time data. The results showed that, generally, the differentiation procedure was satisfactory and that the use of accelerometers available at that time would involve excessive complications and constriction of the test subject. Paul (1967) determined the forces transmitted by the hip joint using cine photography, force plate and EMG recordings, during normal level walking. The displacement data was obtained in three dimensions by using two cine cameras, and measurements were made over a complete gait cycle. The manual measurement of the film and force plate records, and preparation for input to a digital computer took 23 man hours. A considerable improvement over the time required for the University of California studies but still a major obstacle for this sort of study. Paul was able to compare his results with an in vivo test made by Rydell (1966) who used an instrumented femoral head prosthesis. Paul's results showed some agreement with those of Rydell, when compared on a basis of body weight and stride length. Morrison (1967) used the same experimental techniques as Paul to evaluate the forces transmitted by the knee joint. The reasons for these studies were similar to those given by the University of California group.

Grieve (1968) introduced the concept of angle/angle diagrams, in which the angles of rotation about one joint were plotted against those of another joint. This method produced closed loops which Grieve suggested provided consistent and recognisable patterns, illustrating this by showing the changes produced by different walking speeds. Data for these studies was obtained using cine film and because of this time consuming method only 13 subjects were studied. Peizer (1969) used a variety of the available techniques to measure locomotion and also measured oxygen consumption and CO_2 production to assess the physiological energy cost of particular activities. An instrumented pylon, similar to that first used by the University of California (1947), was developed to measure directly the axial load, knee moment, anterior-posterior shear, medial-lateral shear and torque.

A study of hip joint motion in three planes was made by Johnston and Smidt (1969) using electro-goniometers. They measured extension and flexion, abduction and adduction, and internal and external rotation. Patterns of hip

movement for normal subjects were presented and qualitative observations of departure from the "normal" patterns were noted in certain pathological cases. Measurements from the recordings were made manually. Kettelkamp et al (1970) followed up Johnston's work with a similar study of the knee using the same goniometer apparatus. In both studies errors arose due to the geometrical offset of the goniometer with respect to the joint centre, and due to the mobility of the soft tissues to which the goniometer was strapped.

Wirta (1970) uses a variety of techniques for his analysis - electrogoniometers, foot switches, myoelectric detectors and twin force plates, together with specialist instrumentation to measure the spatial displacement of the hips. The significant aspect of Wirta's system is his data collection method. The system records the data on magnetic tape which is then processed automatically to provide data in punched card form, suitable for input to a digital computer. However this system still involves many physical attachments to the subject and hence constrains completely free movement, as Figure 1.7 shows. Extensive use is made of a computer, as the key to a data handling system, by Lamoreux (1971). In this system an exoskeleton, Figure 1.8, is used to measure skeletal motion. This apparatus is instrumented with potentiometers as in the more normal electrogoniometer. Information from instrumentation which measured pelvic displacement and heel contact was also recorded. The collected data was digitised and stored by a computer system and the data was subsequently processed to produce parameters such as flexion-extension at the joints, relative rotation etc. which were presented graphically. All the experiments reported in this paper were based on one test subject.

Assessment of locomotion performance after total joint replacement has been made using a quantitative measuring system by Charnley (1968) and Murray (1972). The system used by Charnley is discussed in a paper by Jacobs et al (1972) in which the waveforms of the vertical components of force, as measured by force plates, are subjected to further analysis. Comparisons are made between normal and pathological gait, and waveforms are classified according to shape. It was claimed that certain mathematical parameters help distinguish one wave from another and also bear a distinct correlation to the diagnostic state of the hip joint. Murray (1972) carried out an analysis on 30 patients

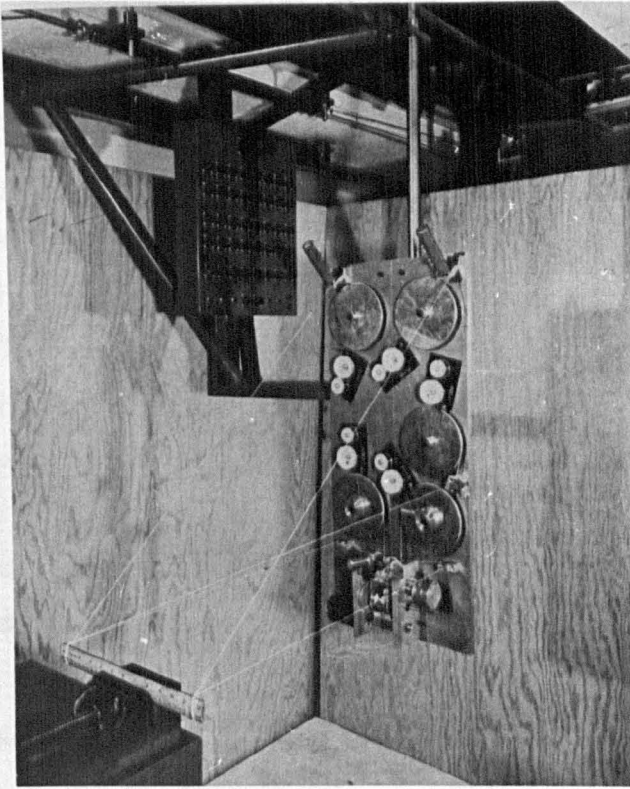


Figure 1.7. Wirta's apparatus to measure hip position during walking. (From Wirta (1970)).

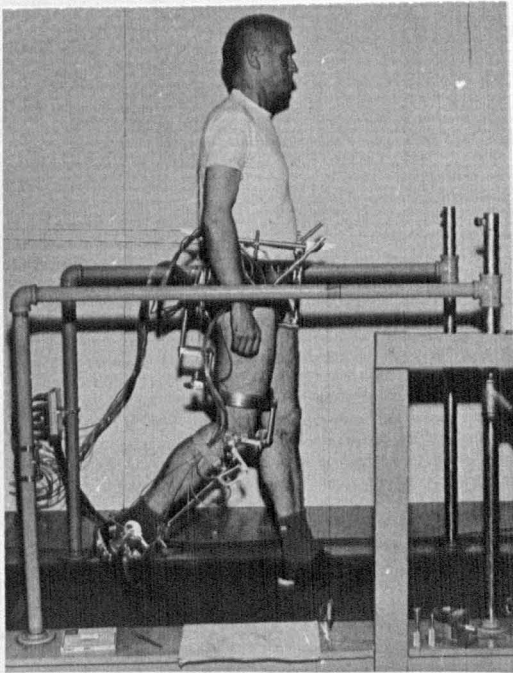


Figure 1.8. Lamoreux's "exoskeleton" in use. (From Lamoreux (1971)).

before and after hip joint replacement. The same measuring system was used as in Murray (1964) for the locomotion studies. The paper concludes by saying "The quantitative values presented in this study provide a basis for comparing the functional performance following major reconstructive procedures on the hip". However it is difficult to see how true comparisons can be made because of the number of variables - the ages of the subjects ranged from 36 to 85, there were large variations in the extent of the pathology, the degree to which other joints were affected, a lack of information about weight and height etc. The improved performance was clearly shown in the results, but a large amount of manual effort was required to obtain the data.

A strain gauged force plate and cine cameras were used by Poulson (1973) to perform a biomechanical analysis of the leg during fast and slow walking, walking up and down a ramp, and up and down stairs. In his studies on the biomechanics of the leg in normal level walking he considered 1) muscle force, 2) internal joint force, 3) muscle tension versus length, 4) segment energy levels, 5) joint power and 6) muscle power. The cine film and force plate record were transcribed into a form suitable for input to a digital computer by using an X-Y analyser. This device produces coordinates in punched tape form of the position of a pointer which is placed over the point on the recording to be measured. Even using this labour saving device $1\frac{1}{2}$ to 2 hours were still required to transcribe the data for one stride.

A statistical study of the kinematics of normal locomotion was presented in a paper by Winter et al (1974). The data for this two dimensional analysis was collected by a television-computer system. Three walking speeds were analysed over at least 3 strides for each test. Ranges of linear and angular measurements of displacements, velocities and accelerations are shown, and comparisons are made with the studies of Murray (1964) and Lamoreux (1971). These comparisons show agreement between the studies where comparable measurements were made; except in the case of the angular accelerations of the ankle reported by Lamoreux. Winter et al suggest that this difference might be attributed to the different types of footwear used, the instrumentation

worn by Lamoreux' subject and the fact that a treadmill was used. However the last two factors would certainly have affected the other parameters also. Lamoreux used a sampling rate of 200/sec compared with Winters 60/sec; this could also account for the difference in the accelerations measured because Winters' results would have been subjected to a much higher degree of smoothing and hence loss of the high frequency components which would produce the higher accelerations.

1.3 Kinematic Measuring Systems

For the purpose of this review these systems will be grouped under the following general titles:- Photographic, Non-Photographic Optical, Contacting, and Other Techniques.

1.3.1 Photographic Systems

Interrupted Light. This method was introduced by Marey (1895) and is still in use today. The use made of the technique by Marey and Fischer has already been discussed. Bernstein (1934) made refinements to the technique and used the system extensively, he referred to it as cyclography. Tiny gas filled electric light bulbs were used to mark positions on the test subject, the light from these bulbs to the camera was interrupted by means of a rotating shutter. Bernstein claimed that he could achieve exposure rates of up to 600/sec, although to avoid the recorded light dots from merging with one another, such high exposure rates could only be used when observing purely fast movements. Most of his experiments were made at exposure rates of between 60 and 190/sec., the measurement of shutter speed being accomplished by the determination of a tone emitted by a siren located on the shutter and rotating with it. Later an electromagnetic tuning-fork was used to switch a neon bulb and the speed of the shutter was adjusted until a circle of astericks marked on the shutter appeared motionless when illuminated by the neon. Enlargements were made of the exposed film and during this process a grid of 1/2 or 1 mm squares was superimposed on the recording. Measurements of the displacements of the recorded points were then taken and used for the analysis. Cyclography is not suitable for the recording of overlapping movements, and to overcome this

deficiency Bernstein introduced kymocyclography i.e. cyclographical exposures on slowly and evenly moving photographic film. A further extension of the technique was the three dimensional recording of movement by placing a mirror at 45° to the main optical axis of the objective and so effectively making a stereoscopic recording. The manual labour involved in obtaining quantitative data from these methods must have been colossal, although Bernstein refers to this part of the procedure as being "easy".

The University of California used the interrupted light technique in their fundamental studies of locomotion as reported by Bresler (1950) and Eberhart (1951). Ophthalmic electric bulbs were attached to the subject at estimated joint centres of the leg, on the iliac crest, and on the heel and toe of the shoe. The lights were continuously lit and the subject walked in a darkened room, the field of view of the open camera lens being interrupted 30 times/sec by a rotating disc with an 18° opening. The speed of the disc was made uniform by using a mains synchronised motor to drive it. A Kodatron speed lamp was synchronised with the shutter and fired to catch the subject in mid-field for identification. The data was measured from the resulting photograph using a toolmakers' microscope (30X magnification) and a scale factor. Only data in one plane was obtained using this method and Eberhart stated that the interrupted light technique was "not particularly useful in evaluation of gait".

More recent use of the method has been reported, by Murray (1964, 1967, 1972). Instead of using light sources as markers and a rotating shutter Murray uses reflective markers and a stroboscope flashing at 20 times/sec. Fourteen markers are used to identify various anatomical landmarks on the subject, and an overhead mirror in the field of view of the camera provides a view of lateral displacements of markers on the head, neck, thorax and pelvis. Although only sampling the positions of the points every 0.05 sec Murray frequently quotes intervals between events such as 1.06 ± 0.09 sec (Murray (1964), p.294).

All interrupted light measurement systems suffer from the problem of manual data reduction. The technique can be quite accurate for movements in one plane, but there are identification problems if movements overlap, and if a point remains more or less stationary (as in the foot during stance phase). Also

if the subject moves out of the defined plane of motion then parallax will occur. The recording of movement in 3 dimensions is not impossible, as Bernstein demonstrated, but it is difficult. Another disadvantage is that subjects are generally required to walk in a darkened room.

Cine. Muybridge's serial photographs of human and animal movement can be said to have pioneered cine photography. Elftman (1939) was one of the earliest users of cine photography to obtain displacement-time data of locomotion, although he only made two-dimensional measurements. The University of California made extensive use of the method and an evaluation of its use is given in Eberhart (1951). In these studies two cameras were used, set up to provide front and side views of a glass walkway (Figure 1.5). The side view camera was also used to provide a bottom view by means of a mirror placed at 45° to the vertical underneath the glass walkway; by this means it was possible to obtain an estimation of the transverse rotation of the leg. An adjustable contact brace was strapped to the pelvis locating on the posterior and anterior iliac spines; anterior and posterior projections were attached to this brace to magnify angular displacements and allow the transverse rotations of the pelvis to be measured. To measure transverse rotations at the ankle, targets were fixed to a "U" shaped bracket holding two small pins. The pins entered the cortices of the bone of the medial and lateral malleoli, after a local anaesthetic had been administered. These projections on the ankle and the pelvis could be seen via the mirror and hence it was possible to measure the transverse rotations. Sagittal and lateral displacements were measured from 1" diameter black dots on a white background placed over the joint centres of the limb. To minimise errors due to parallax the cameras were placed forty feet away. Measurements of the target displacements, to an accuracy of $\frac{1}{2}$ ", were taken from each frame of the exposed film; as the cameras were running at a film speed of 48 frames/sec. the manual measurement process took a considerable time. High speed cine, with film speeds of up to 700 frames/sec., was also used during these studies. Filming had to be done outdoors in bright sunlight. No quantitative results are quoted from the high speed cine tests and it appears that only qualitative use was made of the technique.

A much higher degree of accuracy was obtained by Paul (1967) who used cine photography in his studies of the forces at the hip joint. Two Paillard Bolex H16 reflex 16 mm cameras were used running at a film speed of 50 frames/sec. Synchronous electric motors provided the drive to the cameras and the frame speed was shown to be constant within 0.4% by filming a crystal controlled timing unit. After a test run the film was re-wound and then re-exposed to a grid of 5" squares placed in a known position in the field of view. Subsequent measurements were taken from the film using the grid as a reference, optical errors thereby being restricted to those occurring within a 5" square. A high level of illumination was required (8 KW total giving an illumination of 260 foot candles over the measurement area) as the markers used to identify the anatomical landmarks were small ($\frac{1}{4}$ " diameter) white paper spots, and exposure times were relatively short for indoor high speed filming (1/125 sec). The camera records were synchronised by firing a flash bulb which was in the field of view of both cameras. Firing of the flash bulb also triggered an event marker which was recorded on a U.V. recorder along with ground reaction forces provided by a force plate. A worst possible phasing error of 18 m S was quoted, and the measurement accuracy of the displacements of the markers was stated to be 0.05" (~ 1 mm). The measurement of film and force plate records for one test on one subject occupied two operators for approximately 9 hours; preparation and checking of the data for input to a computer took a further 5 hours. Corrections for parallax were made in the subsequent analysis.

In an attempt to remove the manual effort required to analyse cine film Kasvand (1971 & 1972) developed a computer-based system to do the processing. A flying spotscanner under computer control was used to interrogate each frame of the test film. Two digital to analogue converters provided the deflection voltages, from digital x, y coordinates, to position the scanner beam. Once the deflection voltages had been set up the scanner beam was turned on and the output of the photomultiplier was read with an analogue to digital converter to provide a measurement of the transparency of the film at the point x, y. It was stated that a 35 mm frame can be resolved into 4,000 x 3,000 elements

with this system. During filming the camera was moved along parallel to the subject and to provide the necessary spatial reference 5 parallel digital encoded bars contained between two continuous bars were visible in the background. Anatomical landmarks were indicated by 1" diameter black spots on a white background, the spatial reference bars being 1" wide and also black on a white background, as seen in Figure 1.9. The computer programs used to obtain the displacements from the film provided for operator intervention at any stage. Initially a coarse scan of the first frame was done and the results displayed, the operator then marked off the area of the spatial reference code and selected the spots to be analysed, defining a neighbourhood for each spot, Figure 1.10. Having set up the initial conditions the computer program then reads the reference code and locates the specified spots from frame to frame, providing a continuous on-line display as shown in Figure 1.11. Processing speed for 5 spots in one frame was about 15 sec if no operator interaction was required. EMG signals from 4 major muscle masses were recorded simultaneously on a FM tape recorder together with signals from foot switches which were used to synchronise EMG data and displacement data. No details of the measurement accuracy of this system were given and the difficulties of expanding the system to three dimensional measurement were not discussed. Synchronisation of EMG records and displacement data, by using the foot switch signals, could not be particularly accurate because of difficulties in estimating the corresponding times of heel contact etc. from the displacement data.

An automatic system for transcribing data from cine film has also been developed in the Biomechanics Facility of the Childrens Hospital in Iowa City, U.S.A. (Pepoe (1970)). This system utilises fibre optics to provide a line scan of cine film, and is interfaced to a PDP 12 computer. Three sheets of fibres are used, with 1024 fibres in each sheet. The fibres are arranged in bundles in such a way as to digitally encode the position of a light spot falling on the fibre ends by illuminating an array of phototransistors. The sheets are clamped together and cine film is projected onto the fibre ends. The illuminated phototransistors provide a digital x co-ordinate (to 1 part in 1024) of any light

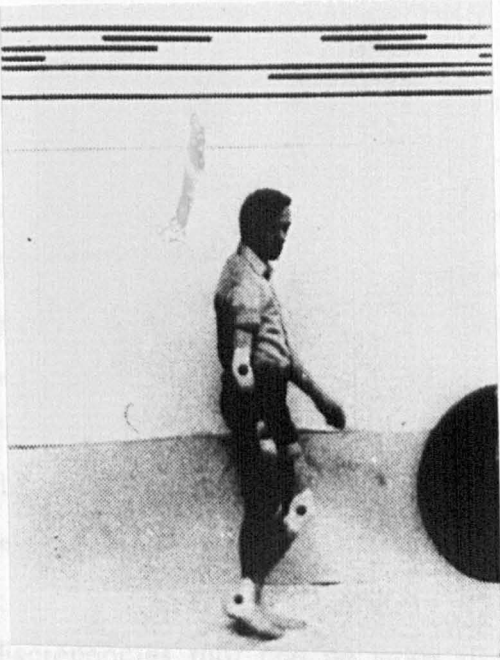


Figure 1.9. Marker and spatial reference system used by Kasvand (1971).

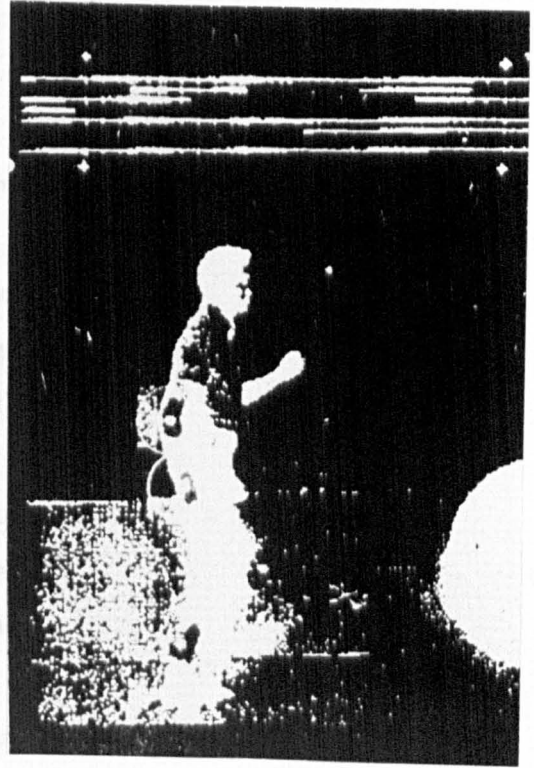


Figure 1.10. Coarse computer scan with local search areas marked. (Kasvand (1971)).

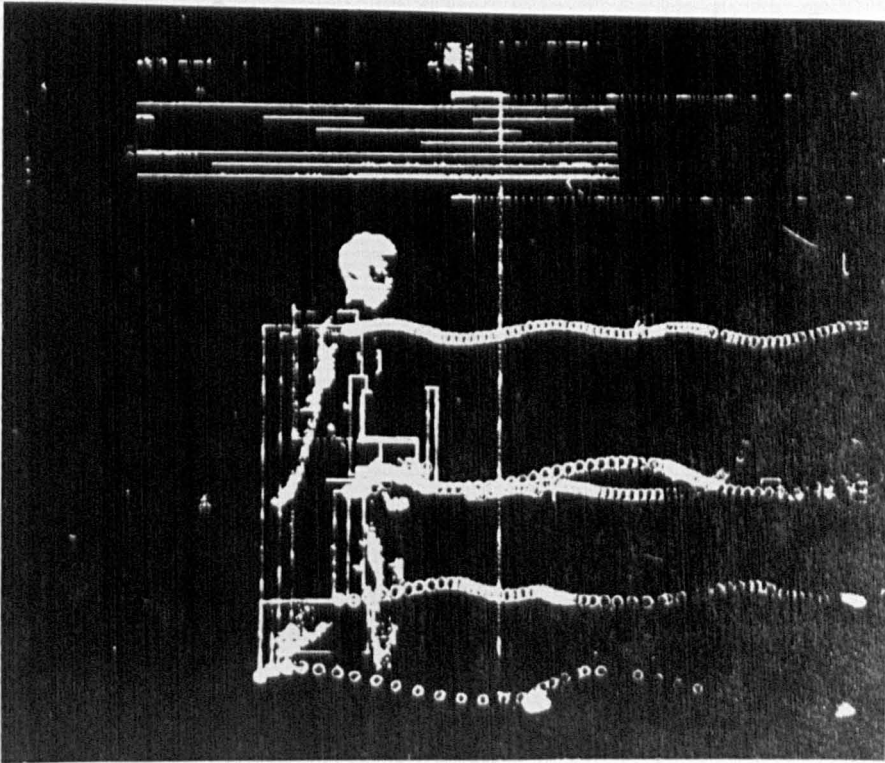


Figure 1.11. On line computer display of marker trajectories produced by Kasvand's system (Kasvand (1971)).

spots in the film. The y co-ordinate is provided by a shaft encoder which is rotated by the moving film. The shaft encoder increments a counter in the PDP 12 which is reset at the beginning of each cine film frame, providing 750 counts/frame. If two light spots fall in line then the co-ordinates generated by the system will be erroneous. This would prove to be a limitation when markers are placed close together, such as on the foot, or at the pelvis. The film is processed at the rate of 2 frames/sec., which is faster than the flying spot scanner method of Kasvand (1971), but further processing would be required to identify co-ordinates belonging to particular markers.

In a more conventional cine system Sutherland (1972) discusses the discrepancies that can be made in manual measurements of cine film. Comparisons were made of the differences in values computed from measurements, made by two observers, of the same piece of film. The parameters compared were rotation of the pelvis, femur and foot; flexion-extension of the knee; and plantar flexion and dorsiflexion of the ankle. The discrepancies in values were most serious where the total range in the value of the parameter was small. The worst case was pelvic rotation in which there was an average discrepancy of 2.6° (20% of total range) and a maximum discrepancy of 6.3° (48% of total range). The best agreement obtained by two observers was in the measurement of flexion-extension of the knee. Here the average discrepancy was only 5% of the total range and the maximum was 13%. Repeatability of measurements by a single observer was found to be better and resulted in variations of computed parameters of 0° to 2.5° . Sutherland used a Vanguard motion analyser to obtain the displacement measurements from the film; even so it took 2 hours to digitise the data.

Cine systems have played a major part in the most important locomotion studies. However the manual effort required to obtain data from film has been a great obstacle. The number of subjects studied and the number of tests carried out for each subject has been severely limited by this constraint. Semi-automatic systems of film analysis have reduced the data collection time substantially, fully automatic systems are still in the development stage and present problems of synchronisation of film data and other data. If a fully automated system were developed to the point where three dimensional studies

of movement together with data from other sources could be achieved there would still be disadvantages in using cine. Firstly the recorded movement is not immediately available, the film must be developed; secondly in addition to the cost of computer processing time (which will be required for any comprehensive analysis) there will be an appreciable cost in film and film processing; thirdly flying spot scanners for this purpose are not commercially available; and lastly an appreciable amount of computer time would be required for data collection.

Photogrammetry. The technique of obtaining three dimensional measurements from two planar recordings of an object has been used extensively in surveying (Halbert (1960)). The method requires that two simultaneous recordings are made of the spatial position of the object. Providing the distance (base) separating the recording devices is known and the focal length (principal distance) of the optical system is also known then the third dimension (usually called depth) can be derived from the two planar measurements. Bernstein (1934) made the first use of the method for studies of human movement; no details of the accuracy obtained were given and his general method has already been discussed. More recently Ayoub (1970) has developed a stereometric system for measuring human motion. A measurement accuracy of 2% was claimed, although to obtain this it was stated that it was necessary to measure the differences in the spatial positions of common points in stereo-pairs (the two planar recordings) to within 0.0004 inch. Stereocomparators can certainly measure to very high accuracies, but it must be remembered that when photographing movements, because of the relatively long exposure times used, there is certain to be a degree of blurring of the image. To use the limits of accuracy of a stereocomparator sharp edges are required. The other measurements involved, base and principal distance, must also be known to a high degree of accuracy, as any errors in these measurements will affect the overall accuracy considerably. Ayoub studied hand and arm movements using stereo-photography and the interrupted light technique to produce multiple exposure stereo-pairs. The type of movement studied is limited for the same reasons as with interrupted light photography and again manual measurement of the results is necessary.

Lippert (1973) used a photogrammetric system to study the mobility of fracture fragments of the tibia. Lippert rejected conventional methods of movement measurement, such as cine photography, because of the "artificial restrictions imposed on the subjects' movement by the measuring apparatus". In Lippert's tests targets were attached above and below the fracture site by stainless steel pins inserted into the bone (through both cortices). A Zeiss SMK-40 stereocamera was used and a measurement resolution of 1.0 - 1.5 mm was claimed. The patient was photographed, on the same film, in four positions - one "no load" and three other positions which put various loads on the limb. The tests had to be carried out in a darkened room which, with patients whose movements were restricted by their plaster casts, was inconvenient and occasionally led to targets being knocked. Measurements from the exposures were made manually and 6 personnel were required to run the tests.

Photogrammetric methods can be used to obtain three dimensional displacement-time data for human movement, but very precise alignment of the photographic system is required. Measurements must be made manually using a stereocomparator, which is very costly and could not be justified for exclusive use in these sort of studies. The clinical use suggested by Lippert is not acceptable in terms of the effort required, and the insertion of pins into the bones of patients.

1.3.2 Non Photographic Optical Methods

Methods of this type did not become available until the late 1960's. Obviously television/computer systems could be included under this heading but discussion of such systems will be deferred. Limited studies using non photographic optical systems have been carried out, but so far the studies have been restricted to kinematics (and kinetics where no external forces are involved) and no comprehensive analysis of locomotion has yet been presented. In some cases the systems have only been available for a short time and no doubt the inclusion of other measurements will soon be attempted; in other cases the additional facilities required for measuring other aspects of locomotion have not been available.

Systems using polarised light have been developed by Reed and Reynolds (1969), Grieve (1969) and the University of Loughborough (1973 and Mitchelson (1975)). The techniques used are essentially identical and so a description will be limited to that of the University of Loughborough system. A schematic diagram of the system is shown in Figure 1.12. Light from a D.C. powered light source is polarised by a linearly polarised filter which is rotated. This has the effect of rotating the plane of polarisation through 180° , which happens twice for every revolution of the polarising filter. The polarised light is received by a matched pair of photodiodes connected in opposition to each other. A window of polarising filter is placed over each photodiode with the planes of polarisation at right angles to each other. The effect of this arrangement is that any non-polarised light incident on the photodiode pair is received in equal amounts by each and thus the resulting signals cancel each other. On the other hand polarised light incident to the photocells is first transmitted to one cell and extinguished at the other, and then vice versa as the plane of polarisation of the light source rotates. A sinusoidal voltage is thus produced by this receiver. A reference mark on the rotating filter of the light source is detected by a photocell, a reference pulse being produced every time the mark passes the photocell. The logic of the electronic circuitry is arranged so that a linear voltage ramp is initiated on receipt of the reference pulse, and the ramp is stopped when the sinusoidal signal from the receiver passes through its zero reference voltage in a negative going sense. The final value reached by the voltage ramp is stored in a sample and hold circuit. This voltage represents the angular displacement of the receiver with respect to the light source. Any orientation of the receiver to the light source can be chosen as zero angular displacement by delaying the reference pulse by an appropriate time. The output voltage is updated once for every revolution of the light source filter, in the case described 150 times a second. The measured noise level of the angular displacement output was stated to be in the region of 0.3 degrees (in a total range of 180°), and the linearity of the system was quoted as $\pm 0.1^\circ$ over the total range. Measurement errors can arise when the receivers undergo two rotations with respect to the plane of polarisation of the

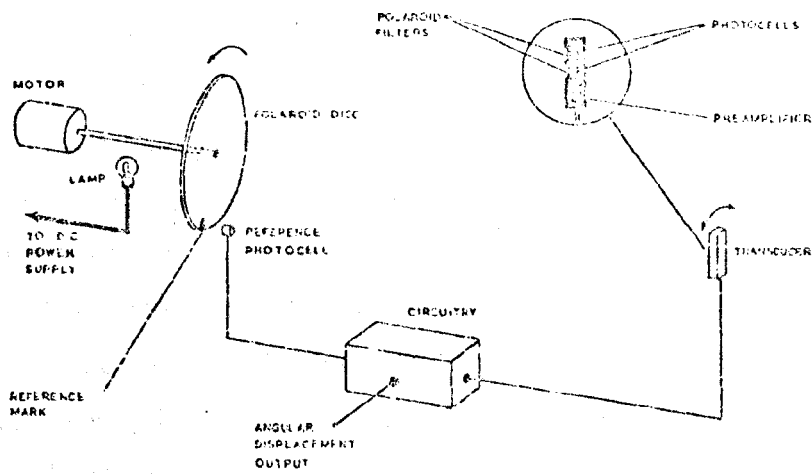


Figure 1.12. Schematic diagram of the Polarised Light Goniometer.
(From Mitchelson (1975)).

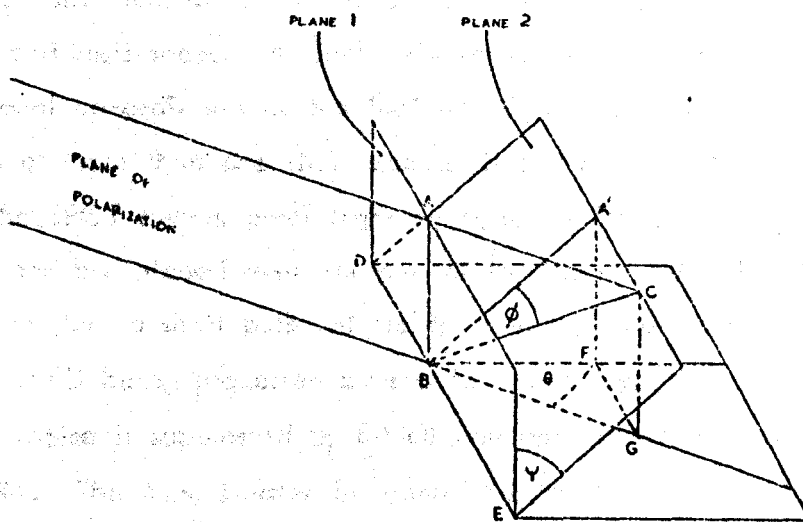


Figure 1.13. Diagram illustrating the error angle ϕ , due to the two rotations of the transducer ψ and θ .

light source, in addition to the rotation which is being measured. Mitchelson shows that this can amount to a measurement error of 7° for two additional rotations of 20° in planes mutually perpendicular to the plane in which the measured rotation is taking place. Obviously careful positioning of the receivers can keep such errors low for locomotion studies. Analogue outputs of angular velocity can be obtained by appropriate analogue processing of the angular displacement voltage; although the effects of noise must be carefully considered, and preclude the derivation of acceleration in this way.

This technique provides a relatively inexpensive means of obtaining angular displacement data. The disadvantage of the technique is the need to attach the receivers to the body and provide a power source and connecting leads to them. Also only relative measurements are made, the technique does not provide absolute measurements.

A continuous light spot position sensor forms the basis of the commercial SELSPOT system, Lindholm (1974), Selcom (1975). The sensor is basically a large area silicon photo-diode of the Schottky barrier type manufactured by United Detector Technology Inc.. An equivalent circuit of a single axis sensor of this type is shown in Figure 1.14 (from Woltring (1973)). The average position of a light spot imaged on the sensor surface at any instant varies the current in the load resistances. A dual axis version of the sensor provides the two dimensional co-ordinates of the incident light spot. In order to monitor the position of more than one light source time division multiplexing must be used. In the SELSPOT system small light emitting diodes (LED's) are used as the light sources and are placed over anatomical landmarks. The LED's are switched on in turn to give a short pulse of light; the x, y co-ordinates from the detector for each LED being processed by special noise suppressing and linearizing circuits (noise is suppressed by 60 dB and non-linearity is decreased from 60% to 0.5%). The co-ordinates for each LED are then made available as analogue signals on individual output channels. The resolution of the system depends to a large extent on signal to noise ratio in the signal processor and detector, and hence on the incident power of the light source on the detector. For sufficiently large incident power (~ 1 mW) a resolution of 10^{-4} (1 in 10,000) is quoted

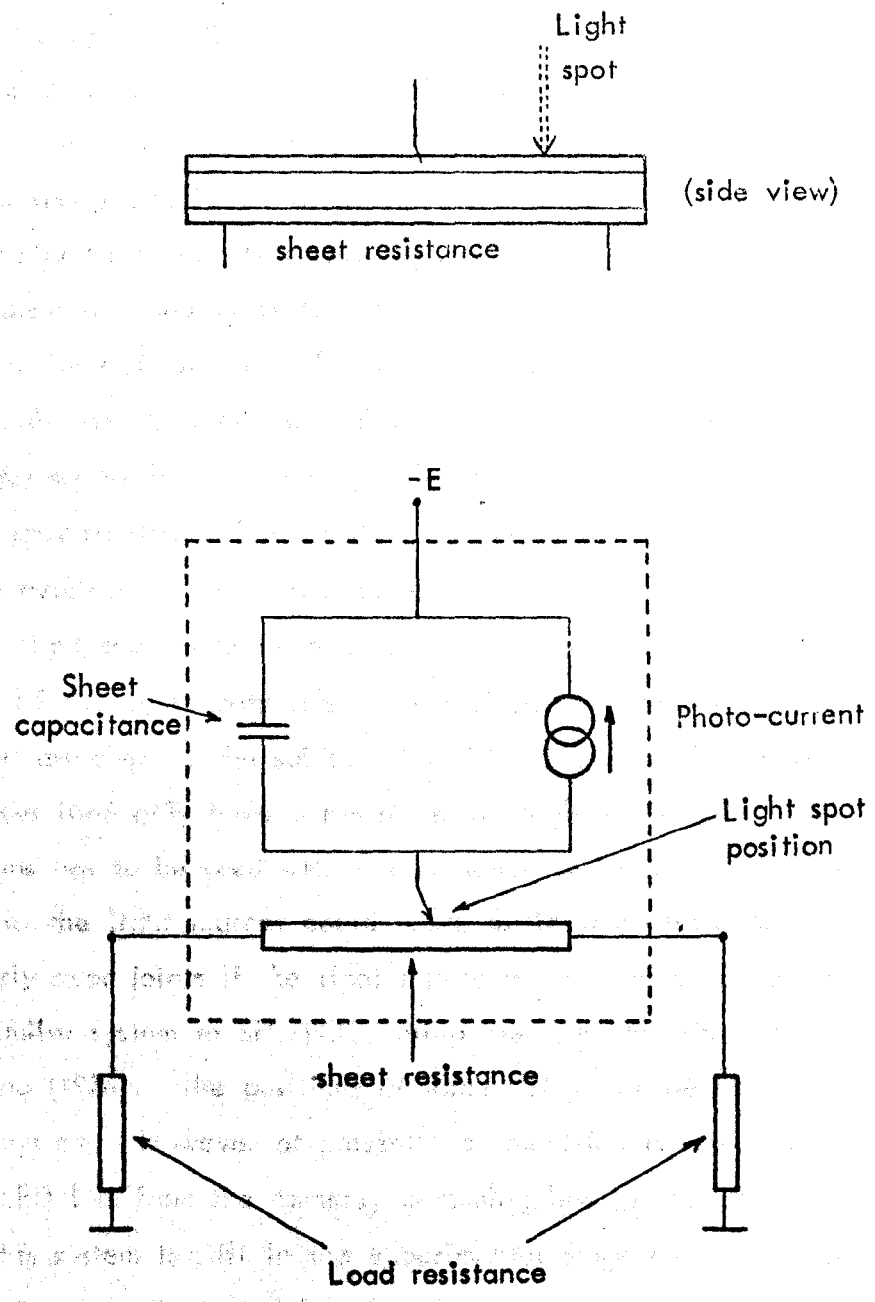


Figure 1.14. Equivalent electrical circuit for a single axis continuous light spot position sensor (From Woltring (1973)).

(Woltring (1973)), however currently available LED's cannot produce this sort of incident power at the ranges of interest in locomotion studies. The duration of the incident light pulse also affects the signal to noise ratio due to the time constant of the sensor, so that Laser LED's of high power but rather short duration (200 nS - 1 μ S) are not particularly suitable. However a resolution of better than 10^{-3} is claimed with currently available LED's over a range of 6 m within a cube of 3 m sides, at a sampling frequency of over 200 Hz for up to 30 LED's (Selcom (1975)). Three dimensional measurements can be obtained by using two sensors, and there is also the possibility of using extra sensors positioned so as to pick up light sources that may occasionally be obscured to the main sensor. The SELSPOT system has the advantage that output signals are obtained for individual markers and there are therefore no co-ordinate/marker identification problems; also the resolution and sampling frequency specifications of the system are good, provided LED's of sufficient power are available. The disadvantages of this type of system are the necessity for active light sources to be mounted on the body, the need for switching circuits, and the consequent requirement of power supply all of which would have to be attached to the subject if trailing leads are to be avoided. The light sources used only have a narrow solid angle of radiation and hence some kind of lens has to be used with a consequent reduction in radiant power. The mounting of the light sources could prove difficult on some anatomical landmarks (particularly over joints if the light source is strapped onto the subject).

A similar system to SELSPOT, using the same detector, has been reported by Woltring (1974). The positions of three LED's can be sampled at 300 Hz with this system. However at present the resolution is rather poor - 1 in 400 with the LED 1 m from the camera, becoming less as the LED is moved further away. This system is still in the experimental stage and it is hoped to improve the resolution by a factor of four for distances up to 3 m.

Another opto-electronic measurement system is being developed by Mitchelson (1974, 1975). This system, called CODA (Cartesian Optoelectronic Dynamic Anthropometer) uses an array of silicon photodetectors in front of which is placed an encoded optical mask, Figure 1.15. Cylindrical optics are used to focus point sources of light into a line image onto this array. The component

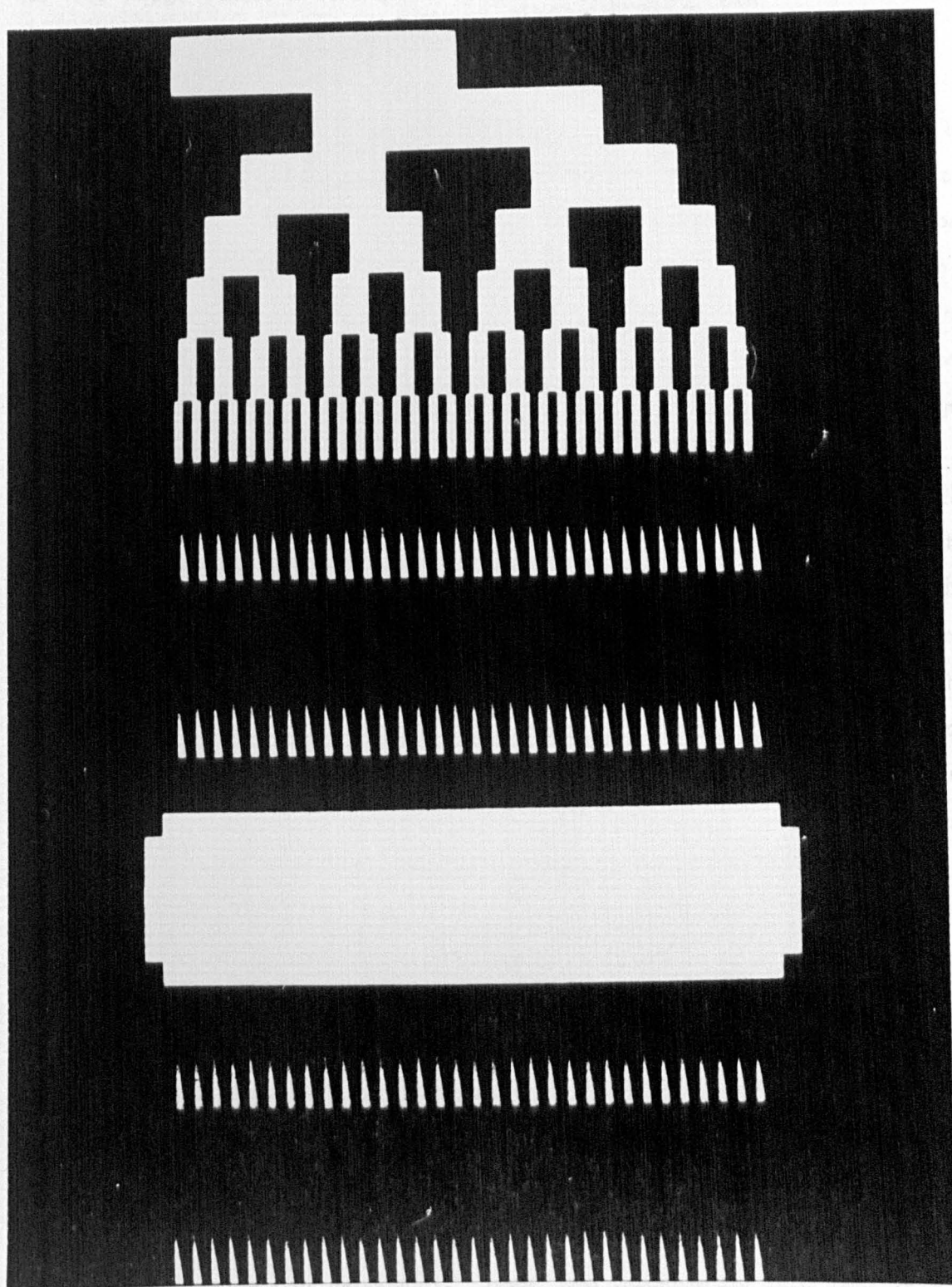


Figure 1.15. Optical mask used in "CODA" system. (From Mitchelson (1975)).

of movement of the point source that is at right angles to the orientation of the line image causes a corresponding shift of the line image across the focal plane. The first seven rows of detectors are behind the digitally encoded portion of the optical mask, and a direct digital readout of the position of the line image is thus obtained (7 bits). Further resolution of the position is obtained by using the analogue vernier part of the optical mask. The analogue vernier consists of 4 rows of 64 transparent wedges, the pattern of wedges being advanced by $1/4$ of the interval between wedges for each row. As the line image moves across the wedge the amount of light falling onto the photodetectors is linearly proportionate to the position of the image on the wedge. However, the width of the line image may be as much as $\frac{1}{2}$ the base width of a wedge so the linear relationship only holds good in the central half of the wedge. Due to the staggered arrangement of the 4 rows the line image can only fall in the linear part of a wedge on one row at a time. The system can select the row in which there is a linear response, which therefore provides 2 further bits of resolution (1 out of 4 rows). The analogue output of the selected row is then digitised and a further 6 bits of resolution is claimed. The seven bits from the digitally encoded portion of the mask together with the two bits from the row selection and the remaining 6 bits obtained from the analogue output gives a total of 15 bits resolution. One bit is lost in order to avoid ambiguous or erroneous matching of the outputs from the digital and analogue parts of the system. Two more bits are lost in the arithmetic processing necessary to derive parallax free three dimensional co-ordinates. A final output resolution for the instrument of 12 bits or 1 part in 4096 is the design specification. The broad transparent portion of the optical mask provides a reference signal which allows corrections to be made for fluctuations in the intensity of the light sources.

Three dimensional co-ordinates are obtained by using three cameras. One of the cameras is oriented so that it is sensitive only to the vertical components of displacement; the other two cameras are both oriented to be sensitive to the horizontal components of movement. These two cameras are separated by a known base length and therefore stereo-photography principles can be applied to derive the "depth" component of position. Corrections for the effects of parallax can also be made. As in the SELSPOT system LED's are used to

provide the light source, but due to the faster response of the sensors used it has been possible to use the higher power Laser LED's. Each light source is switched on in turn to give a light flash of approximately 200 nS duration, 90 μ S is then required to derive the co-ordinates of the light source; and so with a total of 10 light sources (markers) a sampling frequency or repetition rate of 1 KHz can be attained (although Mitchelson appears to be limiting the system to 8 sources).

Mitchelson states that the resolution of the system is limited by the signal to noise ratio at the sensors. With the laser light source 2 m from the camera this is stated to be 33 to 1; which gives an overall resolution of better than 1 in 4,000 (when the resolution of the digital part of the system is also considered). The signal level from the LED at the sensor follows the inverse square law so there is a corresponding decrease in resolution with increasing distance from the camera. Table 1.1 shows the effect of this on the resolving power of the system. The "width of overlap" columns give the amount by which the fields of view of the outside cameras overlap and hence the working dimensions of the system. With this system the subject would walk towards the cameras, so the minimum overlap required to ensure that lateral movements would be observed would be 0.75 m. If the recording of motion is started when the subject is 7 m from the camera then two and possibly three strides would be accommodated, and the resolution would vary from 10 mm to 0.5 mm. At the present stage of development of this system there is a systematic error of 1 part in 400 due to the imprecision of the vernier (wedge) part of the optical mask. This error is compounded when calculating the parallax free co-ordinates of the light source and Mitchelson (1974) quotes errors in horizontal and vertical co-ordinates of 1 part in 130, and of 1 part in 200 for the depth co-ordinate.

The overall accuracy of this system must also depend on the accuracy with which the constants of 1) base length between the outside cameras, 2) focal length and 3) width of the focal plane can be set up. These constants are used in the analogue computation of the parallax free co-ordinates and are represented by preset constant voltages or currents. Any inaccuracy in

Distance from camera	Resolution	Field of view of camera	Overlap for base = .5 m	Overlap for base = 1 m	Resolving power
m		m	m	m	mm
2.0	1:4000	1	0.5	0	0.25
2.5	1:2500	1.25	0.75	0.25	0.5
3.0	1:1800	1.5	1.0	0.5	0.85
3.5	1:1300	1.75	1.25	0.75	1.3
4.0	1:1000	2.0	1.5	1.0	2.0
5.0	1:640	2.5	2.0	1.5	4.0
6.0	1:450	3.0	2.5	2.0	7.0
7.0	1:330	3.5	3.0	2.5	10.0

Table 1.1.

setting up these constants will affect the overall accuracy. Precise alignment of the sensor with the optical system is required, and also the camera must be oriented precisely to the external measurement axis. Michelson suggests that improvements in the precision of the optical mask and more efficient diffusion of the light from the laser landmarks will allow the design specification of a resolution of 1 part in 4,000 to be reached.

These optoelectronic methods are mostly still at the development stage. If the design specification of some of them are attained then they will provide powerful measuring systems. However it seems most likely that the range over which movement can be monitored will be restricted by the power and angle of radiation of available light sources. To monitor the movement of more than one light source it is necessary to use active light sources which will obviously require a power supply. Switching of the light sources will require control cables to be attached to the subject, or a telemetry link.

1.3.3 Contacting Methods

Goniometers. The electrogoniometer as used by Karpovich (1959) is a very simple and cheap instrument; basically it consists of a precision potentiometer fixed to a support bracket with the spindle of the potentiometer fixed to another support bracket. Relative motion between the support brackets rotates the potentiometer spindle and hence varies the resistance in direct proportion to the angle between the brackets. By suitably attaching the brackets on either side of a joint relative motion can be measured. Johnston (1969) used goniometers to measure sagittal, coronal and transverse rotations about the hip joint; the same device was used by Kettelkamp (1970) to obtain similar measurements about the knee joint. The device consisted simply of a linkage in which the three rotations were individually transmitted to three goniometers; obviously, the measurements obtained were of the rotations of the linkage which is not the exact centre of the joint. Corrections were made for this but errors arising from movement of the goniometer assembly due to relative motion of the soft tissues with respect to the skeleton could not be accounted for.

Lamoreux (1971) designed an exoskeleton which was attached to the lower limb, Figure 1.16. The exoskeleton at the hip provided an external analogue

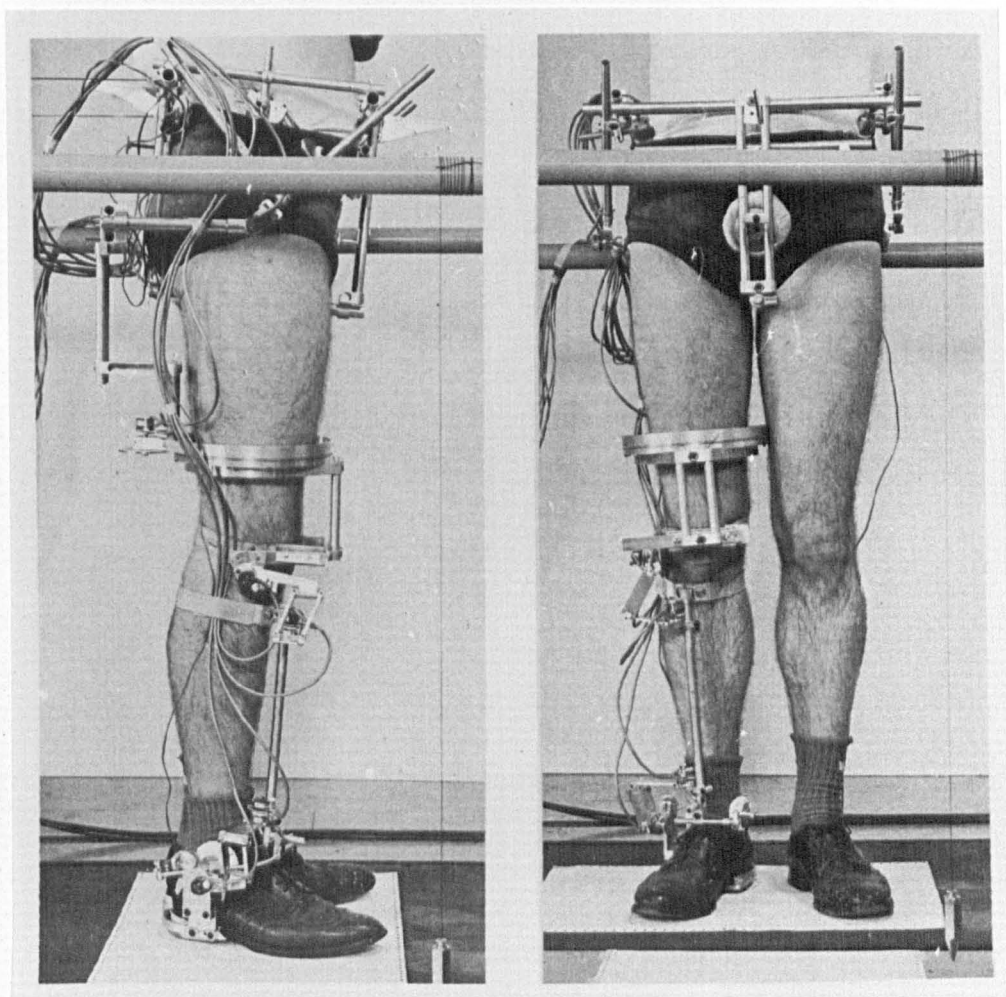


Figure 1.16. Front and side view of Lamoreux's "exoskeleton". (From Lamoreux (1971)).

of the hip joint which, after careful alignment, could have its effective centre at the centre of the hip joint. The three joints of this analogue were instrumented with potentiometers to measure the relative rotations. Parallelogram linkages were used to measure the relative rotations at the knee and ankle. These linkages will transmit two of the three components of an arbitrary rotation in three dimensions and absorb the third. Lamoreux states that precise alignment of these linkages is not necessary and that the axes of measurement are the axes of the exoskeletal joint between which the parallelogram linkages are attached. To measure the three components of rotation two linkages are required; with Lamoreux's system simultaneous measurement of the three components can be made at the ankle but not at the knee. Measurements obtained via the exoskeleton were made while the subject walked on a treadmill. The physical attachment of the exoskeleton (total weight approx. 6 lbs) and the use of the treadmill, must condition the subject's gait.

A potentiometer instrumented linkage system was used by Kinzel (1972a, 1972b) to measure the total motion between two body segments. His linkage had six degrees of freedom and he could therefore measure the three rotations and three translations which completely describe the motion at a joint. By making separate measurements of the joint surfaces Kinzel was able to study the relative motion between the joint surfaces. The end points of the linkage system were securely mounted on pins which had been inserted into the bone. This system was used to study the scapula-humerus joint in Alsatian dogs, walking on a treadmill. The measurements obtained were able to show the path by which the scapula moves over the humeral head, the percentage of the time spent by the scapula at different locations on the humeral head, and the apparent areas of contact. To apply the system to measurement of human joint motion would require a non-invasive method of attaching the linkage to the subject, and a non-invasive method of measuring the joint surfaces. Both of these requirements would limit the accuracy of measurement considerably.

Tachograph. This is another inexpensive form of instrumentation, which, in its simplest form, consists of a D.C. generator which is driven by a string

attached to the subject. The output voltage of the generator varies with the velocity of the attachment point of the string on the body. Drillis (1958) used the method to record the horizontal velocity of the trunk. Ganguli (1973) claimed that the system was suitable for use in a clinical situation. With his system the string was located in horizontal pulleys which were mounted on shafts extending between floor and ceiling, a bearing being provided at each end of the shaft. Four such pulleys were arranged at the corners of a rectangle with the string in a loop around them. The drive to the D.C. generator was taken from another pulley on one of the shafts. The string was attached to the side of the subject in the region of the iliac crest of the pelvis; the height of the string being adjusted by the position of the pulleys on the shafts. This arrangement would mean that the recorded velocities would be affected by transverse rotations of the pelvis, and would therefore not be a true measure of the motion of the centre of gravity of the subject. Ganguli evaluated the data by taking the ratio of the first period to the second period of the "centre of gravity" curve (the fundamental frequency of this curve being twice that of the gait cycle frequency). He claimed that this ratio would be independent of all factors except the symmetry of gait.

Molen (1972) used similar principles to measure the instantaneous velocity of the centre of gravity of the body. Magnetic tape prerecorded with pulses at a fixed frequency and constant tape speed was attached to the subject at front and rear at the level of the centre of gravity. The tape was then looped round guides and passed over a tape read head; vibration damping was incorporated into this path and the height of the tape read head was adjustable to suit the subject. As the subject moved the output frequency of the pre-recorded pulses was proportional to the instantaneous velocity of the tape passing over the read head. A frequency to D.C. converter was used to give a proportional output voltage.

These methods are inexpensive, but the information that they give is very limited. They may show, for instance, an asymmetry in the gait, but it is most unlikely that they will indicate the cause of the asymmetry.

Accelerometers. The technique of measuring acceleration directly with accelerometers was used during the University of California's fundamental studies of locomotion. (University of California (1947)). In the report by Eberhart (1951) on the experimental procedures used in these studies, the data provided by accelerometers was stated to be "inferior" to that obtained by using cine film techniques, but this was most likely due to the type of device available at the time. The vertical and fore- and - aft accelerations of the trunk together with the angular accelerations of the shank were measured directly using accelerometers by Gage (1964). Harmonic analyses were made of the resulting data and comparisons were made between normal and amputee gait. The degree of symmetry of gait in the normal was clearly shown by the dominance of the even harmonics; the odd harmonic values in the amputee gait being consistently higher, showing the more pronounced asymmetry of their gait. Gage stated that certain gait defects showed up as specific abnormalities in the harmonic analysis, although combinations of defects were difficult to interpret from the frequency spectra. No attempts to derive velocity and displacement data from the accelerometer recordings were reported by Gage. Accelerometers were used to verify the procedures by which accelerations were derived from displacement/time data by Hunt (1965). Measurements were made from accelerometers mounted on the shank of the subject and the results obtained showed general agreement with data derived from cine film recording.

Smidt (1971) followed up suggestions made by Gage (1964) and studied the effect of "induced" abnormalities - by immobilizing certain joints. He used three orthogonally mounted accelerometers placed on the body close to the centre of gravity. Data was recorded over 4-6 strides and foot switches were used to determine heel strike, foot flat, and toe off. Again, a Fourier analysis was performed on the acceleration curves of the fore-aft and vertical - the lateral accelerations were not particularly cyclic and hence not suitable for Fourier analysis. Smidt quantified the "smoothness" of the gait by taking the ratio of the sum of the even harmonic coefficients to the sum of the odd harmonic coefficients. A high harmonic ratio indicating a smooth acceleration curve. Smidt stated that the harmonic ratio could discriminate between the

"induced" abnormal gaits.

The total movement of the shank (with the one exception of the transverse rotation) was studied by Morris (1973) using five accelerometers mounted on a perspex platform. Morris suggests that the inferior results obtained by earlier investigations using accelerometers was due to the use of unsuitable transducers. Strain-gauge accelerometers which deform elastically due to inertial force were used by Morris and stated to be the most suitable type. The signals from the accelerometers were recorded on a portable tape recorder carried by the subject or passed by a lightweight cable to a fixed recorder. The recorded data was processed on a small digital computer which allowed the use of interactive programmes. The operator selected one cycle of the data by setting cursors on a visual display of the data to mark the beginning and end points. This portion of data was then filtered to remove drift and to set a lower frequency limit to the signal pass band. Further processing obtained angular velocity, direction cosine, translational acceleration, velocity and position data. The system developed by Morris has the advantage that it can be used outside of a specialist laboratory, with little discomfort to the subject who may wear normal clothing. However, the system has so far only been developed for use on the shank. Attaching accelerometers to the thigh, for instance, will be difficult because of tissue movement; the site chosen on the shank was relatively free of such problems.

The use of accelerometers always requires some kind of physical attachment to the subject, and some kind of link to a recording device. Studies have been limited to one or two segments, and no investigator has attempted to incorporate ground reaction into the analysis, as far as is known.

1.3.4. Other Techniques

Cine radiographic techniques have been suggested as a method of gait analysis (Eberhart (1951)). The equipment necessary is not only costly but it is also limited in its scope. The depth of field is restricted, and the width of the field of view is somewhat narrow. The only recorded use of such equipment for locomotion analysis is by Jenkins (1972); however, this study was of the

gait of chimpanzees. Recordings of the gait were made at 50 frames per second while the animal walked on a treadmill. The field of view was only 25.4 cm, which meant that the separate sequences had to be recorded for each joint. The data was quantified by measuring joint angles from the film, and the results were compared with the gait of man. The use of cine radiographic equipment is probably not worthwhile, because of the above mentioned restrictions and because manual reduction of the data is still required. It may, however, be useful to obtain data of the movement of marked positions on the skin with respect to anatomical skeletal landmarks.

A novel technique to measure the velocity of a point on the body was developed by Nadler (1958). A sound source (at 20 KHz) was attached to the point on the body to be measured and three microphones with their directional axes orthogonal were set up to receive the sound. Because of the Doppler effect the frequency of the sound received by the stationary microphones varied proportionally to the velocity of the sound source. This frequency variation was measured and hence the instantaneous velocity of the source would be described in three-dimensions by the signals from the microphones. More than one sound source could be used by choosing different source frequencies. The method has been used to study hand motion (Kattan (1969)), but has, so far, not been used for locomotion analysis. There are problems in using the technique because of reflections of the sound from the surroundings, and shielding of the sound by the body.

1.4 Television Systems

Several systems based on television, for the measurement of movement, have been independently developed. All of the systems use the same basic principle to obtain the spatial co-ordinates in one plane of a detectable point or area within the field of view of the camera. This principle is that one co-ordinate may be obtained by reference to the television raster line on which the point is detected and the other co-ordinate may be obtained from its position on this line. Sampling of the position of a detectable point in time is obtained by the sequential scanning action of the television camera.

Furnee (1967) reported the development of a television-computer system to measure arm movements. A detailed description of the method may be found in Steilberg (1968), a review will be given here. The television standards used were the 625 line, 50 fields/sec British system (for more information on television standards see appendix A1). A 3.995 MHz crystal controlled clock was used to count intervals along a line giving, in $64 \mu\text{S}$, 256 intervals. $8 \mu\text{S}$ of this was taken up by the line flyback period which meant that the active line time was divided into 226 intervals (the x-co-ordinate counter). For the y-co-ordinate an 8 bit counter was used which allowed 256 scanning lines to be counted, the remaining scanning lines were blanked off. Small electric lamps were used to pinpoint landmarks on the arm, the normal scene illumination and the camera settings were adjusted so that the light from the lamps gave the highest level in the video signal. A simple threshold detector was used which was set to give a constant duration output pulse whenever the video signal reached the level caused by the lamps. On receipt of this pulse the contents of the x and y counters were instantaneously stored in a buffer register. As the light from the lamp covered more than one TV line it was arranged that only the detector pulse received on the first line would cause readout of the counter contents. In the version of the system reported by Steilberg (1968) the co-ordinates contained in the buffer register were immediately transferred to the computer memory on a cycle stealing basis, during this transfer period no further marker co-ordinates could be registered. However as the system used a direct memory access method to transfer the data this period would have been of the order of microseconds (i.e. very much less than the duration of one line - probably about $4 \mu\text{S}$). Later versions of the system incorporated a 16 word buffer memory for each co-ordinate, and the contents of this memory were transferred to the computer or to a digital tape recorder during the field blanking period (Ingen Schenau (1973)). The sequential scanning action of a television camera means that the positions of the lamps are not all sampled at the same instant. To provide simultaneous sampling of the lamp positions the Dutch group introduced a synchronous shutter into the system, Ingen Schenau (1973) and Stokrom (1973).

This shutter rotated 50 times/sec and a window in the shutter exposed the field of view to the television camera for 2 mS; synchronising circuits ensured that the exposure was made during the field blanking period. The light pattern stored on the camera signal plate was then scanned in the normal way.

Methods for identifying the lamps from the co-ordinates are discussed in Ingen Schenau (1973); also various computer programs to filter the resulting data are presented. Non-linearities due to the camera are corrected by formula, found experimentally - the linearity of y was shown to be independent of the linearity of x . Scaling of the data was done by reference to the co-ordinates (corrected for non-linearity) of 3 points arranged in a triangle. The number of lamps that could be used to mark anatomical landmarks was limited to 5 (in the buffer memory version some locations must be reserved for pick up of spurious signals). So far only one camera has been used, so only movements in two dimensions have been studied. Most of the work done by the Dutch group has been in the study of arm movements, Stokrom (1973), Ingen Schenau (1973). Some locomotion studies have been made, but the results have not been reported, other than on the locomotion of cats, Furnee (1974). No attempt has so far been made to incorporate measurements of external forces.

A television system to measure three dimensional co-ordinates was developed by Waas (1969). Two television cameras were used to provide two planar views of electric lamps placed on anatomical landmarks of the subject. Up to 4 lamps could be used and it was necessary to place them in positions where they would always be in view of the two orthogonally positioned cameras. Digital counters were used to provide the co-ordinates, similar to the Dutch system. On detection of a lamp signal in the video the contents of the co-ordinate counters were read out and stored on magnetic tape. This transfer operation lasted for 9 full scan lines (570 μ S) and no further detection of marker signals could take place during this time. The system was designed so that the second camera only gave one co-ordinate, the Z co-ordinate. Obviously the time required to transfer co-ordinates to magnetic tape placed a severe restriction on the system. Markers had to be arranged so that at

no time in the gait cycle did they come within 9 scan lines of each other. It is understood that this system is no longer in use.

A computer interface for television has been developed by Dinn (1970). This is a general purpose digital system which performs an analogue to digital conversion of the television video signal. The standard American television system is used (525 lines, 60 fields/sec.) and the sampled area of each field can be varied from a single point up to a 256×256 window, with the height and width of the window independently variable. The system could be set to sample every television line or every 2nd, 3rd, 5th, 8th or 12th line, with corresponding sampling intervals along the line. The amplitude quantisation of the video signal conversion could be set to 1, 2, 3, 4 or 5 bits (i.e. 2-32 levels of brightness). The interface could also be set up to sample every television field, every second field etc. up to every 64th field. The word length of the resulting digital data was also variable and could be set to 12, 16, 20, 24 or 32 bits, which gives a wide range of compatibility for different computers. The object of this system was to provide a general purpose facility which could convert television images into a digital equivalent which could then be processed by a digital computer.

Extensive use of this interface has been made in studies of human locomotion at Shriners Hospital for crippled children in Winnipeg. The experimental set up is described in Winter (1972), and shown in Figure 1.17. The interface is set to give one bit amplitude resolution (i.e. either bright or dark) and a sampling matrix of 96 points by 96 points for each field. Comparatively large reflective markers are used to indicate various landmarks on the limb. Winter shows that by using such large markers a more accurate estimation of the spatial co-ordinates of the centre of the marker can be made - a minimum of 10 sample points lie within the marker area of the video signal, and a spatial resolution of 1 mm is quoted. In order to use the comparatively coarse sample matrix it is necessary that the field of view of the camera only covers a small area. This restriction in turn makes it necessary to "track" the subject with the camera. An operator pushes the trolley mounted camera along a rail keeping the walking subject in view of the camera at all times. The spatial reference of the

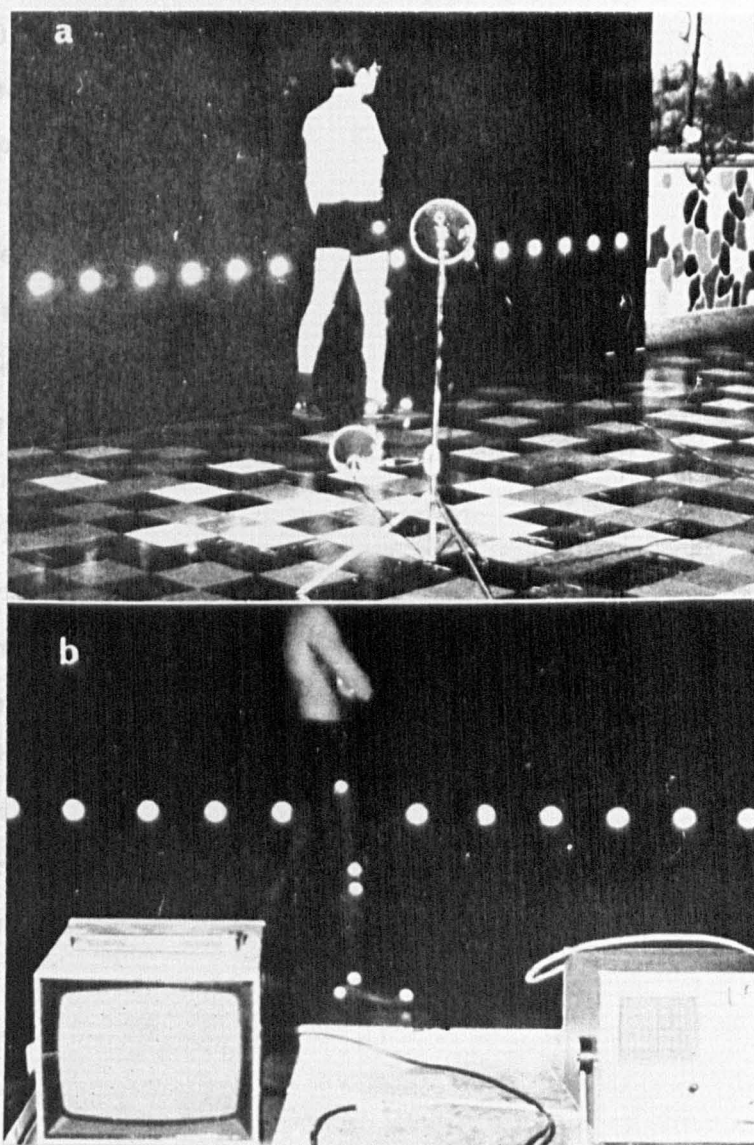


Figure 1.17. Experimental set up of the television system in use in Winnipeg. (From Winter (1972)).

instantaneous camera position is obtained from large markers placed at known intervals in the background, one such marker always being within sight of the camera. These background markers are larger than the body markers and so can be identified in the resulting digital data. Even the restricted sample matrix of 96×96 for each television field results in a very high data rate of over 20,000 words/sec (assuming that the one bit analogue conversion is packed into 24 bit words for transfer to the computer). Consequently fairly large computers have to be used, in the Winnipeg case a CDC 1700. The television signal is recorded on a video tape recorder for later digital conversion, after conversion computer programs calculate the absolute co-ordinates of the centre of the body markers with reference to the background markers. Corrections being made for the parallax error introduced by the position of the background markers being some distance behind the plane of walking.

A kinematic study of normal locomotion using this system is presented in Winter (1974). From co-ordinate data for 3 to 4 strides fundamental kinematic information was calculated including - x, y trajectory plots, plots of vertical co-ordinate against time, velocities of markers, acceleration, joint angles and angular velocities and accelerations. The comparative ease with which the data for these calculations was collected demonstrates the power of the Winnipeg system. However at this stage only two-dimensional measurements can be made and the problems of introducing an additional camera to the system have yet to be overcome. No rotating shutter is used in the system. The reason for this being that in normal locomotion the vertical position of the markers only varies over a small part of the television scan and therefore the time between samples is virtually constant. Of course there is a lag between the sample instant for a marker at the top of the scan and a marker at the bottom of the scan; but this could be corrected as the time relationship between the two positions is fixed by the scanning rate, which is known.

On line use of a PDP 11/10 mini-computer was made by Cheng (1974) to obtain co-ordinates of markers from a television camera. This system is quite similar, in some ways, to the Dutch system. 8 bit counters are used for the horizontal and vertical co-ordinates. The horizontal counter is clocked

at a frequency of 4.55 MHz, and reset by the line blanking pulses (using the American standard television system), which gives 241 horizontal intervals. The vertical counter is clocked by the line blanking pulses and reset by field blanking pulses, giving a count of up to 246. A simple threshold circuit is used to detect the presence of a marker signal in the video (either reflective markers or light sources are used), and on receipt of a marker pulse the contents of the horizontal and vertical counter are stored in a buffer register. At the same time a program detectable flag is set so that the contents of the buffer may be transferred under program control to the computer memory. On transfer the computer checks the data to see if it is "noise" (caused by the interface at the beginning of each field). If the data is not "noise" then it is compared with previous data to see if it is caused by a marker being detected on several lines. If the data is a new set of co-ordinates then it is stored for later output onto paper tape. Another program detectable flag indicates the end of a television field. During this programmed transfer and evaluation the interface is prevented from detecting further markers. The computer program written to process the interface data would, in the worst case, inhibit further co-ordinate generation for 117 μ S (best case time would be 74 μ S). This limits the system to detecting a maximum of one marker on any line, which effectively prevents markers being placed at the same vertical position on the body (such as markers on the pelvis for the frontal view). This could be improved, by resetting the "ready" flag immediately after the interface data has been stored, to a worst case time of 34 μ S (best case 25 μ S). However this is still an unacceptable limitation (greater than half of the television line would have been scanned in this time). No corrections are made for non linearity of the camera system and, because the system only operates in two-dimensions, corrections cannot be made for parallax errors.

All of the systems reported, and still in use, can collect data for two-dimensional movements. At present none of the systems are operating in three-dimensions, and only the Winnipeg system incorporates other measuring facilities (EMG data). The Winnipeg system requires that the camera is moved with the subject, but this does give fairly high resolution. However a fairly

large computer is required to derive the basic data, and it is unlikely that results could be viewed in the laboratory within a short time of the test. The results from Winnipeg have indicated the feasibility of using television to obtain useful displacement/time data. The Dutch system and that of Cheng have very much lower data rates but they do not make full use of the available resolution of a television camera; and the methods of data transfer to bulk storage limit the marker configuration that can be used.

CHAPTER 2

POSITION DETECTION OF THE BODY SEGMENTS

IN SPACE USING TELEVISION

- 2.1 Introduction
- 2.2 Detection of the Segment
- 2.3 Markers
 - 2.3.1 Response of Retro-Reflective Tape
 - 2.3.2 Size of Marker
 - 2.3.3 Marker Shape
 - 2.3.4 Marker Discrimination

2.1 Introduction

The television camera is a sequential scanning device which converts a light pattern focussed onto a target plate into an analogue electrical signal the amplitude of which is proportional to the amount of light falling on the target. The time co-ordinate of this waveform is related to position on the target. With the British 625 line system the target is scanned from top to bottom by 292 lines every 20 mS; as a line is scanned the charge pattern on the target, corresponding to the light pattern, is substantially removed and a fresh charge pattern may be built up. The charge pattern will be retained on the target for some time until it is scanned. Unless some means of controlling the exposure of the camera target is used the trajectory, between samples, of a movement will be stored on the target (smear effect). This exposure may be controlled by using a rotating synchronous shutter (Ingen Schenau (1973)); although it is quite possible to compute the average position of a moving point between sample instants from the trajectory and dispense with the need for exposure control. The resolution of the television camera is 1 part in 292 in the vertical scan and 1 part in 1000 in the horizontal scan. The spatial resolution is, therefore, 0.34% and 0.1%. The sampling frequency of British television systems is 50 Hz; which, according to Winter's studies (Winter (1974)), should be sufficient for locomotion studies. The basic principles and parameters of television are further discussed in Appendix A1.

2.2 Detection of the Segment

For the purposes of locomotion analysis it is necessary to find the spatial co-ordinates of defined points on the skeleton. This requirement precludes the possibility of detecting the outline of a limb and then deriving the co-ordinates of that part of the skeleton. The outline of the limb has a certain degree of mobility with respect to the skeleton; also it would have to be assumed that the bone was placed centrally in the limb tissues so that rotation of the limb had no effect on the relative position of the bone and outline as the television camera is a two dimensional detector. A further objection to this method is the high data rate that would be required to define the outline. With the television camera field of view concentrated on, say, one lower limb a data

rate in excess of 30,000 words/sec. would be required. It would not be possible to measure rotations of the limb and there would be problems in detecting the outline when other body parts formed the background. Although the method is attractive in that it would require no attachments of any sort on the subject, it is impractical for the reasons outlined above. Methods of marking anatomical landmarks were, therefore, considered.

2.3 Markers

Anatomical landmarks may be indicated with active or passive light sources. Light emitting diodes (LED's) have been used as light sources in various opto-electronic systems (Selcom (1975), Mitchelson (1974), Cheng (1974)). With this type of source it is necessary to use a diffuser over the source in order to obtain radiation of light in as wide a solid angle as possible, because the LED normally has its maximum intensity of radiation in one main direction. Without a diffuser it would be necessary for the source to be always pointing at the camera, clearly this condition could not be met when marking a point on the limb, due to rotations. Using a diffuser reduces the already restricted optical power of the LED. Peak power can only be obtained when the LED is pulsed - up to a point the shorter the duration of the pulse, and the lower the repetition rate (duty cycle), the higher the optical power that can be obtained. To be able to detect short duration light pulses a camera target having a fast response would have to be used, which is costly. The highest power, currently available, LED's emit light in the Infrared region; this has the advantage that the light is not visible and will, therefore, not distract the subject. Also ambient illumination which does not emit in the infrared region can be arranged (Fluorescent lighting for example,) and optical filters may be used to ensure that peak signals from the camera target are only caused by infrared sources.

Incandescent light bulbs can provide much higher intensity levels, with a much greater solid angle of radiation than LED's; but they tend to be bulkier and, of course, the light is visible and may distract the subject. All active light sources require a power supply. This may be provided by small

individual batteries for each source, or a central power supply carried by the subject. If the higher power of LED's, obtained by pulsing them, is to be used then a pulsing circuit and a means of synchronisation with the television scan would be required. Inevitably, with active light sources, there will be connecting leads and also some reasonably firm method of fixing the source to the skin would be required.

Passive markers require no power supply or connecting leads, and are easily attached to the skin, with no restriction of the subject's movements. Several materials were considered from which to form markers, these included polished metallic surfaces, white paper, retro-reflective tape and coloured versions of these. The materials were compared by placing 5 mm diameter samples of each onto a plywood board (which effectively simulated skin tones) and observing the level of the video signal caused by each under the same lighting conditions. Retro-reflective tape gave by far the best response and was selected as the most suitable marker material.

2.3.1 Response of Retro-Reflective Tape

The retro-reflective tape used was 3 M's "Scotchlite" (see Appendix A4 for characteristics). This material is a plastic sheeting containing extremely small spherical glass lenses which are uniformly bonded at their equators. The optical glass lenses function as microscopic spherical mirrors which focus and return (retro-reflect) incoming light rays directly back to the light source. The sheeting has a pressure sensitive adhesive on the reverse side which is covered with a removable paper liner. To obtain the maximum response from this material, in the television video signal, the lighting must be positioned as close as possible to the camera lens.

With two Malham SE 23 lamps fitted with tungsten halogen 500W bulbs, one on each side of the camera lens, and a camera field of view of 2.5 m the following results were obtained:- 1) the video level of the marker was 90% higher than the level of skin tones 2) the level of signal was within 5% of its peak over the entire field of view 3) rotations of the marker of up to 30° resulted in a change of video level of no more than 5%. These results were

obtained using a marker of 5 mm diameter and a television camera with a plumbicon tube. Normal room lighting was left on during these tests as it was considered undesirable to use markers which would require low levels of background illumination.

On the basis of these results it was decided that passive markers of retro-reflective tape would be used instead of active light sources, thereby eliminating the need for power supplies and connecting wires on the subject.

2.3.2 Size of Marker

The size of marker will depend on the type of system evolved. However the minimum size of marker is defined by the television camera parameters and the dimensions of the field of view. To be sure that the marker is detected in the video signal it must be large enough to cover at least two scan lines sufficiently to provide a detectable signal on each. The diameter, D , of a marker to meet these requirements is given by:-

$$D = \sqrt{(W.H_r)^2 + (H.V_r)^2}$$

where H_r = horizontal resolution of camera

V_r = vertical resolution of camera

W = width of field of view covered by horizontal scan

H = height of field of view covered by vertical scan.

For a television camera with a horizontal resolution of 1 in 1,000, and a vertical resolution of 1 in 292 covering a field of view of 2.5 m x 1.8 m a marker diameter of 7 mm would be required. A smaller marker could still be detected, although it would not give as high a signal level.

2.3.3 Marker Shape

The basic shape of a marker which is used to indicate a point on the body should be circular, so that permitted changes in orientation of the marker with respect to the camera axes do not change its effective shape. If it is desired to detect the same marker at two cameras whose axes are at right angles to each other (as in the case for 3-dimensional measurements) then the

marker should be spherical.

2.3.4 Marker Discrimination

The response obtained from retro-reflective tape is so good that all that is required to detect a marker signal in the video is a simple threshold detector. However an alternative method was considered which could be worth using in a "noisy" situation. This method uses patterned markers and a hardware pattern recognition system (Jarrett (1973)). A very simple pattern recognition system for the marker of Figure 2.1a is shown in Figure 2.1c. When the video waveform, shown in Figure 2.1b, reaches a lower threshold level an enabling pulse of duration T sec is triggered off, if at any time during this period the upper threshold limit is crossed the "marker detected" pulse is generated, as shown in the timing diagram of Figure 2.1d. This idea can be extended to more complicated patterns, which would reduce the risk of a pattern being generated by noise and also would eliminate the "smearing" effect of the television camera. An example of this is shown in Figure 2.2. This design of marker would eliminate the smear effect because the pattern would only be picked up at the end of the marker trajectory during each sample period.

In the laboratory conditions used it was not found necessary to resort to pattern recognition techniques for marker discrimination. A simple threshold detector as shown in Figure 2.3 was used and found to be reliable. A more sophisticated threshold detector which automatically adjusted its comparison threshold to be a certain proportion of the peak signal received was developed from a basic design by Texas Instruments (1974) by B. Andrews. The basic principle of operation for this detector is shown in Figure 2.4. The advantage of this type of threshold detector is that any variation in signal level from the marker is automatically compensated for. The circuit for this detector is shown in Figure 2.5.

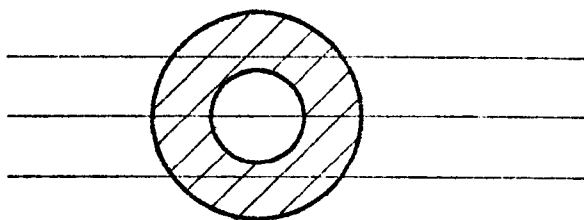


Figure 2.1a Patterned marker.

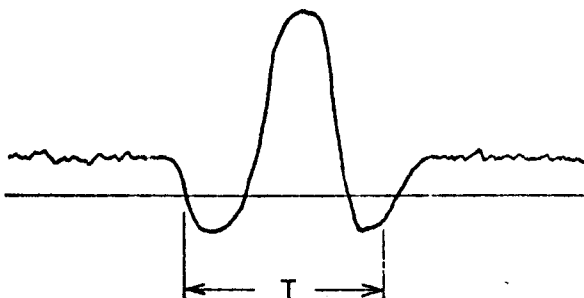


Figure 2.1b. Video signal produced by marker.

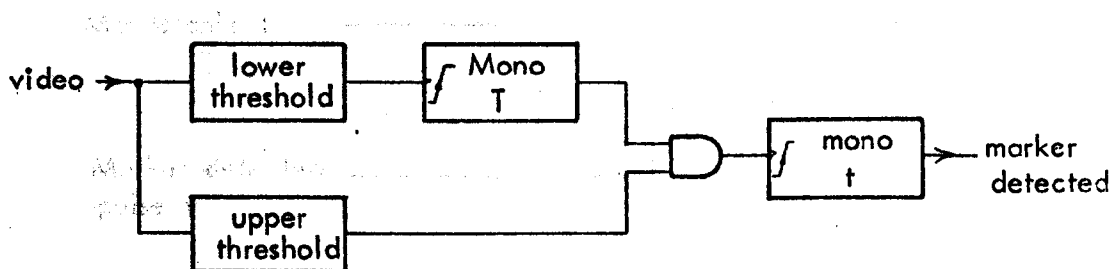


Figure 2.1c. Hardware pattern recognition system for the marker shown above.

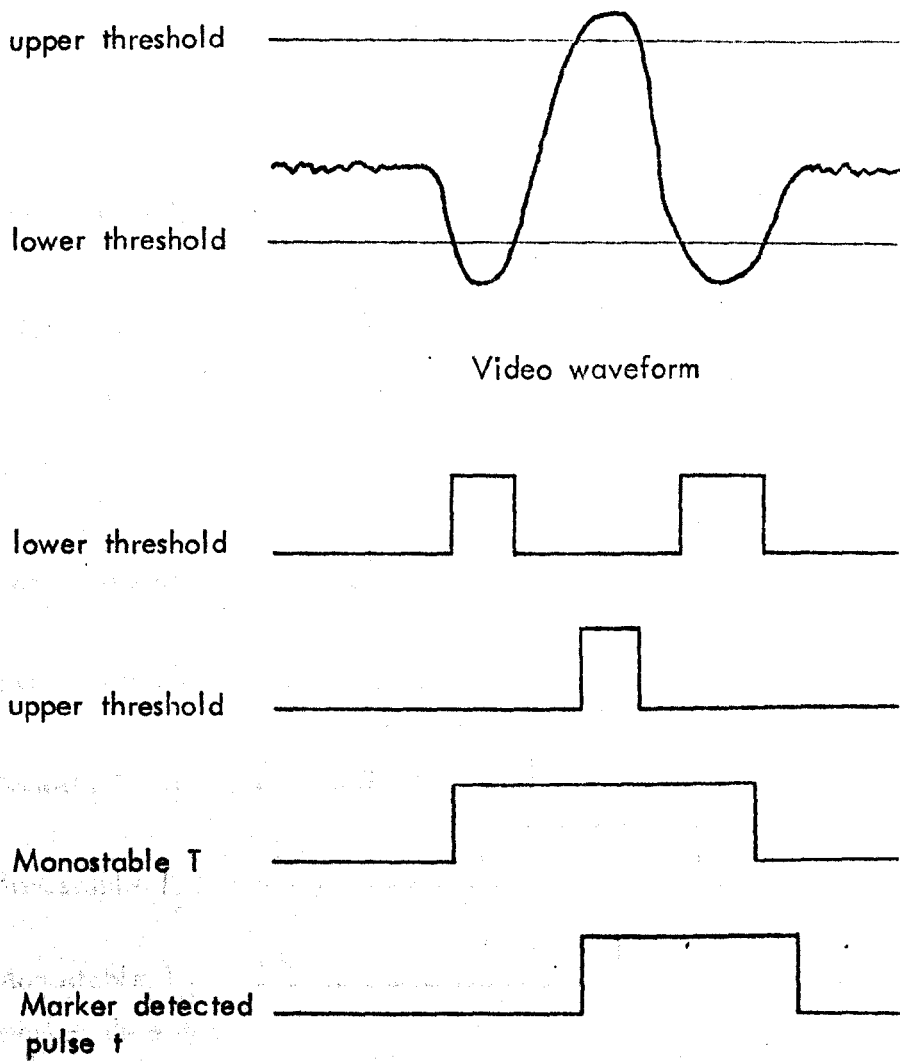


Figure 2.1d Video waveform of patterned marker and timing diagram for the logic of Figure 2.1c.

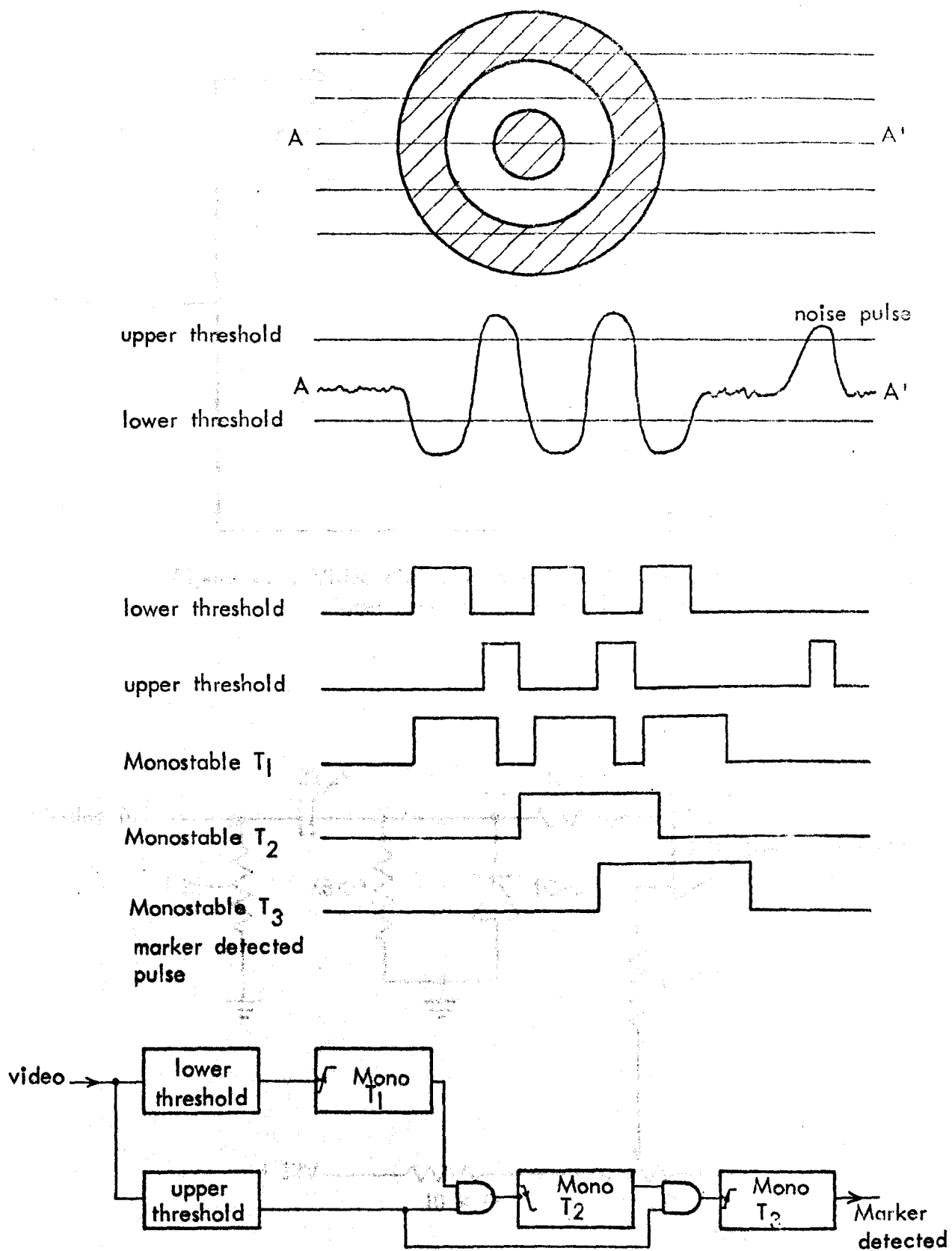


Figure 2.2. Pattern recognition system to eliminate "smear effect" of the television camera. The patterned marker and its video waveform are shown together with the decoding logic and a timing diagram.

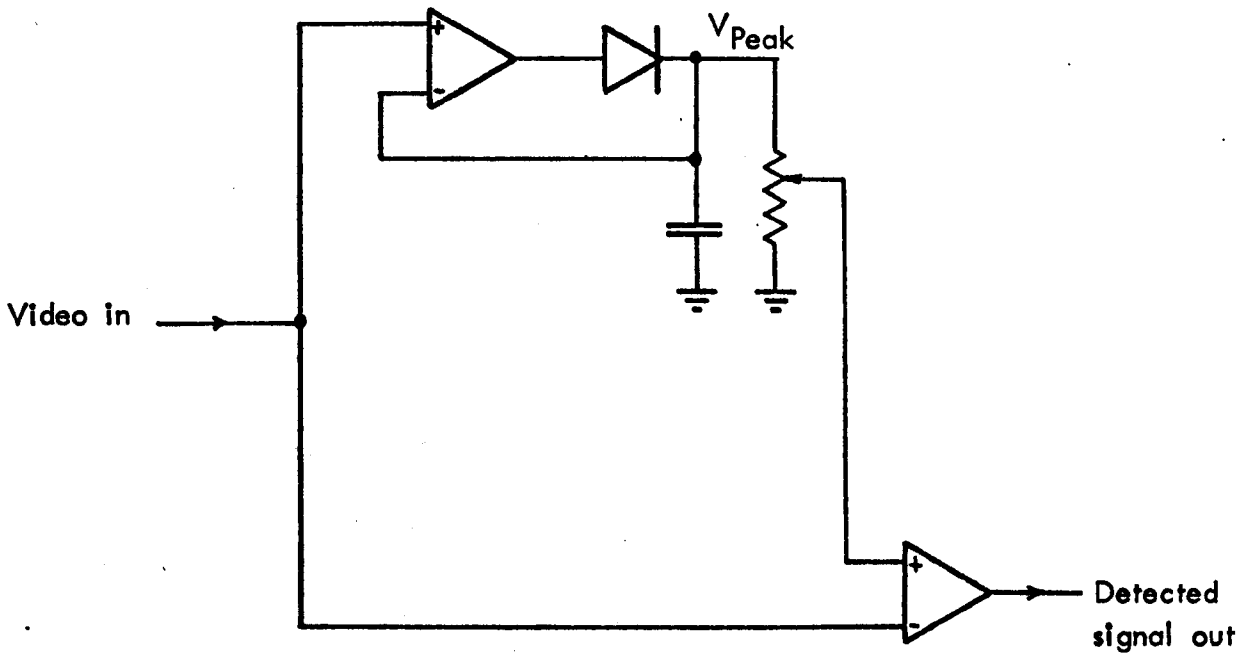


Figure 2.4. Video digitising circuit with automatic trigger level (From Texas Instruments (1974)).

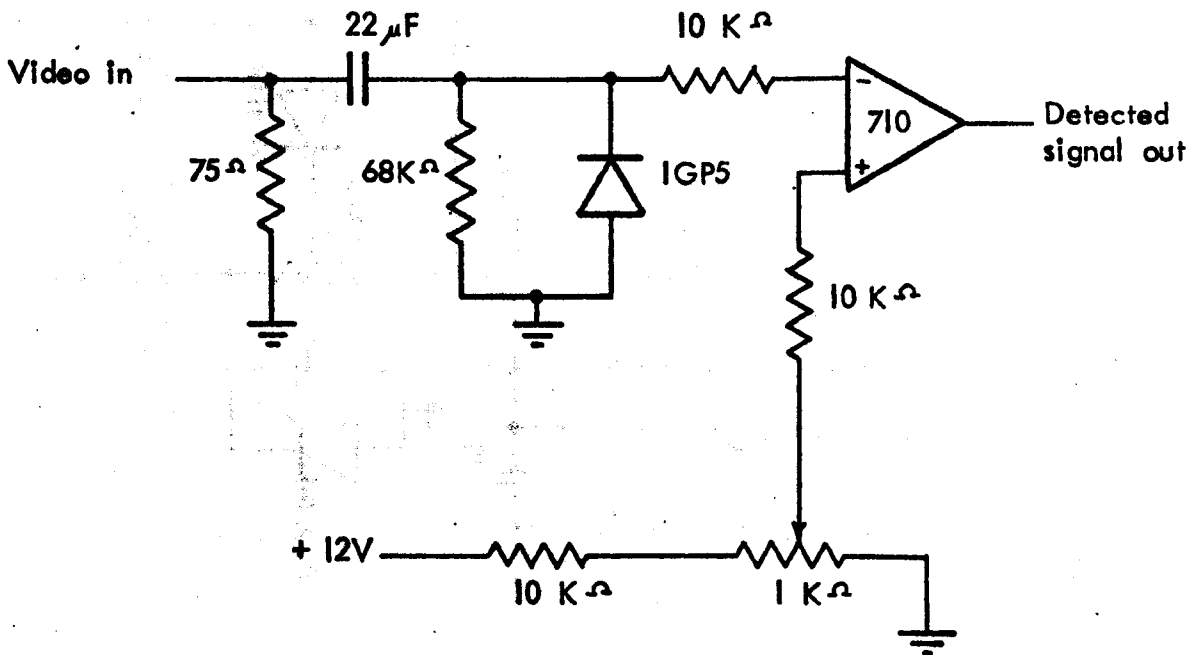


Figure 2.3. Threshold detector circuit used for marker detection.

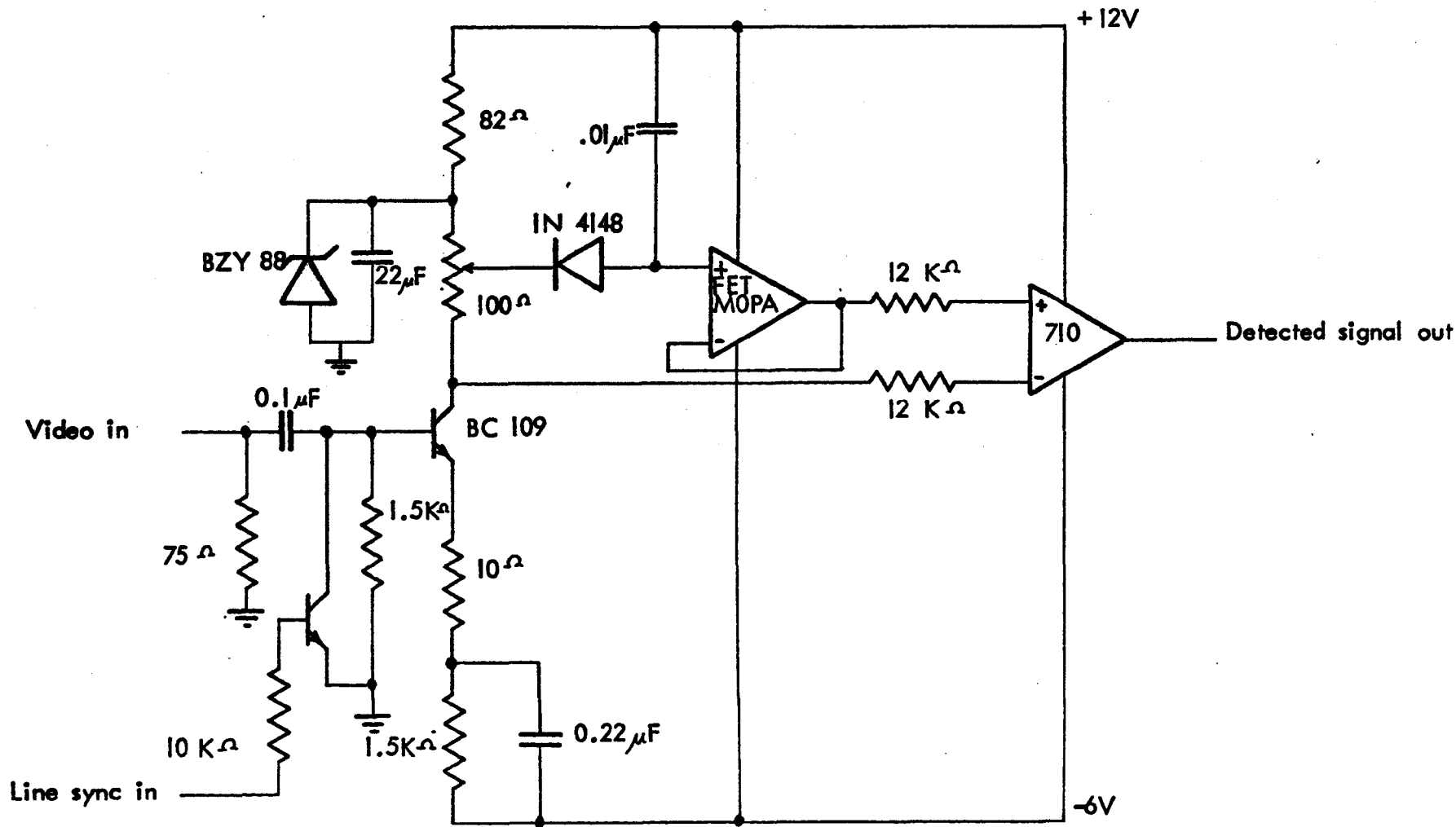


Figure 2.5. Circuit for the scheme shown in Figure 2.4 (developed by B. Andrews (1976)).

CHAPTER 3
THE INTERFACE

- 3.1 Introduction
- 3.2 Analogue System
- 3.3 Digital System
 - 3.3.1 Data Transfer
 - 3.3.2 Co-ordinate Generator (CG)
 - 3.3.3 Sync Generator and Simulator (SGS)
 - 3.3.4 Address Register (AR)
 - 3.3.5 Calibration (CAL)
 - 3.3.6 Computer Instructions (INS1 and INS2)

3.1 Introduction

The currently functioning television/computer systems for locomotion analysis are all limited in several respects. This interface was designed to overcome most of these limitations and provide a certain degree of flexibility in its mode of operation. The acquisition of kinematic data is but one of the requirements for locomotion analysis. In a few cases it may be the only requirement, however a complete analysis would include measurements of other parameters, such as external forces.

The requirements to be met by the interface were as follows:- 1) to be capable of measurement in three dimensions; 2) the ability to provide the co-ordinates of more than one marker on any single television scan line; 3) that the number of co-ordinates collected during one television field is limited by the computer and other considerations, not by the interface; 4) to allow extension of the system without loss of performance; 5) to provide a system of calibration; 6) to allow simultaneous and synchronous operation with other data acquisition systems; 7) to be simple to operate and maintain.

Any system which uses a small computer, such as a PDP 12, must have a restricted data rate. The method used by Winter (1972), in which the whole video signal is converted into a digital equivalent and stored, is not practicable. However it is not necessary to restrict the data rate so much that only a very limited number of co-ordinates can be collected in each television field. In the following systems the data rate is restricted by only generating co-ordinates when a marker signal is detected in the video.

Frequent reference will be made in this section to the basic terms and parameters of television systems, further explanation of these will be found in Appendix A1. Similarly where reference is made to the PDP 12 computer further details will be found in Appendix A2.

3.2 Analogue System

Initially an analogue method of interfacing the television cameras to the computer was considered (Jarrett (1973)). With this system detection of a marker pulse in the video signal caused the computer to sample two ramp

voltages, the ramps being initiated by the line and field synchronising pulses of the television scan. The voltages sampled by the computer corresponded to the co-ordinates of the marker and were digitised and stored. A three dimensional system was proposed in which the video signal from the second camera was mixed with that from the first. The fields of view of the two cameras were arranged so that the side view camera observed the lower limb in the bottom half of its scan, and the front view camera (turned on its side) observed the lower limb in the top half of its scan. The relevant parts of the video signals containing co-ordinate information were then mixed to provide one composite video signal as shown in Figure 3.1. A block diagram of this system is shown in Figure 3.2, and it can be seen that this was a very simple method which required only three inputs to the PDP 12.

The advantages of this system were that it was extremely simple and could probably be used on different computers with little or no modification, and that it was possible to record three dimensional tests on a single video channel, allowing off line experiments to be conducted. The disadvantages far outweighed these merits in that the co-ordinates of only one marker on each television line could be acquired; the resolution was very limited; the system could not be expanded to cover several strides or the whole body without a further loss in resolution; ramp generators of high stability and linearity were required; and the potential data acquisition capabilities of the PDP 12 were wasted. Some of these limitations could undoubtedly have been overcome (using several sample and hold circuits, for instance, to immediately store the value of ramp voltages on detection of marker pulses), but investigations into a digital system (Jarrett (1973)) showed that it was possible to provide a much more powerful and flexible system in this way.

3.3 Digital System

The design of a digital system to meet the requirements listed at the beginning of this chapter requires that the performance capabilities of both television cameras and the computer are used to the full. One crucially important aspect is the means of data transfer between interface and computer.

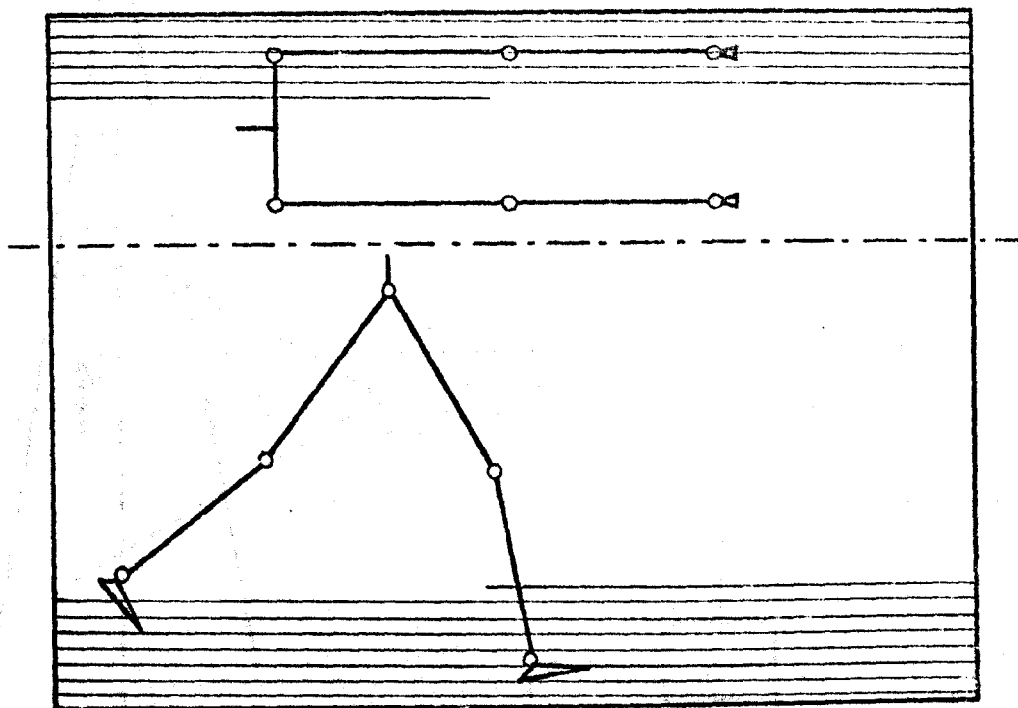


Figure 3.1. Division of television field for 3-D analogue system. Two camera video outputs are mixed as shown.

Other aspects that must be considered are the use of more than one camera to obtain three dimensional data, future extension of the system, simultaneous operation with other systems, control of the interface, calibration, and maintenance. By taking all these aspects into consideration at the design stage it should be possible to arrive at a near optimum solution with the minimum of compromise.

3.3.1 Data Transfer

There are two basic methods by which digital data can be transferred to the PDP 12. The first method, "programmed data transfer", requires that a flag is set in the interface when there is data ready to be transferred, a computer program senses this condition and transfers the data word from the interface to the Accumulator. The program must then store the data in memory, decide if any more data can be transferred, and clear the flag. This process is illustrated in Figure 3.3 with the time taken for each step shown in brackets, a total time of 19.05 μ S being required to transfer each word. The operation could be speeded up slightly if a block of data was transferred each time, even so it would still take about 15 μ S per word.

The second method uses the direct memory access facility of the PDP 12 - "Data Break". With this method data transfers to the computer are made under the control of the interface. When the interface is ready to transfer data a "break request" is made, the computer completes the current cycle and then enters the "break state". Once in the "break state" the interface has complete control and may transfer data from or to memory until the "break request" is cleared. Two types of data break are available - single cycle and three cycle. These modes are explained in detail in Appendix A2, but their basic characteristics will be stated here. Three cycle data break requires 4.8 μ S to transfer each word; the address in memory to which the data is to be transferred, and a count of the number of data words is provided by two sequential locations in memory (current address and word count registers). These locations are set to some initial value by the computer program, the interface simply provides the address (hard wired) of the first of these locations together

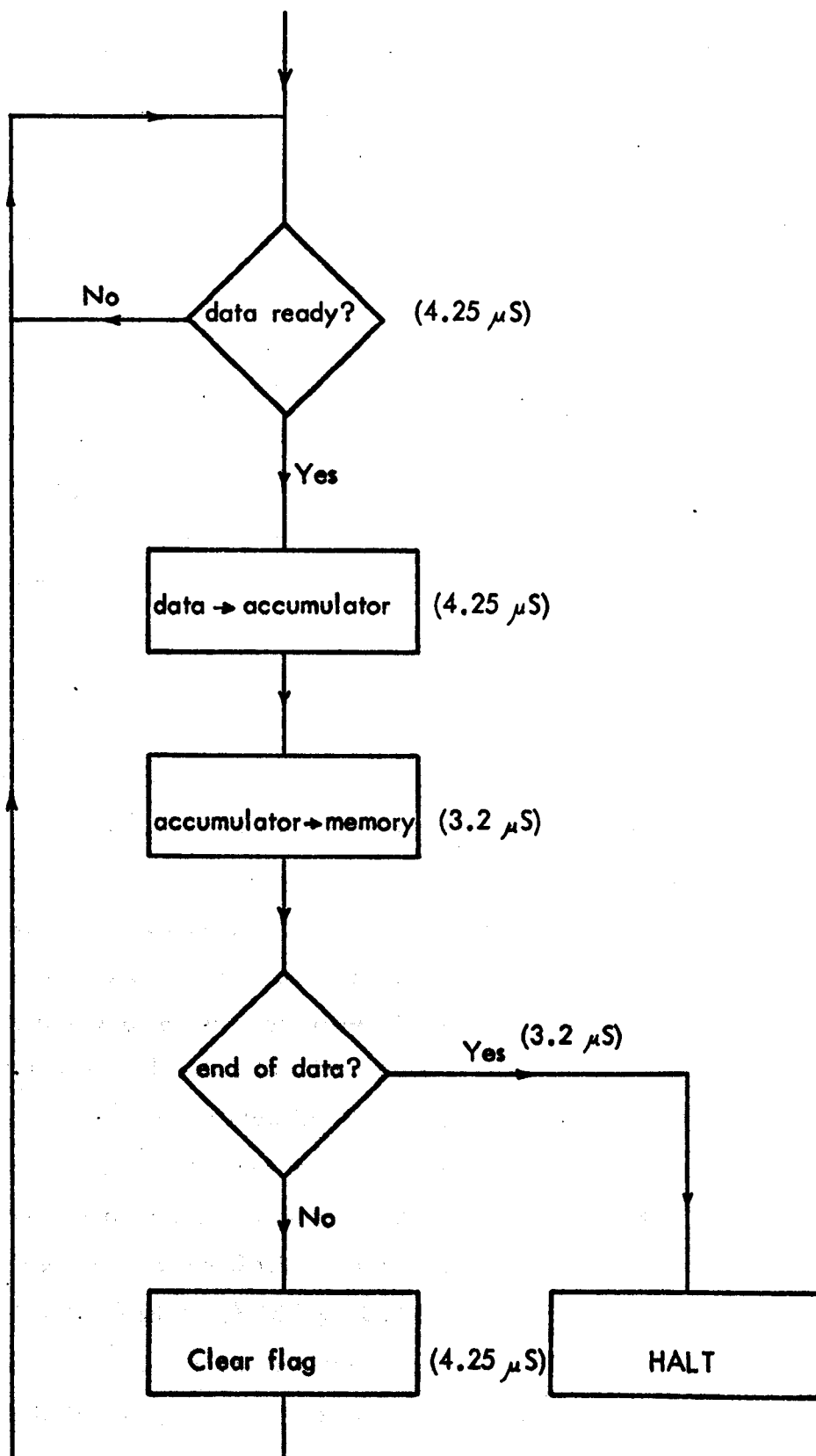


Figure 3.3. Programmed data transfer.

with the data word to be transferred. When data transfers are made the word count and current address registers are automatically incremented, overflow of the word count register indicating that all the data has been transferred. With single cycle data break the current address and word count functions are provided by the interface and only $1.6 \mu\text{S}$ is required to transfer each word. All of these data transfer methods can be used to transfer data in either direction.

The fastest of these methods still takes $1.6 \mu\text{S}$ to transfer each word, in addition to the time required to answer the break request (latency time; which could be anything up to $18.2 \mu\text{S}$, but more usually around $1.6 \mu\text{S}$). To transfer a vertical and a horizontal co-ordinate, therefore, will take $4.8 \mu\text{S}$, which is approximately 10% of the active line time of the horizontal scan. In terms of distance, if a field of view of 2.5 m horizontal length were being covered, this would mean that co-ordinates could not be generated for markers which were closer than 25 cm and appeared on the same television line. If a second camera were being used then only markers separated by at least $4.8 \mu\text{S}$ scan time could be recorded. For some marker configurations this limitation is of little consequence, however if it is required to mark several points which lie close together (such as on the foot) then the limitation is unacceptable. This problem can be overcome, quite simply, by using a data buffer in the interface. As markers are detected their co-ordinates are immediately transferred to the buffer memory; allowing for the delays of the necessary control logic this operation may be completed in 100 nS or less, or 0.2% of the active line time (a distance of 0.5 cm in the above example).

The use of a buffer memory in the interface will still place some restrictions on data acquisition. However, these restrictions may be minimised by making the correct choice of data transfer time to load the buffer into computer memory. Three possible times are available for this operation. The first possibility requires the use of a double buffer memory - as one is being filled, the other is being unloaded into computer memory. The additional complexity and expense of a double buffer was considered to be unnecessary when the other methods were appraised. The second method uses the field

blinking period, when there is no picture information, to transfer data. This period lasts for approximately 1.2 mS and therefore 750 words of data could be transferred using single cycle data break (250 using three cycle data break). It would be unwieldy as well as expensive to provide a buffer memory of such capacity in the interface, whereas a buffer size of, say, 50 words is practical but something of a compromise. The third, and chosen, time for data transfer is the line blanking period, which lasts for 12 μ S. If single cycle data break is used then up to 7 words of data could be transferred within this period. Transfers could be made on every active line so a total of 2000 words could be transferred during any single television field.

3.3.2 Co-ordinate Generator (CG)

A block diagram of the basic co-ordinate generator system is shown in Figure 3.4. The crystal controlled sync generator provides a composite sync to control the camera scan. Line syncs increment a vertical counter which is reset by a field sync. To take account of the effect of interlaced scanning (see Appendix A1), and also to indicate the transition from one television field to the next, the vertical counter counts over two fields i.e. up to 625. This is done by only resetting the counter on every other field sync. A horizontal counter is incremented by a high frequency clock (20 MHz), and counts approximately 1000 intervals along the television line before being reset by the line sync. When a marker is detected in the video signal a marker pulse is generated which causes the latch to immediately read the current contents of the horizontal counter. This horizontal co-ordinate is then transferred to the buffer memory and the system is then ready to repeat this process if necessary - total time \sim 100 nS. The horizontal co-ordinates of up to 5 markers can be stored in the buffer memory. At the end of the television line the contents of the vertical counter (i.e. the vertical co-ordinate) are transferred through the horizontal counter and the latch into the buffer memory. A "break request" is made and the contents of the buffer memory are transferred to the core memory of the PDP 12 during the line blank period. The interface is ready to repeat this procedure, if markers are detected, on the next line.

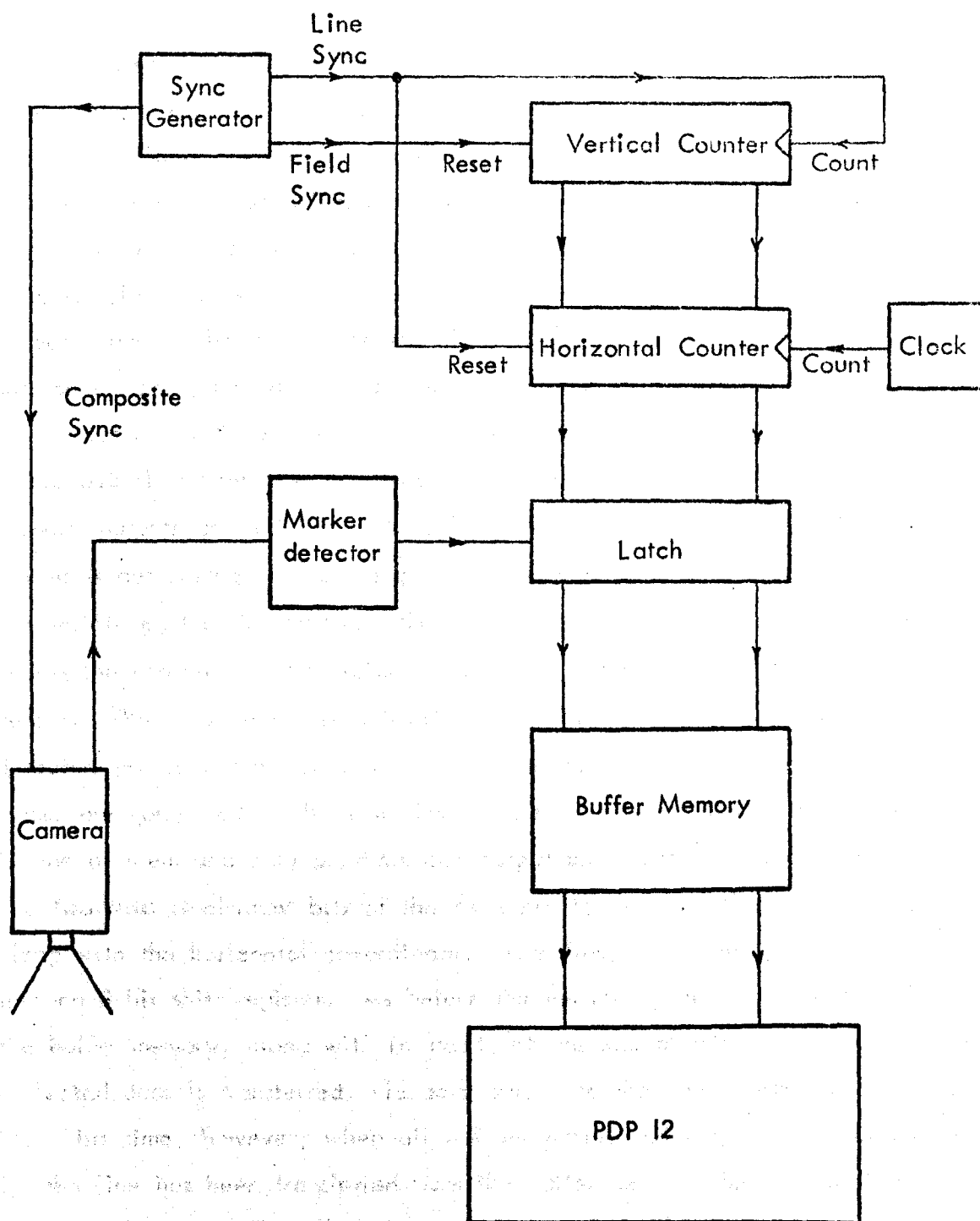


Figure 3.4. Basic Co-ordinate Generator.

The vertical and horizontal co-ordinates require a 10 bit word for binary representation. The word length of the PDP 12 is 12 bits, thus two bits are available for coding purposes (4 separate codes). As the number of horizontal co-ordinates collected during any one line are variable (up to 5) it is necessary to use one code to identify the vertical co-ordinate. The three remaining codes could be used to indicate three cameras. This would be adequate for the three dimensional requirement, but to allow future expansion of the system it was considered desirable to provide codes for up to six cameras. To do this a 3 bit code is required. The block diagram of Figure 3.5 illustrates how the system, with coding, operates. The 6 marker detector outputs are fed into a 6 to 3 coder and priority register. When a marker is detected in a video signal a marker pulse is generated by the appropriate marker detector and the priority register provides a pulse which causes the contents of the horizontal counter to be read by the latch, as before. The coder provides a 3 bit code to indicate which of the 6 marker detectors provided the marker pulse. In the event that two or more marker pulses are generated at the same instant the priority register allocates priority to one of them and only provides one output pulse and the appropriate code. The two most significant bits of the code are loaded into the buffer memory along with the horizontal co-ordinate. The third bit of the code is clocked into an 8 bit shift register. As before the vertical co-ordinate is loaded into the buffer memory, along with its code, at the end of the line; and then the collected data is transferred, via data break, to the core memory of the PDP 12. This time, however, when all the horizontal and vertical co-ordinate data for the line has been transferred from the buffer memory the shift register word is then transferred. The allocation of codes is shown in Figure 3.6, and it can be seen that the vertical co-ordinate is unique and independent of the third bit. Data is always transferred to computer memory in the same order, that is the vertical co-ordinate followed by up to five horizontal co-ordinates and lastly the shift register word. It is then a simple matter for the program to reconstruct the appropriate codes, and hence define the cameras from which a particular co-ordinate came.

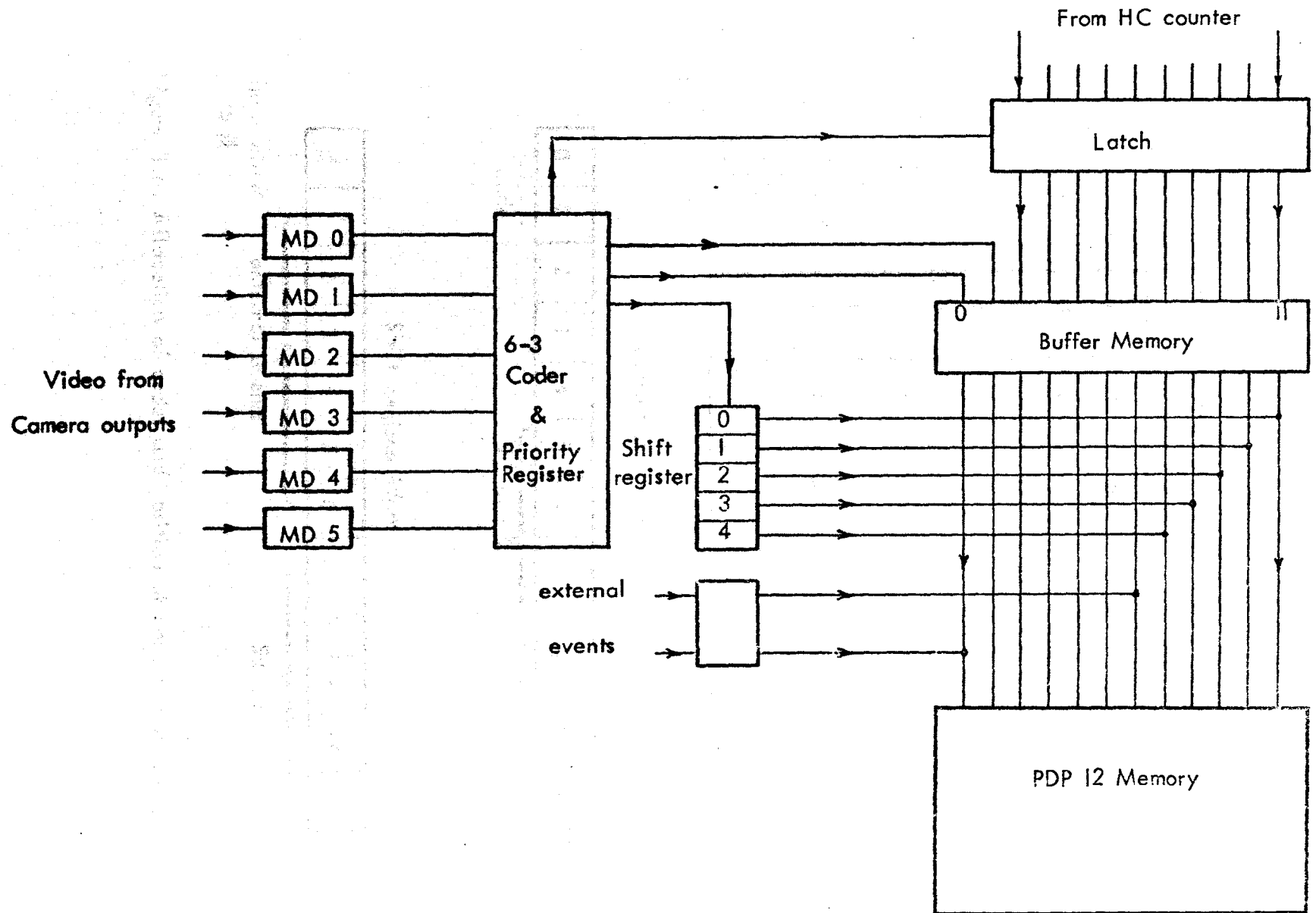
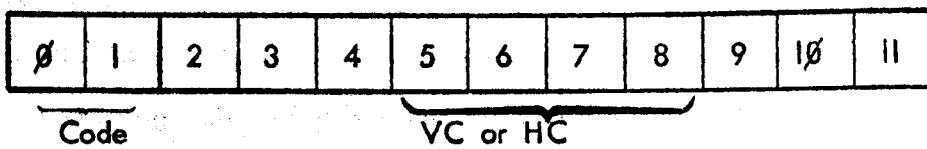


Figure 3.5. Scheme to provide codes for six cameras.

Code bits			Allocation
\emptyset	1	SR	
\emptyset	\emptyset	\emptyset	MD 0
\emptyset	\emptyset	1	MD 1
\emptyset	1	\emptyset	MD 2
\emptyset	1	1	MD 3
1	\emptyset	\emptyset	MD 4
1	\emptyset	1	MD 5
1	1	\emptyset	VC
1	1	1	VC

Co-ordinate Word



Shift Register Word

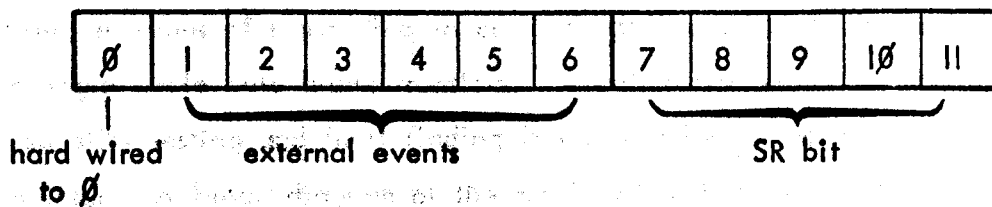


Figure 3.6. Allocation of codes and position of code bits in data.

With the data break method it is possible to run a program independently of data transfer (with due regard to areas of memory used for program and data transfer). As a maximum of 7 words are transferred during any one television line, and as data is only generated when a marker is detected, there is a considerable amount of time available for running programs. Some of this time can be used for collecting data, via the analogue to digital conversion facility, from other measurement systems such as a force plate. This data collection must be synchronised with the television system and the interface provides synchronising pulses for this purpose. To avoid the restriction of having to start data acquisition simultaneously a means is provided for indicating when additional measurements are started. This is done by using the spare bits in the shift register word (total 12 bits when transferred to the PDP 12). With this word the most significant bit is permanently set to '0' to avoid ambiguity with the vertical co-ordinate code. As the system is restricted to 5 horizontal co-ordinates on any one line then 6 bits of the shift register word are available for additional coding. When data collection from some other measurement system is started one of these bits is set to a '1'. As the shift register word is transmitted with every set of co-ordinates the exact starting point of this other data collection is stored. An example of a set of data is shown in Figure 3.7, together with the translation of the codes.

For its operation the co-ordinate generator requires a set of synchronising pulses, a means of responding to computer software and the control facilities for single cycle data break transfers. A calibration system is required, and to simplify testing and fault finding it was considered desirable to provide a simulator. A block diagram of the total system is shown in Figure 3.8.

3.3.3 Sync Generator and Simulator (SGS)

The SGS derives from the television line and field sync inputs six related synchronisation pulses which are required at various points in the system.

These are:-

location	0	1		2	3	4	5	6	7	8	9	10	11	Octal value of bits 2-11
0	1	1	VC	0	0	0	1	0	1	1	0	1	0	0132
1	0	0	HC	1	0	1	1	0	0	0	1	1	0	1306
2	0	0	HC	0	1	1	0	1	0	0	0	1	1	0643
3	0	1	HC	0	1	1	0	0	1	1	1	1	1	0637
4	0	0	HC	0	0	1	0	0	0	1	0	1	0	0212
5	0	0	SR	1	0	0	0	0	0	0	1	0	1	Third code bits
6	1	1	VC	0	0	0	1	0	1	1	0	1	1	0133

Data as received from the interface. Bit 2 of the shift register (SR) word being set indicates that an external event has occurred.

location	0	1	SR (bit)	Code
0	1	1	X	VC
1	0	0	1(11)	Camera 1
2	0	0	0(10)	Camera 0
3	0	1	1(9)	Camera 3
4	0	0	0(8)	Camera 0
6	1	1	X	

Three bits codes for each HC. X = irrelevant.

Figure 3.7. Set of data showing de-coding.

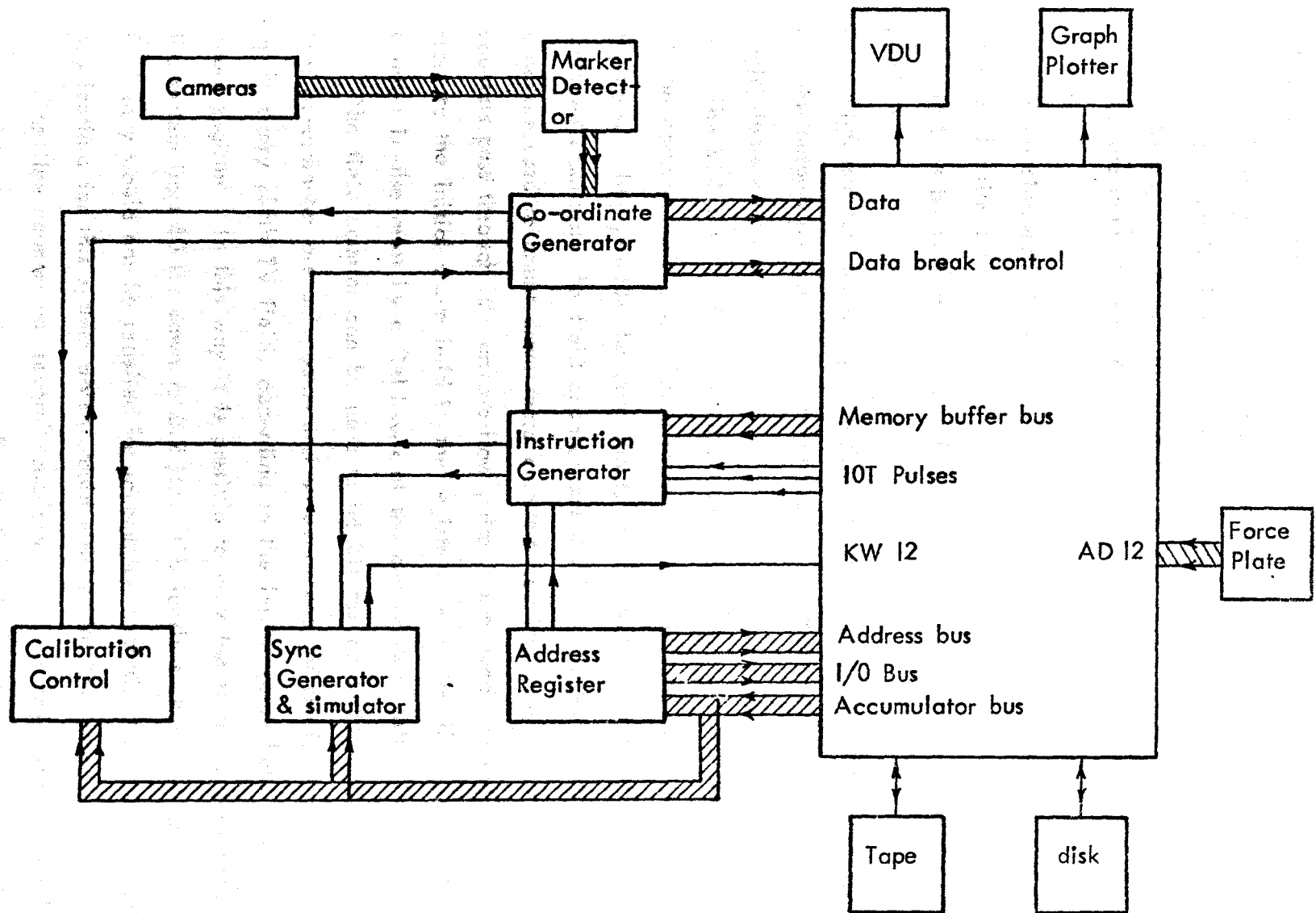


Figure 3.8. Complete System.

1) Field sync A - a 20 μ S pulse occurring at the beginning of each TV field.

2) Field sync B - a 4.7 μ S pulse which occurs when Field sync A and the Line sync are in phase, i.e. every second TV field (due to interlace).

3) Line sync - a 4.7 μ S pulse at the start of each TV line.

4) Line blank A - a 12 μ S pulse initiated by the leading edge of the Line sync.

5) Leading edge line sync - a 100 nS pulse initiated by the leading edge of the line sync.

6) Line blank B - an 11.9 μ S pulse which starts 100 nS later than Line blank A.

High and low logic level versions are provided of all the above syncs and the outputs are capable of driving up to 30 standard TTL (Transistor Transistor Logic) loads each.

The SGS also contains two 8 bit registers which may be set to any value by the computer software. These registers are the Field count and Line count registers. They provide the data inputs for the initial values of two 8 bit up/down counters; the field counter and the line counter. Field or line syncs are used to count down these counters from the initial value. When the counters pass through \emptyset an overflow pulse is produced and the counters are reset to the initial value held in the register. With the line counter the counter is also reset by a field sync at the end of each television field.

The field counter can be used to inhibit the co-ordinate generator so that co-ordinates are only generated on every 2nd TV field, every 3rd etc. up to every 256th TV field, according to the initial value held in the field count register. In this way it is possible to vary the sampling frequency of the system from 50 Hz down to 0.2 Hz. Obviously such a low sampling frequency would not be required for locomotion analysis, but there are other applications for this system which would require this low rate.

The line counter has several functions. As with the field counter it is set to give an output pulse according to the initial value held in the line count register. The first use is to provide simulated marker signals. This is

done by using the overflow pulse to trigger a series of 7 dual monostables. This arrangement produces seven 90 nS pulses at intervals of approximately 8 μ S. Logic provides various combinations of these pulses, as shown in Figure 3.9. The outputs correspond to video signal levels and are capable of driving 75 Ω inputs. These signals can, therefore, be substituted for the camera inputs and be used to test the complete interface and assist in program development by providing known inputs. The number of lines on which these simulated pulses appear is controlled by the value held in the line count register which is under control of the computer program. The number of lines can be varied from every line to every 256th line. Computer instructions are available to enable and disable the simulator.

Another function of the line counter is to provide synchronisation pulses to control data acquisition from other measuring systems. The overflow pulse is used to generate these pulses so that the frequency at which they occur may be set by the program. Synchronization pulses will always start after the beginning of a field after receipt of an external enabling signal, i.e. at the request of the other measuring system. The time at which this occurs, in relation to co-ordinate generation, is stored in the shift register word as previously described.

A possible future use for the line counter is to provide a series of out of phase field syncs. This can be done using the logic shown in Figure 3.10. At the beginning of each normal field a '1' is clocked into the shift register. This causes a 160 μ S field sync to be generated by monostable 1 at FS1. An overflow pulse from the line counter clocks this '1' along the shift register producing as it goes 160 μ S field syncs at FS2, FS3 etc. From the table in Figure 3.10 it can be seen that a series of out of phase field syncs can be produced according to the value in the line count register. In this way sampling rates higher than 50 Hz could be achieved by using more than one camera to cover the same field of view and using these out of phase field syncs to initiate the vertical scan of each camera. If six cameras covered the same field of view a sampling frequency of 300 Hz would be achieved.

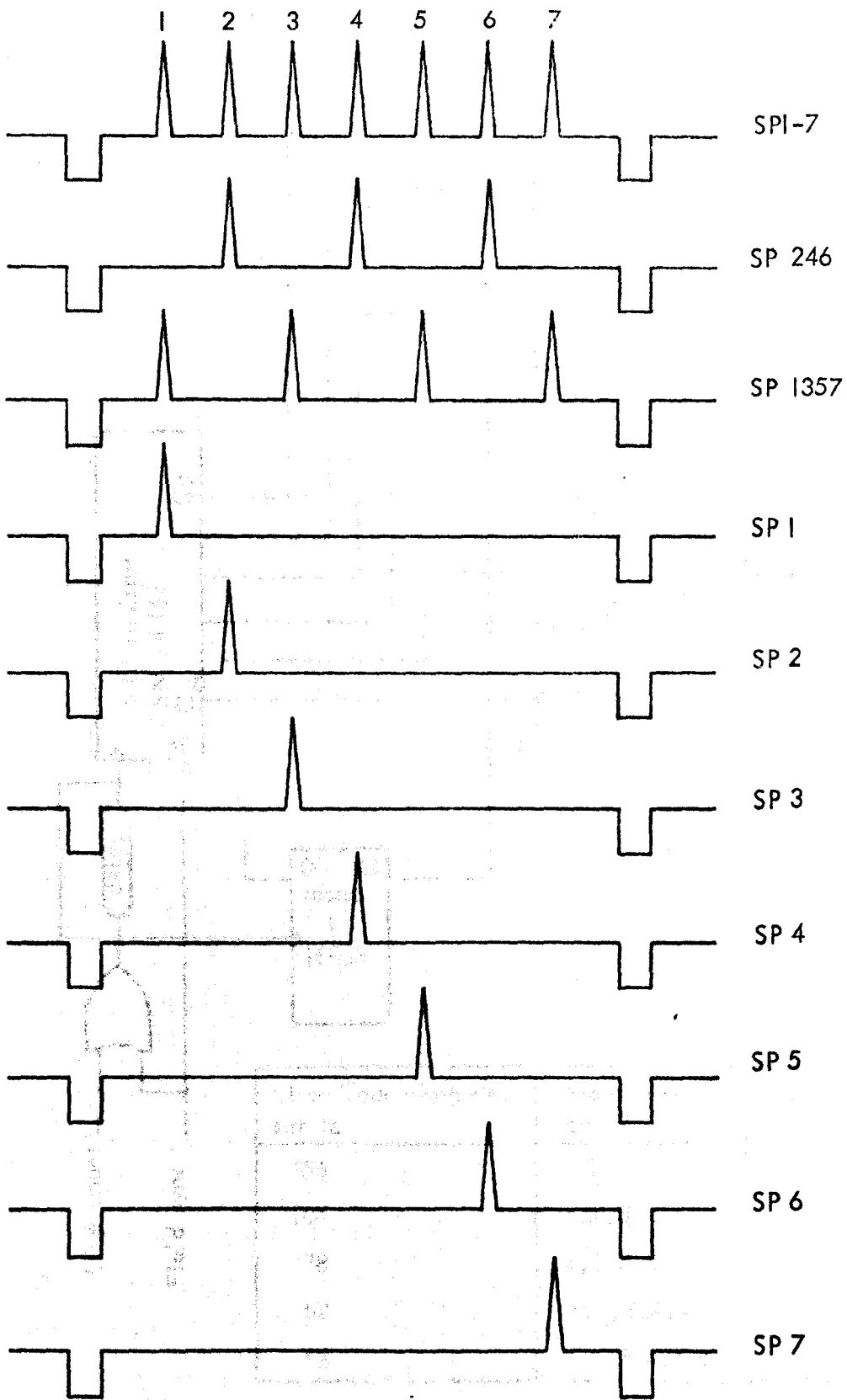


Figure 3.9. Simulated marker pulses.

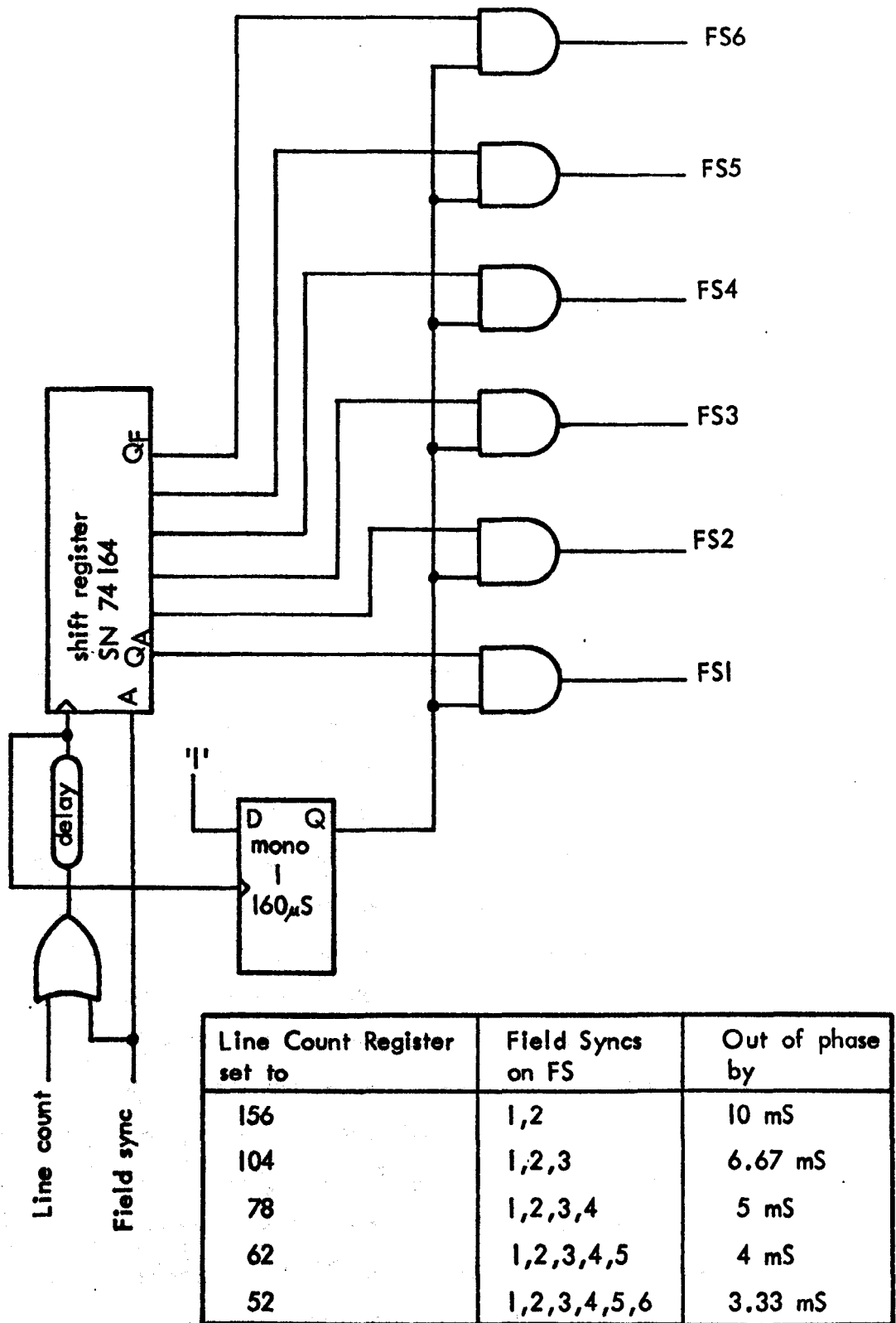


Figure 3.10. Logic to generate out of phase field syncs.

3.3.4 Address Register (AR)

The memory of the PDP 12 is organised in 4 K word segments up to a total of 32 K words. A 12 bit memory address is required to access locations within each 4 K segment, and a 3 bit extended address is required to address the segments - in total a 15 bit address. The address register provides this and a set of computer instructions have been created to control it. The initial address may be set up to any value which is a multiple of 8 (10 in octal notation). The reason for this slight limitation is that by fixing the initial value of the first 3 bits of the memory address to \emptyset both the memory address and the extended address can be set to their initial value with one computer instruction. This instruction loads a 12 bit word from the accumulator into the Initial Data Address register; bits 9, 10 and 11 of this word corresponding to bits \emptyset , 1 and 2 of the extended address (8-mode field address in software terms). The remaining bits ($\emptyset - 8$) are used to set bits $\emptyset - 8$ of the initial memory address. The address register itself is a 15 bit binary counter which is incremented by the Address Accept pulse - a control signal provided by the data break facility (see Appendix A2). The interface can therefore address any location in the PDP 12's memory up to the maximum of 32 K (only 16 K is available on the machine used at present, but it can be expanded to 32 K).

Computer instructions have been created to enable and disable the address register, and also to allow it to operate in a double (or circular) buffer mode. In this mode memory locations 10000_8 to 27777_8 (4 K to 12 K) are used. When the interface has used all the locations up to 17777_8 (4 K to 8 K) the computer is signalled and the program starts to unload this area of memory onto a mass storage device (such as magnetic tape or disc), while the interface is filling up locations 20000_8 to 27777_8 (8 K to 12 K). Similarly when the interface has used up these locations the address register is reset to 10000_8 and locations 20000_8 to 27777_8 are dumped onto mass storage. By using this facility it is possible to have continuous data rates of at least 4 K words/sec if a disk is available as the mass storage device. This effectively means that the duration of data acquisition is not limited by the core memory size of the computer. The capacity of an R K05 disk, as used on the PDP 12, is 1.6 M words. With an incoming data rate of 4 K words/sec (approximately

26 co-ordinate pairs per television field) a test could be run continuously for 6 minutes. Clearly this does not present a limitation as far as data acquisition is concerned, but there would be problems (of time) in processing such a large amount of data. Utilisation of the Double Buffer mode for transferring data to disk is further discussed in section 6.6.

Computer instructions to read the current memory address and extended (field) address are also available, and may be used at any time.

3.3.5 Calibration (CAL)

To calibrate the field of view of each camera a grid of markers is placed in the relevant plane and the system acquires the co-ordinates of this grid. A computer program organises this data into a calibration matrix for the particular camera and all subsequent data generated by the camera is referred to this matrix, and calibrated. This is done by finding the nearest grid point to the data point, the position of this grid point in the matrix defines its true spatial position; the difference between the grid point and the data point is then taken and calibrated by assuming a linear relationship between adjacent grid points, this difference is then added to the known spatial position of the nearest grid point. This procedure can be stated as follows for the x co-ordinate:-

$$x_{ic} = x_{gn} \cdot K + L - \frac{x_{gdn} - x_i}{\left| \begin{array}{l} x_{gdn} - x_{gd} (n - 1 \text{ if } x_{gdn} > x_i) \\ (n + 1 \text{ if } x_{gdn} < x_i) \end{array} \right|} \cdot K$$

- where -
- x_{ic} = calibrated co-ordinate of marker
 - x_{gn} = position of nearest grid point in calibration matrix
 - x_{gdn} = co-ordinate of nearest grid point (grid data point), matrix column n.
 - x_i = marker co-ordinate (data point)
 - K = calibration constant
 - L = shifting factor - to position origin

A similar equation is used to calibrate the y co-ordinate.

The spacing of the elements of the grid should be based on the expected non-linearity of the system. The camera specifications state a linearity within 1%; to allow for this a grid spacing of 100 units is required. A non-linearity of 1% in 100 units would be 1 unit, and therefore the non-linearity becomes indistinguishable from quantization errors. For the fields of view used for locomotion analysis a grid spacing of 10 cm is suggested.

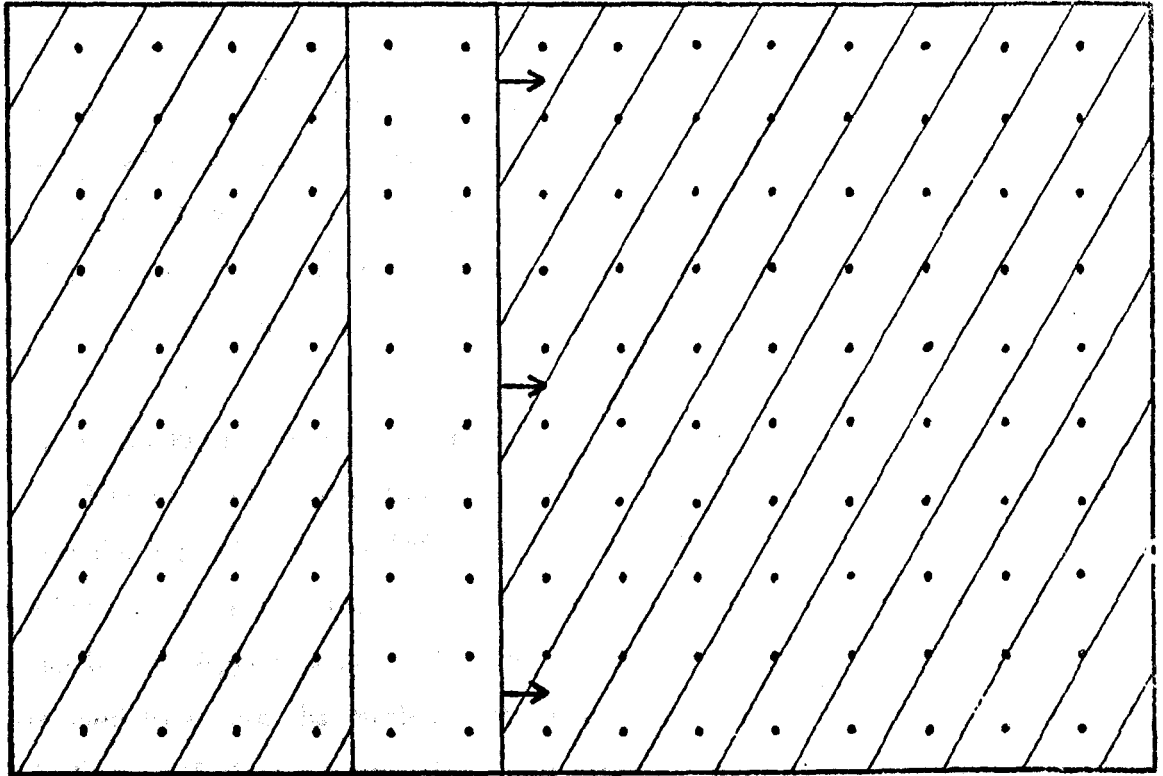
For a field of view of 2.5 m width there would be 25 grid points in the horizontal scan. The co-ordinate generator is limited to taking in a maximum of 5 co-ordinates in the horizontal scan line, therefore a system is required to overcome this limitation for the calibration grid. One way of doing this, of course, would be to slope the grid so that no more than 5 grid co-ordinates could appear on the same line. A correction would have to be made to compensate for this slope during calibration of data and this was considered to be inconvenient. An alternative solution was found by designing a hardware calibration control. This control operates by allowing the CG to successively generate the co-ordinates of columns of 2 markers from the grid until the whole field of view has been covered, the computer is then signalled that calibration co-ordinates have all been acquired. This process is shown, schematically, in Figure 3.11, and may be considered as a window 2 grid points wide moving over the grid. Co-ordinates for the calibration grid are generated over two TV fields, so that the effects of interlaced scanning may be taken into account (see Appendix A1). A set of computer instructions are provided to operate the calibration control, and for maintenance purposes there is a "free run" mode which allows the control to operate continuously.

Ingen Schenau (1973) found, with his TV/computer system, that the linearity in the horizontal was independent of the linearity in the vertical and derived quadratic functions for correction terms of the form:-

$$f(x') = bx(x' - Kx)^2$$

$$g(y') = by(y' - Ky)^2$$

where bx , by , Kx and Ky are constants. The K constant fixed the origin, and the b constant was found by fitting the quadratic functions to values obtained



Moving window

Figure 3.11. Schematic of calibration grid data acquisition.

for the co-ordinates of a grid of points. This gave the following equations with which data could be calibrated.

$$x = x' - 4.7 \cdot 10^{-4} (x' - 128)^2$$

$$y = y' - 4.0 \cdot 10^{-4} (y' - 128)^2$$

These equations applied to the particular camera and lens used and assumed that the characteristics would remain constant. With ageing of components the linearity of the television camera could change over a period of time. It is considered desirable, therefore, to provide a calibration system which can easily be used for different cameras and lens and take account of any change in linearity that may occur over a period of time.

3.3.6 Computer Instructions (INS1 and INS2)

The computer instructions required to control the interface are generated using the Input/Output (6000) class of the PDP 12 instruction set. The method of converting these software instructions into hardware operations is described in Appendix A2. A total of 28 instructions have been created, designed to ensure the minimum of manual intervention during use of the interface, efficient programming, and effective maintenance facilities. Details of the function of each instruction and examples of programming will be found in Chapter 5.

CHAPTER 4
INTERFACE LOGIC

- 4.1 Introduction and Nomenclature
- 4.2 Marker Detectors. MD.
- 4.3 Sync Generator and Simulator. SGS.
 - 4.3.1 Internal Synchronisation Pulses. SGS-A.
 - 4.3.2 Line Count and Field Count Functions. SGS-B.
 - 4.3.3 The Simulator. SGS-C.
 - 4.3.4 External Synchronisation. SGS-D.
- 4.4 Co-ordinate Generator. CG.
 - 4.4.1 Enable Logic and Clock. CG-A.
 - 4.4.2 Priority Register and Coding. CG-B.
 - 4.4.3 Co-ordinate Counters and Latch. CG-C.
 - 4.4.4 Buffer Memory. CG-D.
 - 4.4.5 Buffer Memory Address and Control Logic. CG-E.
 - 4.4.6 Additional Code Bits. CG-F.
- 4.5 Address Register. AR.
 - 4.5.1 Initial Data Address Register and 15 Bit Address Register. AR-A.
 - 4.5.2 Address Increment Logic and Computer Bus Drivers. AR-B.
 - 4.5.3 Computer Bus Drivers. AR-C.
- 4.6 Calibration Control. CAL.
 - 4.6.1 Calibration Initialisation and Control Logic. CAL-A.
 - 4.6.2 Counter and Right Hand Limit Latch. CAL-B.
 - 4.6.3 Left Hand Limit Latch and Margin Logic. CAL-C.
 - 4.6.4 Window Control Logic. CAL-D.

- 4.7 **Instruction Generator 1. INSI.**
 - 4.7.1 **Calibration Control Instructions. INSI-A and INSI-B.**
 - 4.7.2 **Sync Generator and Simulator Instructions. INSI-C.**
- 4.8 **Instruction Generator 2. INS2.**
 - 4.8.1 **Co-ordinate Generator Instructions. INS2-A.**
 - 4.8.2 **Address Register Instructions. INS2-B to INS2-E.**
- 4.9 **Construction and Planning**

4.1 Introduction and Nomenclature

A complete description of the logic of the interface is given in this section. In describing the function of the logic the signal naming and drawing conventions used by DEC have been adopted. This system was chosen because it allows the logic functions to be followed easily and is, of course, compatible with the PDP 12 manuals. Each drawing is allocated a name and a number, the number usually being an abbreviation of the name. Logical signal names are used in the drawings to minimise the number of signal line crossings and to facilitate explanation. The signal name consists of abbreviations which give the origin, function and assertion condition of the signal:-

The origin is specified by the first word of the signal name and is the drawing number upon which the signal is generated.

The signal description (function) is a description of the logical function of the signal when asserted.

The asserted condition is either H (high) or L (low) corresponding to +3 or 0 volts respectively, and is the signal level which indicates that the intended function has occurred (i.e. is True). The signal is considered not asserted when the signal level differs from the assertion level.

Example:-

[SGS-A] LE LSY L

Refers to a signal which originates on the Sync Generator and Simulator drawing part A. The signal will be low when the Leading Edge of the Line Sync occurs.

[CG-B] SGC H

Refers to a signal which originates on the Co-ordinate Generator drawing part B. The signal sets the Generate Co-ordinate flip flop when it is high.

Signals originating from flip flops are defined in terms of the flip flop state, as shown in the following table:-

Signal Name	State of flip flop	Signal Voltage
[CG-B] GC (0) H	0	+3
[CG-B] GC (0) L	0	0
[CG-B] GC (1) H	1	+3
[CG-B] GC (1) L	1	0

Example:-

[CG-B] GC (1) L Refers to a signal which originates on the Co-ordinate Generator drawing part B. The signal line is low when the Generate Co-ordinate flip flop is set.

On the drawings and in the description of the logic of each drawing the origin part of the signal name is omitted if the signal is generated on that drawing, otherwise it is shown enclosed in square brackets. The logic symbols used are those which convey the function of the logic element (conceptual logic) rather than the actual hardware device used, although the reference number of the actual device is also shown. These logic symbols are shown in Figure 4.22 at the end of the logic drawings.

The computer generates the signals [100] BA INITIALIZE H and [100] BB INITIALIZE H which are used to initialize the interface. These signals may be generated by the use of the IO PRESET console switch or by the program.

In the following description of the logic frequent cross references are made between logic drawings. For this reason these drawings will be found together at the end of this section. The drawing numbers are referenced by the sub-headings of the logic descriptions. Other figures, which may show timing diagrams for instance, are placed by the relevant text.

4.2 Marker Detectors. MD.

The circuits for the two types of marker detector used have been shown in Figures 2.3 and 2.5. The MD circuit board also contains circuitry used to condition the field and line syncs from the television sync pulse generator. These circuits are simple comparators with TTL compatible outputs, as shown

in Figure 4.15.

4.3 Sync Generator and Simulator. SGS.

The drawings for the SGS are shown in Figure 4.16, SGS-A to SGS-D. Generation of internal syncs is shown in SGS-A, line count and field count functions are on SGS-B, simulator logic is on SGS-C, and external sync control is on SGS-D.

4.3.1 Internal Synchronisation Pulses. SGS-A.

TTL compatible field and line syncs from the Marker Detector board (MD) are fed into schmitt trigger input gates and used to generate the signals shown on this drawing. FSYB is a field sync of $4.7 \mu\text{s}$ duration which occurs on every other television field, due to the interlace feature of the camera sync generator. Generation of the other signals is self explanatory from the logic.

4.3.2 Line Count and Field Count Functions. SGS-B.

The Field Counter (FC) is an 8 bit binary up/down counter. The clock input, [SGS-A] FSYA H, will count down the counter from a preset binary number held in the Field Count Register (FCR). A low level borrow pulse occurs when the clock input is low and the counter is at zero. This borrow pulse is fed back to the load input of the counter and will cause the counter to be set to correspond to the data inputs, provided by the FCR. As soon as the counter becomes non-zero the conditions required to generate the borrow pulse are not met and the borrow output is returned to its normal high level. The borrow pulse lasts for approximately 40 nS, and is also used to preset ENCG when the Field Count mode is enabled ([INSI-C] ENFC (1) H). ENCG (1) H enables the Co-ordinate Generator inputs for the duration of one television field until ENCG is cleared by [SGS-A] FSYA H. When the Field Count mode is disabled, [INSI-C] ENFC (0) L, ENCG is held permanently set. The Field Count Register is loaded with the contents of the Accumulator (bits 4 - 11) by [INSI-C] LDFC H.

The Line Counter (LC) is a similar up/down counter except that its clock input is the television line sync [SGS-A] LSY H. In this case the counter is also preset at the beginning of each television field by [SGS-A] FSYA L. The borrow pulse is produced as before and is used for several functions. The Line Count Register (LCR), which provides the data inputs to the counter, is loaded with the contents of the Accumulator (bits 4 - 11) by [INSI-C] LDLC H.

4.3.3 The Simulator. SGS-C.

The logic in this drawing shows how [SGS-B] LC L is used to generate the simulated marker pulses. The simulator is enabled by the signal [INSI-C] ENSM (I) H, and when this condition is met the leading edge of [SGS-B] LC L will trigger the seven monostables SPT 1 to SPT 7. The -ve going edge of the pulse produced by each monostable triggers a second monostable to produce 90 nS duration pulses SP 1 - SP 7. These outputs are combined and buffered to provide various simulated marker pulse configurations capable of driving 75^Ω inputs as previously shown in Figure 3.9.

4.3.4 External Synchronisation. SGS-D.

External data acquisition is synchronised by the SYN flip-flop. When synchronisation is requested by [EXT] SYN (I) H (a signal provided by the external device, which may originate from a flip flop, as shown here, or may simply be a level) SYN is set on the -ve going edge of the next field sync, [SGS-A] FSYA H. SYN (I) H is then available to signify that synchronisation has started. It is used here in conjunction with a pulse generated by [SGS-B] LC L to gate line blank pulses. [SGS-B] LC L triggers monostable LBK GATE which is set to provide a 400 μ S pulse. Whenever this pulse occurs, and SYN is set, six buffered line blank pulses will be available as BSYN H. Any number of pulses can be provided by changing the timing components of LBK GATE; the six pulses generated here are used as external sync inputs to the KW12 clock of the PDP 12. The frequency at which this pulse train occurs is controlled by the frequency of [SGS-B] LC L, which in turn is set by the contents of the line count register, LCR.

4.4 Co-ordinate Generator. CG.

The drawings for the CG are shown in Figure 4.17 CG-A to CG-F. Enable logic and the clock is shown on CG-A, the priority register and coding is on CG-B, co-ordinate counters and the high speed latch is on CG-C, the buffer memory is on CG-D, buffer memory address and control logic is on CG-E, and CG-F contains the logic for the additional code bit storage.

4.4.1 Enable Logic and Clock. CG-A.

The marker detector inputs to the CG are enabled by the signal EMDI L. The detector inputs are inhibited during the line blank period by [SGS-A] LBKA L and also, at any time, by the signal [CAL-A] DISCG L. The facility to exercise external control over the CG is provided here by the switch inputs, whose conditions are sampled at the beginning of each television field. SWITCH 1 H and SWITCH 2 H will both set ENABLE on the occurrence of [SGS-A] FSYA H. SWITCH DISCG H will clear ENABLE when the field sync occurs. [100] BB INITIALIZE H (originated by program or console switch) will also clear ENABLE and hence inhibit the marker detector inputs.

The clock for the horizontal counter is shown here, it is a simple R-C multivibrator as described in Texas Instruments (1973a). The clock is reset on the trailing edge of [SGS-A] LE LSY L by a 35 nS pulse. This arrangement has proved to be entirely satisfactory as will be seen by the stability test described in the Results and Discussion chapter.

4.4.2 Priority Register and Coding. CG-B.

The marker detector inputs are controlled by the priority register PRI. The cascading input P_0 is fully over-riding and will inhibit each package when high. A marker detected pulse [MD] CHx H will produce the signal SGC H which clocks a '1' into GC. GC (1) L immediately latches the data in the priority register and inhibits the PRI inputs. The timing diagram for this sequence of events is shown in Figure 4.1. The times shown in this diagram are maximums and in practice the operation is faster. In the event that marker detected pulses occur at the same time on more than one input,

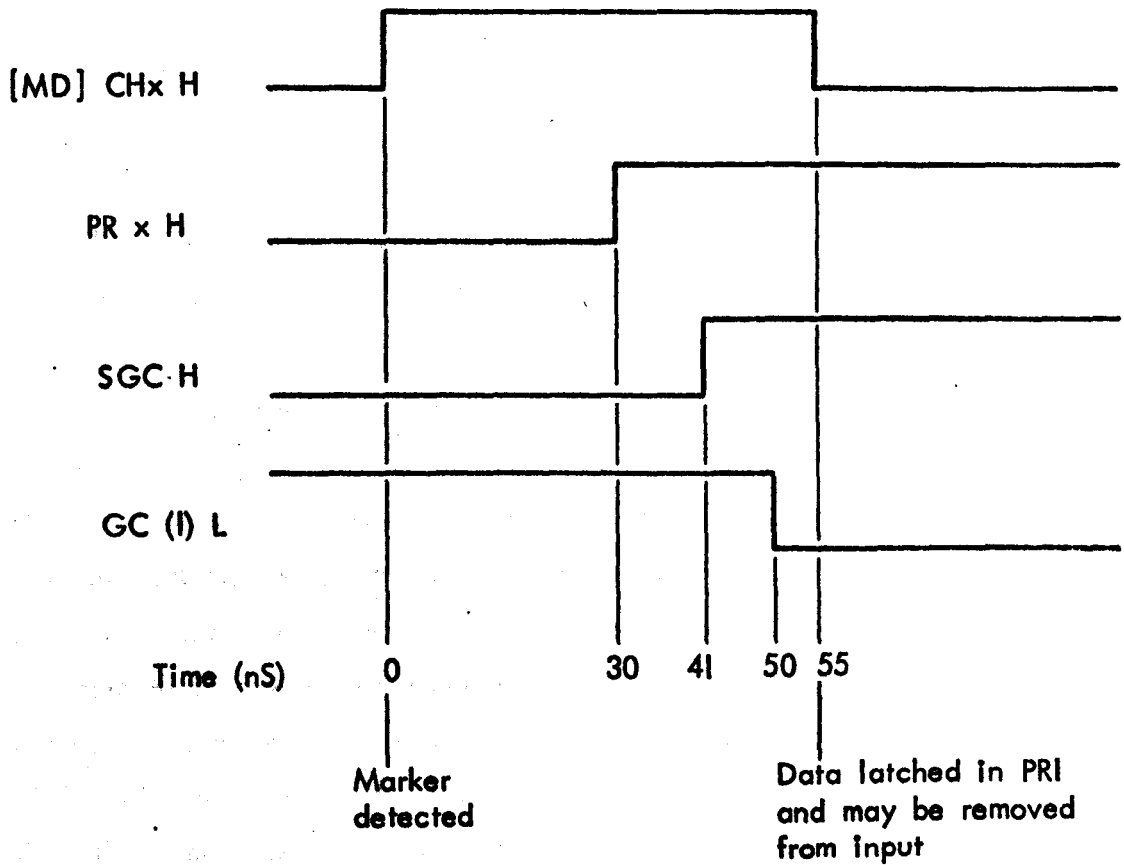


Figure 4.1. Timing diagram showing the latching of the camera channel data in the Priority Register.

the PRI will only give one output which will correspond to the input of highest priority at the time the data is latched. D \emptyset has the highest priority and D5 the lowest. A 3 bit code, to indicate which input generated SGC H, is provided by C0 \emptyset H, C0 1 H and C0 2 H according to the truth table shown in Figure 4.2. SGC H also sets the break request flag BRF, which will request data transfer to computer memory during the next line blank period by asserting BR L. The Break Request signal BR L is held asserted by the computer generated signal [100] BREAK (I) L until the last transfer is about to take place when [CG-F] TSR L clears BRF.

4.4.3 Co-ordinate Counters and Latch. CG-C.

The 10 bit vertical co-ordinate counter VC is incremented by the -ve going edge of [SGS-A] LSY H, and reset to zero by [SGS-A] FSYB H. The VC outputs are fed to the data inputs of the high speed, synchronous horizontal co-ordinate counter HC. The HC is incremented by the +ve going edge of [CG-A] CK H and reset to zero by [SGS-A] LBKB L. The contents of the HC are continuously strobed into the LATCH on the -ve going edge of [CG-A] CK H. When a marker is detected in the video [CG-B] GC (I) L is asserted and the strobe is disabled, the latch will now contain the horizontal co-ordinate of the detected marker. The timing diagram for this sequence of events is shown in Figure 4.3. At the end of each scan line [SGS-A] LE LSY L will be asserted and the HC will be preset to equal the contents of the VC on the next +ve going clock pulse. This data will then be available in the LATCH, and is the vertical co-ordinate corresponding to horizontal co-ordinates generated on that line.

4.4.4 Buffer Memory. CG-D.

The outputs of the LATCH together with the code bits [CG-B] C0 \emptyset H and [CG-B] C0 1 H provide the data inputs to the 16 word (12 bits/word) random access memory RAM. The data inputs are written in to the location addressed by [CG-E] UDC \emptyset to UDC 3 when [CG-E] RAM CE L and [CG-E] RAM WE L are both asserted. During write operations the data outputs OD \emptyset to OD 11 are high. Data is read from the location addressed by [CG-E]

CO 0	CO 1	CO 2	INPUT
∅	∅	∅	∅
∅	∅	1	1
∅	1	∅	2
∅	1	1	3
1	∅	∅	4
1	∅	1	5
1	1	∅	None, VC code
1	1	1	None, VC code

Figure 4.2. Truth table for camera code.

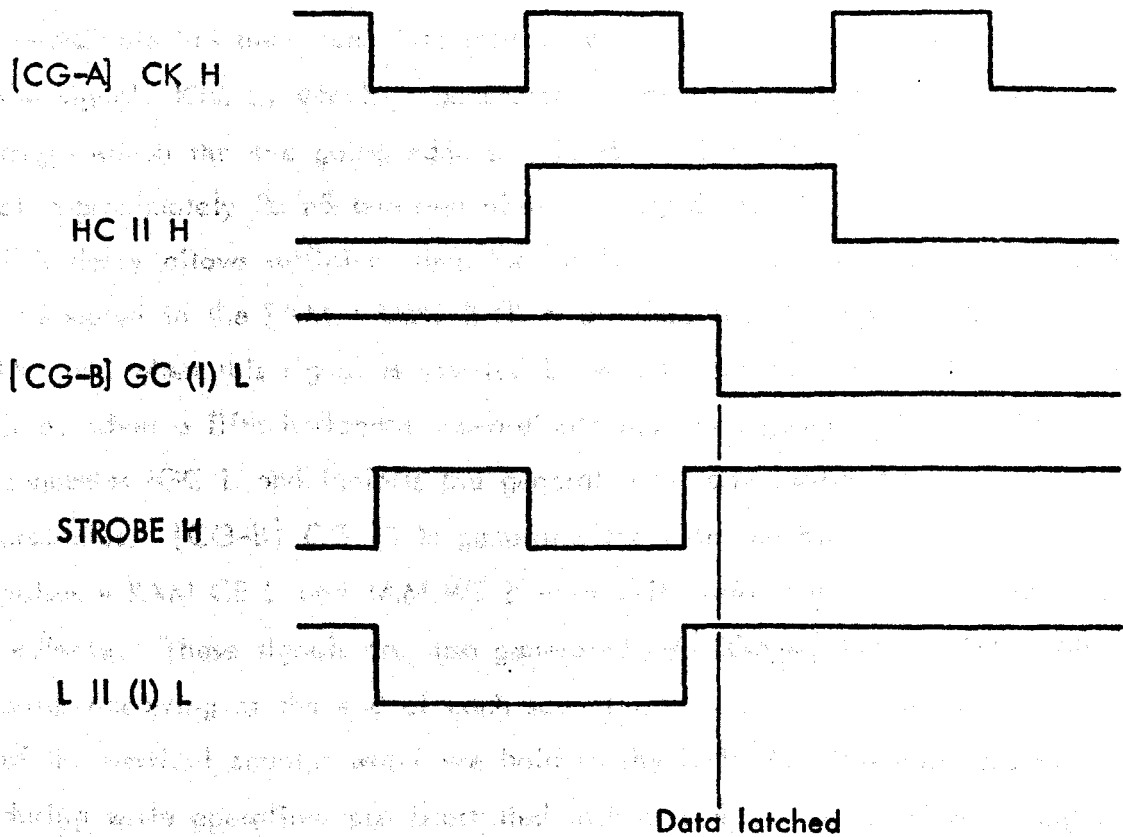


Figure 4.3. Timing diagram to latch horizontal co-ordinate.

UDC 0 to UDC 3 when [CG-E] RAM CE L is asserted and the write enable input (WE) is held high. During read operations the accessed data is placed on the computer data bus via bus drivers. The timing diagram for write operations is shown in Figure 4.4, the CE input must be held low for 50 nS (Access time) and the data input must be available for 20 nS and held for 5 nS after the WE input has been returned to a high level. These times are maximums and typical times are quoted as 35 nS, 18 nS and 0 nS respectively, Signetics (1974). The timing for read operations is not critical as will be described under CG-E. The address inputs are always set up in anticipation of any read or write operation.

4.4.5 Buffer Memory Address and Control Logic. CG-E.

The address of the location to be accessed in the RAM is provided by the up/down counter UDC. [CG-A] 10 INIT L sets the UDC to zero, which is the first location to be used for write operations. The UDC is incremented up on the +ve going edge of [CG-B] GC (I) L, i.e. when the horizontal co-ordinate has been read into memory and GC is cleared. GC is cleared by the signal IGC L, which is generated by the RESET monostable. RESET is triggered on the +ve going edge of [CG-B] GC (I) H and provides a pulse of approximately 35 nS duration after a delay of 80 nS (maximum delay 130 nS). This delay allows sufficient time for the horizontal co-ordinate to be latched and stored in the RAM. UDC 2 (I) H provides the 'D' input to the limit flag LF, and when this signal is asserted LF will be set on the next RESET pulse (i.e. when a fifth horizontal co-ordinate has been generated). LF (I) L generates IGC L and inhibits the generation of any further horizontal co-ordinates. [CG-B] GC (I) H generates the chip enable and write enable pulses - RAM CE L and RAM WE L - to write into memory a horizontal co-ordinate. These signals are also generated by [SGS-A] LE LSY H (a 100 nS pulse occurring at the end of each scan line) to write into memory the contents of the vertical counter which are held in the LATCH. The functions of the UDC during write operations are illustrated in Figure 4.5. A complete timing diagram for generation of the horizontal co-ordinate is shown in Figure 4.6.

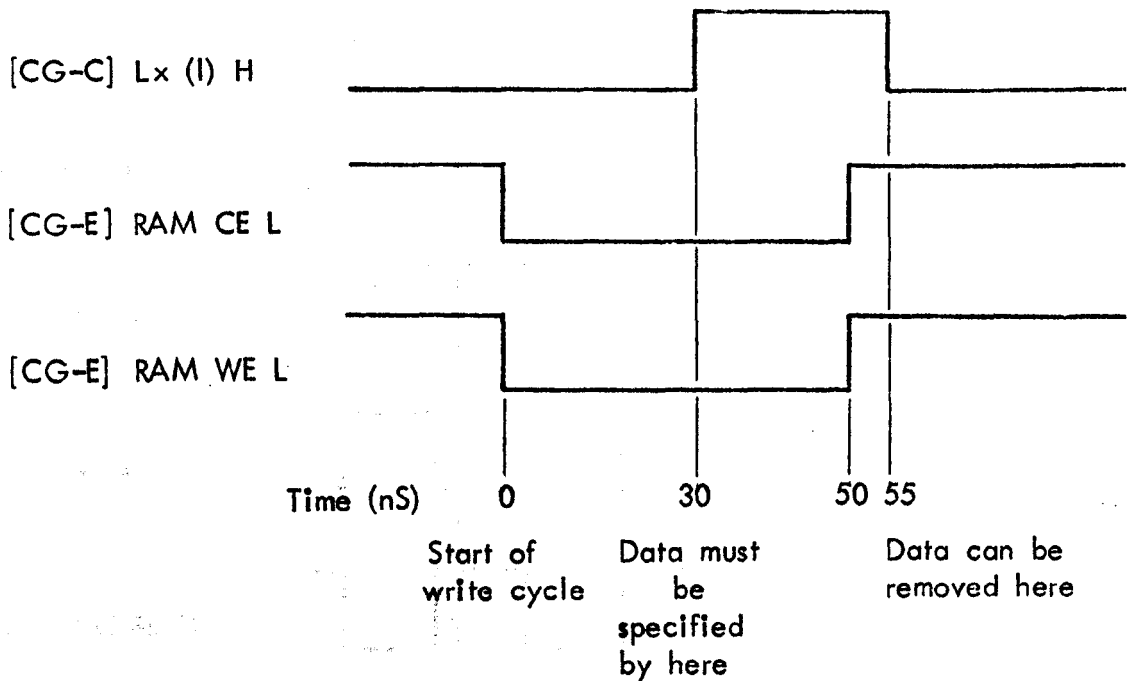


Figure 4.4. Timing for writing data into the Random Access Memory.

3	2	1	∅	Write	Additional Functions
∅	∅	∅	∅	HC 1	None
∅	∅	∅	1	HC 2	None
∅	∅	1	∅	HC 3	None
∅	∅	1	1	HC 4	None
∅	1	∅	∅	HC 5	Set limit flag
∅	1	∅	1	VC	None

NB. The number of HC's is variable, the VC will always be written after the last HC, and is therefore not a function of the UDC address.

Figure 4.5. Random Access Memory address counter (UDC) functions during write operations. (up count).

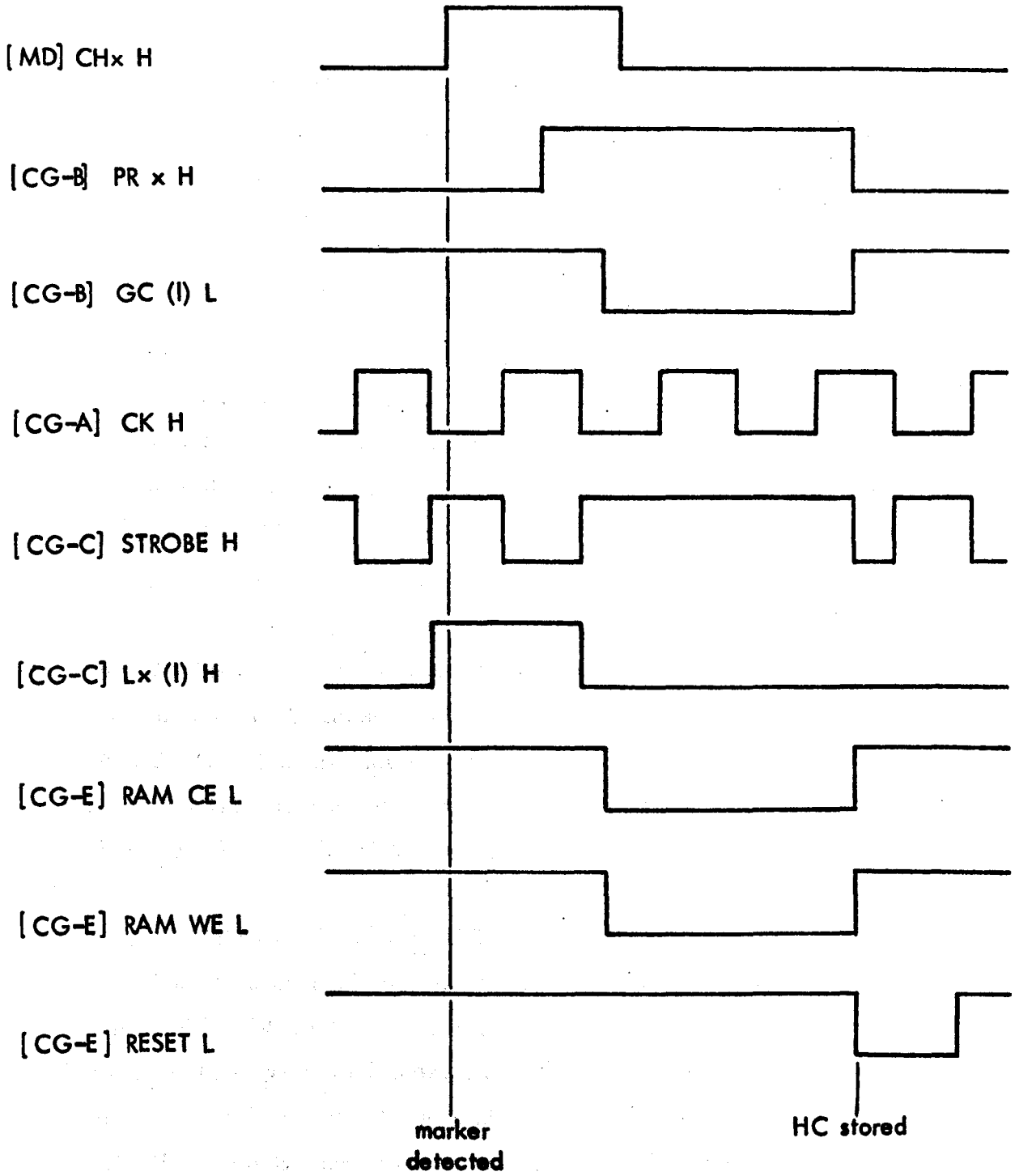


Figure 4.6. Complete timing diagram for storing horizontal co-ordinate.

If horizontal co-ordinates have been generated the break request flag [CG-B] BRF will be set and a break request will be made at the beginning of the line blank period (break request enabled by [SGS-A]LBKA L, and held enabled by [100] BREAK (I) L until all transfers have been made - see CG-B). When the break request is accepted the computer generates [100] ADD ACCEPTED (I) L which is used to trigger a $1\ \mu\text{s}$ pulse from monostable AA. This pulse in turn generates RAM CE L which makes the contents of the currently addressed location of the RAM available at the RAM outputs. As this data is provided on the computer IO bus some 500 nS before it is required the access time of the RAM is not critical. These outputs are taken via buffer amplifiers with open collector outputs and put onto the EXT DATA bus of the computer.

The first word of data to be transferred will be the last word that was read into the RAM, i.e. the vertical co-ordinate. On the -ve going edge of AA L the UDC is down counted by 1, and addresses the RAM location containing the next data word (a horizontal co-ordinate) which is then transferred to computer memory as before. This process is repeated until all the horizontal co-ordinates have been transferred, which is indicated by the UDC underflowing (i.e. UDC output = 1111). When this occurs UDC 3 (I) H generates RAM CE H which disables the RAM outputs. The next AA L pulse will down count the UDC to 1110 which provides a clear pulse and resets the UDC to zero. These functions of the UDC during read out of data are shown in Figure 4.7.

4.4.6 Additional Code Bits. CG-F.

The third code bit for the horizontal co-ordinate is clocked into the SHIFT REGISTER by the +ve going edge of [CG-B] GC (I) L. As each horizontal co-ordinate is generated codes are shifted along by one bit; the code for the last horizontal co-ordinate generated on any line will be at C_A . The SHIFT REGISTER data is transferred to the computer when the UDC underflows on down count. [CG-E] UDC 3 (I) H gates [CG-E] AA H to generate TSR L. This signal enables the EXT DATA bus drivers and the contents of the shift register are transferred to computer memory. Bit \emptyset of this word is permanently set at zero to maintain the uniqueness of the vertical co-ordinate code. Bit 3

3	2	1	0	Read	Additional Functions
0	1	0	1	VC	None
0	1	0	0	HC 5	None
0	0	1	1	HC 4	None
0	0	1	0	HC 3	None
0	0	0	1	HC 2	None
0	0	0	0	HC 1	None
1	1	1	1	-	Read shift register word and clear break request flag
1	1	1	0	-	Reset UDC to 0

NB. The number of horizontal co-ordinates is variable and the VC will always follow the last HC and will be read out first.

Figure 4.7. Random Access Memory address counter (UDC) functions during read operations. (down count).

is set by [SGS-D] SYN (I) H which will indicate if an external device is being sampled; bits 1, 2, 4, 5 and 6 can be used in a similar way if required. TSR L also clears the break request flag [CG-B] BRF, to end the sequence of data transfer to the computer.

4.5 Address Register AR.

The drawings for the AR are shown in Figure 4.18 AR-A to AR-C. The initial data address register and the 15 bit data address counter is shown on AR-A, address increment logic and some computer bus drivers are on AR-B, and the rest of the bus drivers are on AR-C.

4.5.1 Initial Data Address Register and 15 bit Address Register. AR-A.

This drawing shows the Initial Data Address Register IDA, the 12 bit Memory Data Address register DA and a 3 bit Data Field Address register DFA. IDA is loaded with data from the accumulator, AC, by the +ve going edge of [AR-B] ST0 IDA H. Q0 to Q2 of the IDA provide the data inputs D0 to D2 of the DFA. Q3 to Q11 of the IDA provide the data inputs D0 to D8 of the DA. The DA and DFA are preset to equal these data inputs by AR-B L0 IDA L. The DA and DFA is a 15 bit synchronous counter which is incremented (or preset to the value held in the IDA if [AR-B] L0 IDA L is asserted), by the +ve going edge of [AR-B] IA L.

4.5.2 Address Increment Logic and Computer Bus Drivers. AR-B.

The computer instruction 633I (LDCA) generates [INS2-B] BI0T 633I L and [INS2-B] LDCA L. These two signals are combined to produce ST0 IDA H, the combination being used to improve noise immunity. This signal generates L0 IDA L, and triggers the monostable LDCA CLOCK to produce a 300 nS pulse which in turn generates IA L. These signals load the IDA register with the data held in the Accumulator and preset the DA and DFA with the new contents of the IDA register on the +ve going edge of IA L. A timing diagram of this sequence of events is shown in Figure 4.8. The delays introduced by the various logic elements have not been included as their effect on the timing

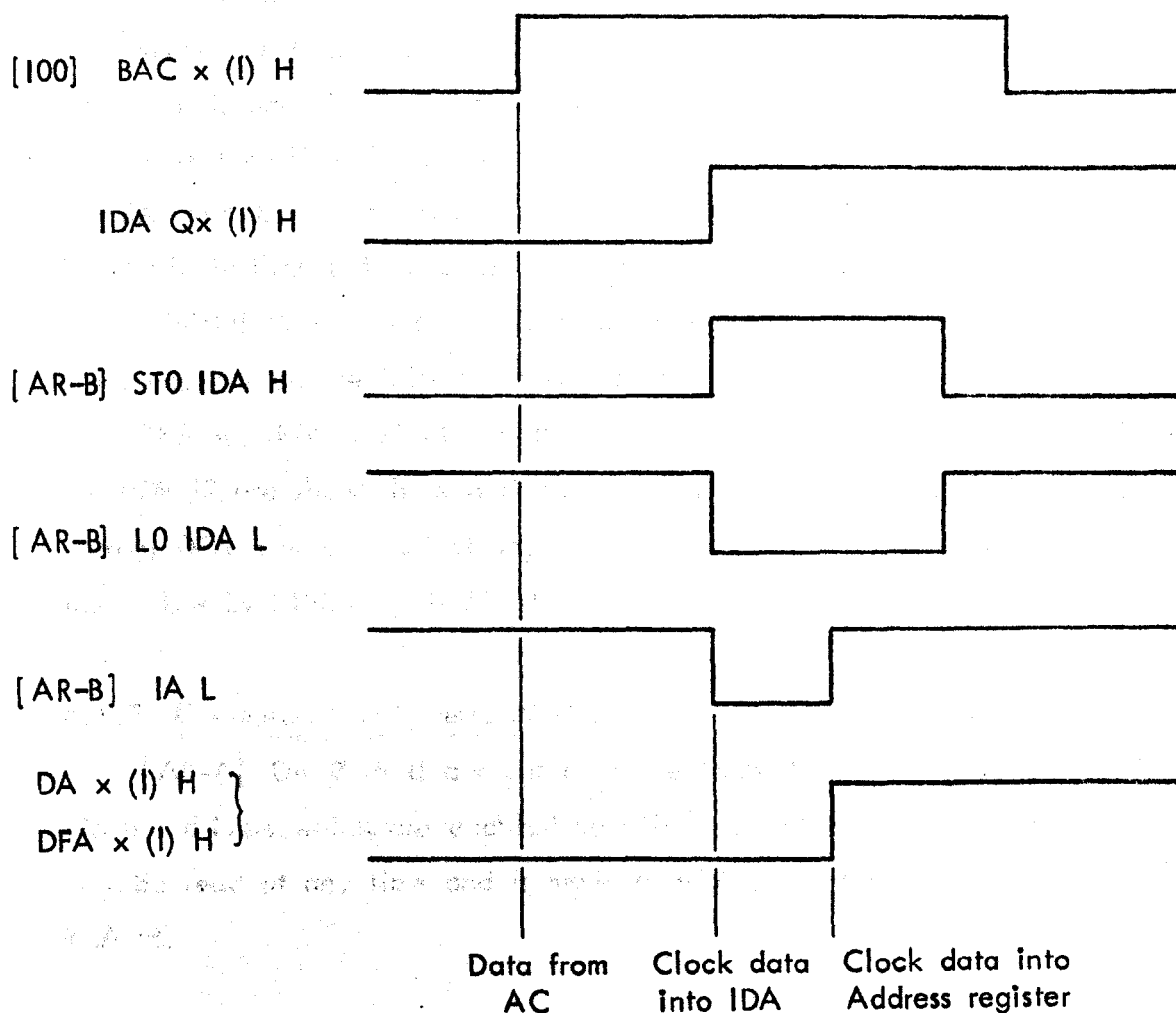


Figure 4.8. Timing diagram for loading and setting up initial address.

has been designed out. IA L is also produced by the [100] ADD ACCEPTED (I) L signal from the computer, the DA and DFA being incremented on the +ve going edge of this signal to provide the next address in computer memory in readiness for the next data transfer.

When the Double Buffer Mode of operation is set ([INS2-E] DBM (I) H) L0 IDA L is generated whenever the DA is at 7777_g and DFA bit 1 is high; on the next clock pulse (IA L) the contents of the IDA register are loaded into the DA and DFA. The timing diagram for this operation is shown in Figure 4.9. The table in Figure 4.10 shows the areas of memory used by the AR according to the initial DFA. The start location within the first memory field of these areas depends on the initial contents of the DA register.

The bus drivers which put the DFA on the Extended Data Address bus of the PDP 12 are shown here and are enabled by [INS2-C] ENFAD (I) H. The current DFA may be read at any time and is made available on bits 9-11 of the IO bus by [INS2-B] RCFA H.

4.5.3 Computer Bus Drivers. AR-C.

[AR-A] DA 0 to 11 are put onto the Data Address bus of the PDP 12 via bus drivers which are enabled by [INS2-C] ENAD (I) H. The current DA may be read at any time and is made available on the IO bus by [INS2-B] RCA H.

4.6 Calibration Control. CAL.

The drawings for the CAL are shown in Figure 4.19 CAL-A to CAL-D. Calibration initialisation and control logic is shown on CAL-A, the counter and right hand limit latch are on CAL-B, left hand limit latch and margin logic is on CAL-C, and the window control logic is on CAL-D. Three of the terms used in the description of the CAL logic are explained here. The Left Hand Limit (LHL) is the horizontal co-ordinate of the left hand edge of the calibration window. The calibration control will enable the co-ordinate generator from this point until the Right Hand limit (RHL) is reached. This limit being the horizontal co-ordinate of the right hand edge of the calibration window. The co-ordinate generator is inhibited once this RHL has been passed. The window will move

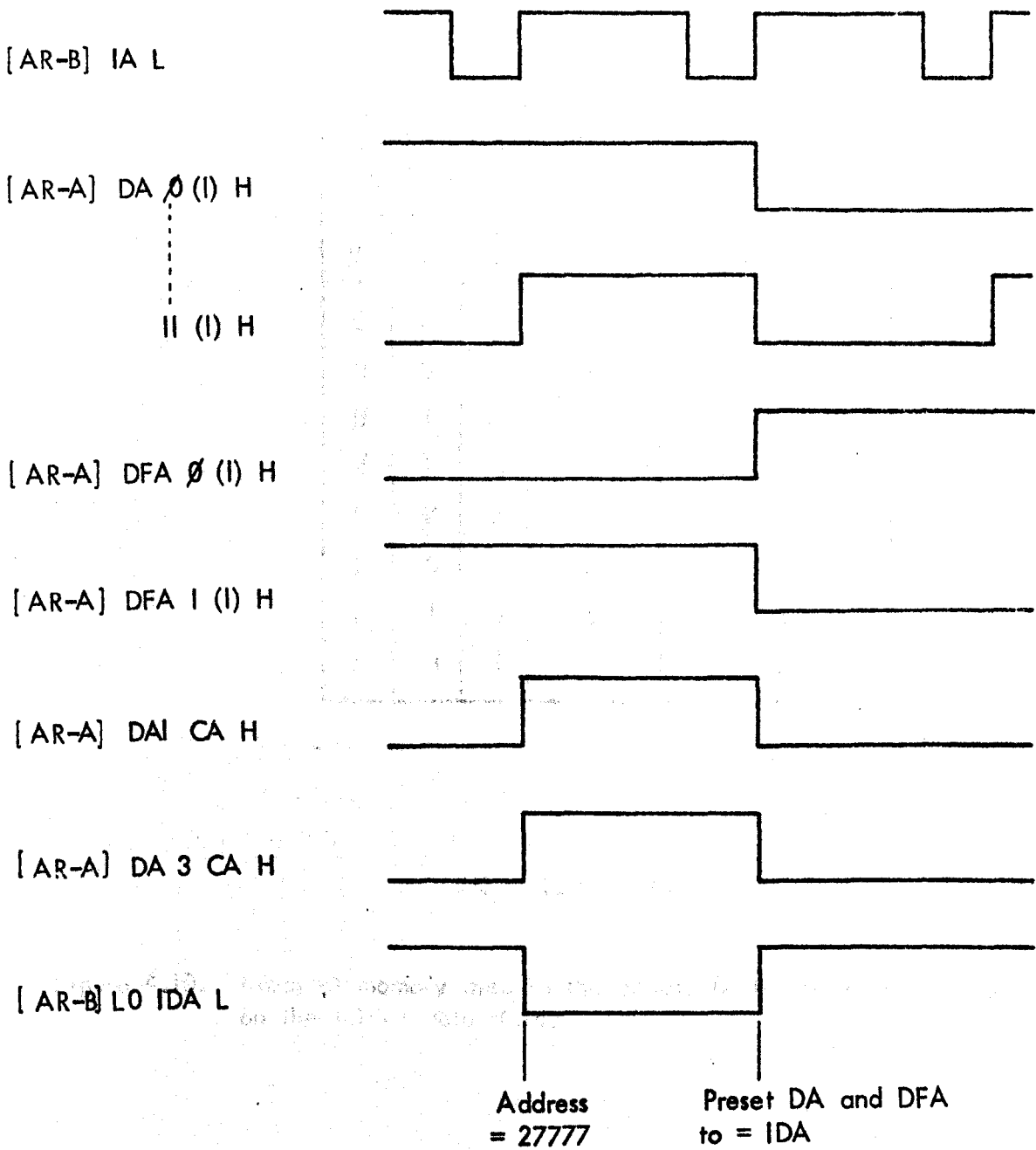


Figure 4.9. Double buffer mode timing diagram.

Initial contents of DFA			Initial data field	Area of Memory used in DBM
\emptyset	1	2		
\emptyset	\emptyset	\emptyset	0	0-12
\emptyset	\emptyset	1	1	4-12
\emptyset	1	\emptyset	2	8-12
\emptyset	1	1	3	12-16
1	\emptyset	\emptyset	4	16-28
1	\emptyset	1	5	20-28
1	1	\emptyset	6	24-28
1	1	1	7	28-32

(K = 1024 words)

Figure 4.10. Areas of memory used in the double buffer mode depending on the initial data field.

across the television field until the Right Hand Margin (RHM) is passed by the LHL, the co-ordinate generator is inhibited when this happens and the calibration complete flag is set. This moving window has already been shown in Figure 3.11.

4.6.1 Calibration Initialisation and Control Logic. CAL-A.

The calibration control is initiated by [INSI-B] CALG0 H, which triggers the CAL G0 monostable to generate a 45 mS pulse. The START and ZERO LHL flip flops are cleared and then reset as shown in the timing diagram of Figure 4.11. These flip flops set up the initial conditions for the calibration control. START (0) L generates DISCG L and inhibits the CG until the beginning of a television field. ZERO LHL (0) L makes the Left Hand Limit zero; this condition may also be generated when the control is in the Free Run mode ([INSI-B] FR (1) L) and [CAL-C] RHM CONT L is asserted. To indicate that calibration has been completed CAL COMP H is generated by [CAL-C] RHM STOP L.

The calibration control is made operative by [INSI-B] CAL MODE (1) H and will inhibit the Co-ordinate Generator when DISCG L is asserted. This inhibit signal may be generated by any of the four signals shown and will control the times during the line scan in which co-ordinates may be generated. When a co-ordinate is being generated [CG-B] BGC (1) L is asserted and will clock a '1' into the shift register, CAL SR, on its +ve going edge (i.e. at the end of co-ordinate generation). CAL SR Q_B provides the 'D' input to the SET RHL flip flop which is clocked by bit 9 of the horizontal co-ordinate counter [CG-C] BHC9 H. The timing diagram of Figure 4.12 shows how SET RHL (1) H is asserted. CAL SR and the SET RHL flip flop are both cleared by FSYB L.

4.6.2 Counter and Right Hand Limit Latch. CAL-B.

The calibration control has its own horizontal counter, HC, which is incremented by [CG-C] BHC9 H. The right hand limit latch, RHL LATCH, stores the current contents of the HC when [CAL-A] SET RHL (1) H is asserted. At the start of alternate television fields the RHL is set to all '1's by [CAL-A] FSYB L.

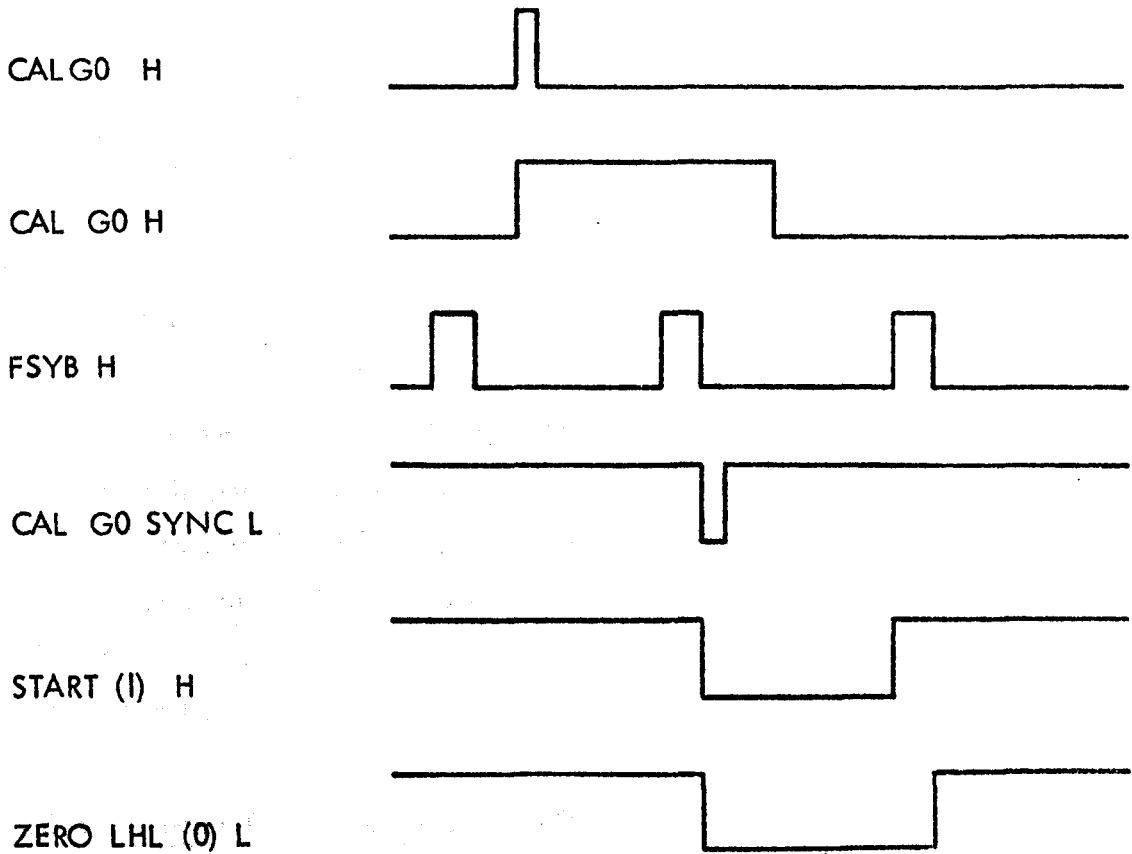


Figure 4.11. Initialisation of calibration control timing diagram.

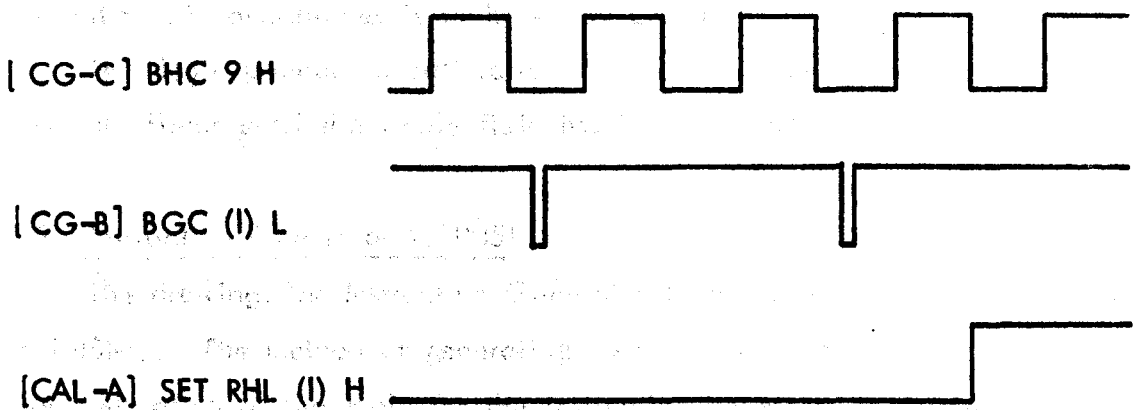


Figure 4.12. Timing diagram to set the right hand limit of the calibration window.

4.6.3 Left Hand Limit Latch and Margin Logic. CAL-C.

The left hand limit latch, LHL LATCH, is loaded with the contents of the RHL LATCH at the start of alternate television fields by the leading edge of [SGS-A] FSYB H. The timing of this operation is shown in Figure 4.13, in which typical propagation times are given for the various signals (from Texas Instruments (1973b)). The data for the LHL LATCH inputs must be held for 5 nS from the +ve edge of the clock signal [SGS-A] FSYB H as shown. The LHL LATCH is set to zero at the start of calibration by [CAL-A] ZERO LHL (0) L. The contents of the LHL LATCH are compared with the contents of the right hand margin register, CAL RHM REG, and as soon as the left hand limit is greater than or equal to the right hand margin RHM STOP L is generated to inhibit further co-ordinate generation. The RHM REG is loaded with the contents of bits 0 - 3 of the accumulator by [INSI-B] LDCAM H. When the calibration control is in the free run mode [INSI-B] FR (1) L inhibits RHM STOP L.

4.6.4 Window Control Logic. CAL-D.

The right hand limit comparator, RHL COMP, compares the contents of the RHL LATCH (A) with the outputs of the horizontal counter [CAL-B] HC 0-7 (B), and when A is less than B, RHL L is generated to inhibit the CG. Similarly the left hand limit comparator LHL COMP compares the contents of the LHL LATCH (A) with [CAL-B] HC 0-7 (B), and as long as A is less than B generation of co-ordinates is inhibited by LHL L.

This logic provides a calibration "window" whose limits are reset on alternate fields until the whole field has been covered.

4.7 Instruction Generator I. INSI.

The drawings for Instruction Generator I are shown in Figure 4.20 INSI-A to INSI-C. The method of generating instructions is described in Appendix A2. The description of the logic for the instruction generator is limited here to a list of the instructions in the form of the octal code for each instruction followed by its function. The signal or combination of signals required to generate the function is shown after the function description.

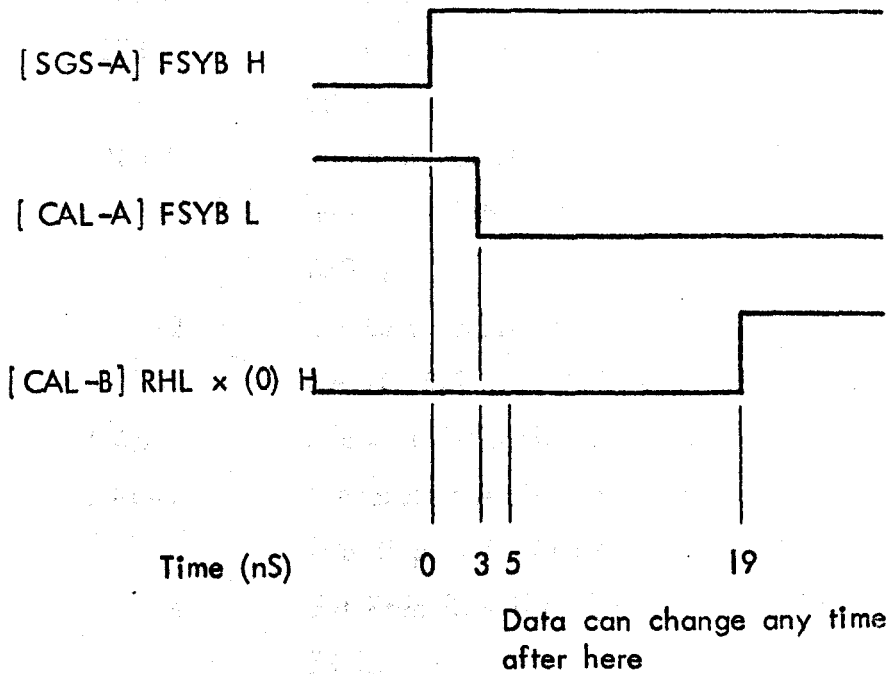


Figure 4.13. Timing diagram for loading the left hand limit latch.

4.7.1 Calibration Control Instructions. INSI-A and INSI-B.

The device selector for the 630X (Calibration Control) instructions is shown on INSI-A together with the appropriate buffering for [100] BMB 9-11, and [100] BA INITIALIZE H. The decoding and associated logic for this set of instructions is shown on INSI-B as follows:-

6301	Set MON EN flip flop. [INSI-A] IOT 6301 L.
6302	Load CAL RHM REG with bits 0-3 of the accumulator. LDCAM H.
6303-1	Generate CALGO H to start calibration, clear CC flag, and set MON EN flip flop. CAGO L and [INSI-A] IOT 6301 L.
-2	Set CAL MODE flip flop, which also enables the interrupt. CAGO SET CAL MODE L.
6304	Clear all flip flops. CLR L
6305-1	Generate CALGO H to start calibration, set MON EN flip flop. CAGO FR L and [INSI-A] IOT 6301 L.
-4	Set Free Run flip flop and clear CC FLAG. CAGO FR SET FR L.
6306	Clear CC FLAG. [INSI-A] IOT 6304 L.
6307-1	Set MON EN flip flop. [INSI-A] IOT 6301 L.
-2	Skip if calibration has been completed. SCAC H.
-4	Clear CC FLAG. [INSI-A] IOT 6304 L.

[INSI-A] IO INIT L also clears all the flip flops.

4.7.2 Sync Generator and Simulator Instructions. INSI-C.

The device selector and decoding with associated logic for the 631X instructions is shown here as follows:-

6311	Load Field Count Register FCR with bits 4-11 of the accumulator. LDFC H.
6312	Load Line Count Register LCR with bits 4-11 of the accumulator. LDLC H.

- 6313-1 Field Count Mode; sets Enable Field Count flip flop ENFC, which allows [SGS-B] ENCG to be controlled by the contents of the FCR. ENFC L.
- 6314 Set Enable Simulator flip flop ENSM. ENSM L.
- 6315-1 Clear ENFC. CFC L.
- 6316-2 Clear ENSM. CSM L.

[INSI-A] IO INIT L also clears ENFC and ENSM.

4.8 Instruction Generator 2. INS2.

The drawings for Instruction Generator 2 are shown in Figure 4.21 INS2-A to INS2-E. The method of instruction generation and the presentation of the logic description is the same as for Instruction Generator 1.

4.8.1 Co-ordinate Generator Instructions. INS2-A.

The device selector and decoding for the 632X instructions is shown here, together with the appropriate buffering for [100] BMB 9-11. These instructions are as follows:-

- 6321 Enable CG, sets ENCG flip flop. ENCG L.
- 6322 Disable CG, clears ENCG. DISCG L.

[INS2-A] IO INIT L also clears ENCG.

4.8.2 Address Register Instructions INS2-B to INS2-E.

The device selector for the 633X instruction set is shown on INS2-B together with the decoding and logic for some of the instructions as follows:-

- 6333-1 Load Initial Data Address register IDA with the contents of the accumulator LDCA L.
- 2 Clear Change Buffer Half flags, FLAG 1 and FLAG 2 (on INS2-E). LDCA CL FL L.
- 6335-1 Clear accumulator. RCA CLA L.
- 4 Read current Memory data Address into the accumulator. RCA H.

- 6336-2 Clear accumulator. RCFA CLA L.
 -4 Read current data Field Address into the accumulator.
 RCFA H.

The decoding for the rest of the 633X instructions is shown on INS2-C as follows:-

- 6331 Set ENAD flip flop to enable Memory data Address onto
 address bus. ENAD L.
 6332 Set ENFAD flip flop to enable data Field Address onto
 field address bus. ENFAD L.
 6334 Clear ENAD and ENFAD flip flops. CLAD L.

[INS2-A] 10 INIT L also clears ENAD and ENFAD.

The device selector for the 634X instructions is shown on INS2-D together with some of the decoding. These instructions are also associated with the Address Register as follows:-

- 6341 Set Double Buffer Mode flip flop, DBM. DBM L.
 6342 Clear DBM. CDBM L.
 6343-1 Enable Double Buffer Mode interrupt, DBI. ENDBI L.
 6344 Disable DBI. CDBI L.
 6345-1 Skip if Change Buffer Half Flag is set, FLAG 1 or FLAG 2
 (on INS2-E). SCBH H.
 -4 Clear FLAG 1 and FLAG 2. SCBH CL FL L.

The decoding for the rest of the 634X instructions is shown on INS2-E together with the interrupt and skip logic for the Double Buffer Mode of operation. The following instructions are included here:-

- 6346-2 Skip if Upper Buffer Half is being filled. SUBH H.
 6347-1 Clear FLAG 1 and FLAG 2. CDBF L.

[INS2-A] 10 INIT L also clears FLAG 1, FLAG 2, DBM and DBI.

FLAG 1 is set when data Field Address bit 1 changes from \emptyset to 1 and FLAG 2 is set when data Field Address bit 2 changes from \emptyset to 1.

4.9 Construction and Planning

Texas Instruments 7400 series TTL logic is used throughout the interface with the exception of the random access memories which are Signetics 8000 series. The technical specifications for the devices used will be found in Texas Instruments (1973b) and Signetics (1974). The integrated circuits (I C's) are mounted on "Veroboard" and all interconnections are hand wired. The layout of the I C's was designed to minimise the length of the interconnections. Two methods of describing the wiring were used. The first consisted of a series of drawings showing the positions of the I C's and the interconnections between them, a separate drawing being provided for each colour of wire used. This method allowed the position of each wire to be specified precisely so that some attempt to minimise critical cross talk effects could be made. The wiring for the Co-ordinate Generator and the Sync Generator and Simulator was described in this way. The second method of wiring description was simply to provide a list of interconnections as shown in Figure 4.14. A drawing of the I C layout and pin configurations was also provided. The technician then made the appropriate connections keeping wire lengths as short as possible. Checking of the wiring was carried out before the components were inserted by testing for continuity between the specified interconnected points.

De-coupling capacitors ($0.1\mu\text{F}$ ceramics) were connected between the +ve supply and ground on at least every other I C. These de-coupling capacitors were also provided for every I C that had a clock input, (such as D type flip flops etc.), to reduce the possibility of the devices being clocked by noise on the power lines. $47\mu\text{F}$ electrolytic capacitors were also mounted on each board for de-coupling. Unused inputs to logic elements were held at the appropriate logic level, or paralleled to other inputs. For low logic levels the input was connected direct to ground, but where high levels were required these were provided by the output of an unused gate, this method providing the best noise immunity.

PIN A	PIN B	Colour	No A	No B	Comments	Signal Name
A1-6	A2-6	Green	2	2		BMB 3 (0) H
A1-5	A2-5	"	2	2		BMB 4 (1) H
A1-4	A2-4	"	2	2		BMB 5 (1) H
PIN 71	A2-12	"	2	1		BMB 6 (0) H
PIN 74	A2-11	"	2	1		BMB 7 (1) H
PIN 75	A2-1	"	2	1		BMB 8 (0) H
A2-8	B6-13	Green	1	2		632 Select L
B6-13	B6-12	Wire	2	1		" " L
B6-11	B6-10	Wire	1	2		632 Select H
B6-10	B6-5	Green	2	2		" " H
B6-5	B6-2	"	2	1		" " H
PIN 55	B6-9	Yellow	2	2	A side	BIOP 1 H
PIN 57	B6-4	"	2	2	"	BIOP 2 H
PIN 59	B6-1	"	2	2	"	BIOP 4 H
B6-6	B7-5	Yellow	1	1	A side	IOT 6322 L
B6-3	B7-3	"	1	1	"	IOT 6324 L
B6-8	B7-9	"	1	1	"	IOT 6321 L
B7-8	B8-13	Yellow	1	1	A side	IOT 6321 H
B7-6	B8-5	"	1	1	"	IOT 6322 H
B8-12	A8-10	Yellow	1	1	A side	ENCG L
B8-6	A7-5	"	1	1	"	DISCG L
A7-6	A8-13	White	1	1		Clear ENCG L
B2-8	A7-4	"	1	2		IO INIT L
1	2	3	4	5	6	7

Columns 1 and 2 give the pin numbers to be interconnected.

Column 3 gives the wire colour to be used

Columns 4 and 5 give the number of connections on each pin.

Figure 4.14. Extract from the wiring list for Instruction Generator 2.

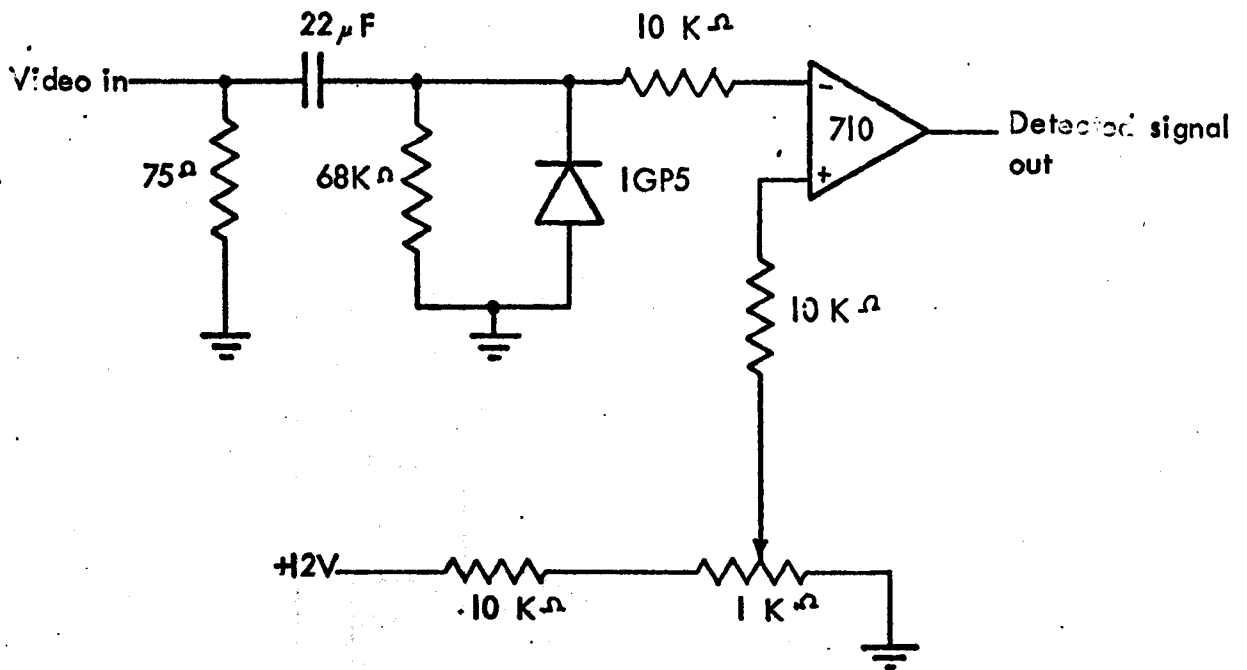


Figure 4.15. Comparator circuit used to generate TTL compatible field and line synchronising pulses from standard sync pulse generator syncs.

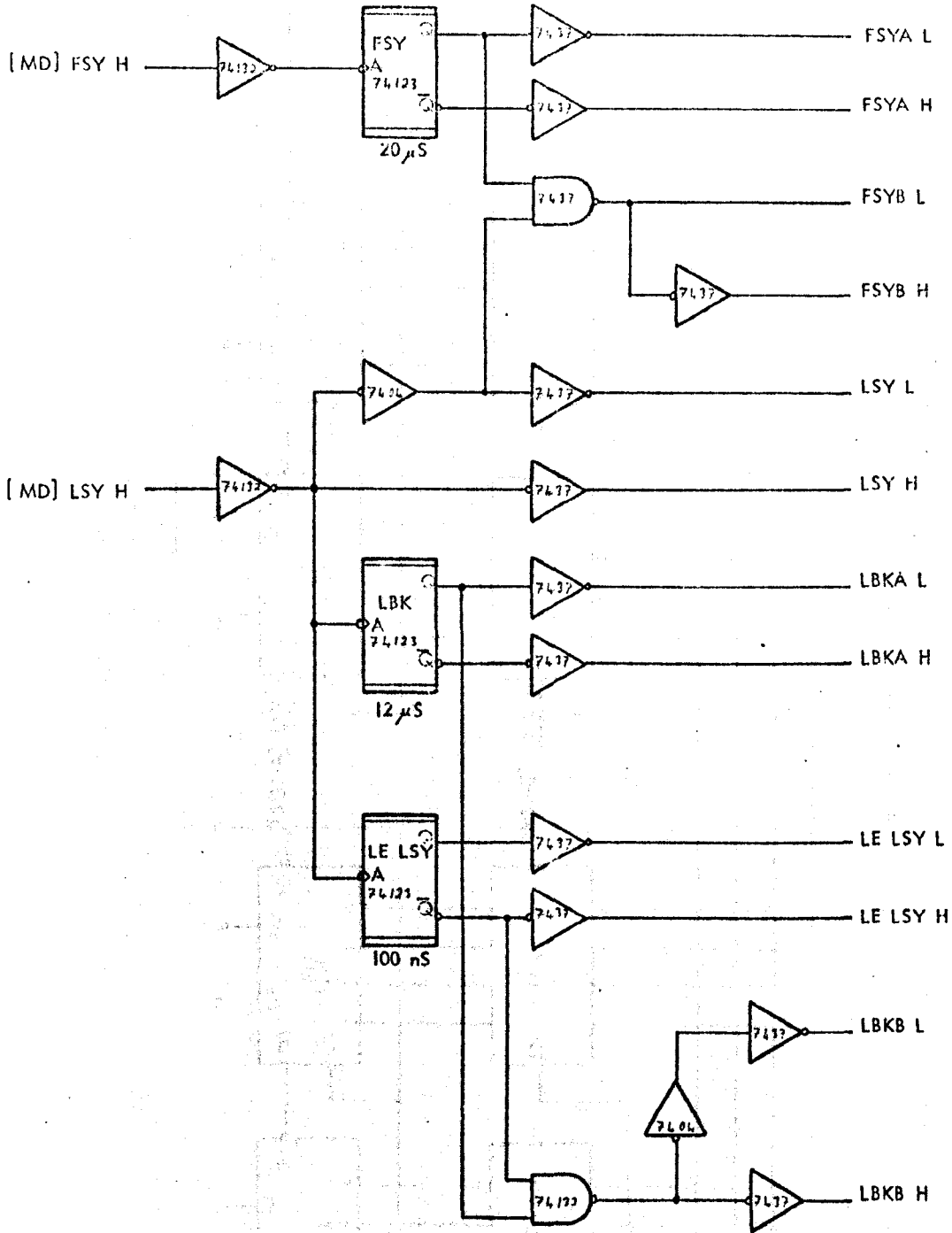


Figure 4.16 SGS-A. Sync generator and simulator - internal syncs.

Drawing Number SGS-A
SYNC GENERATOR AND SIMULATOR
Internal Syncs

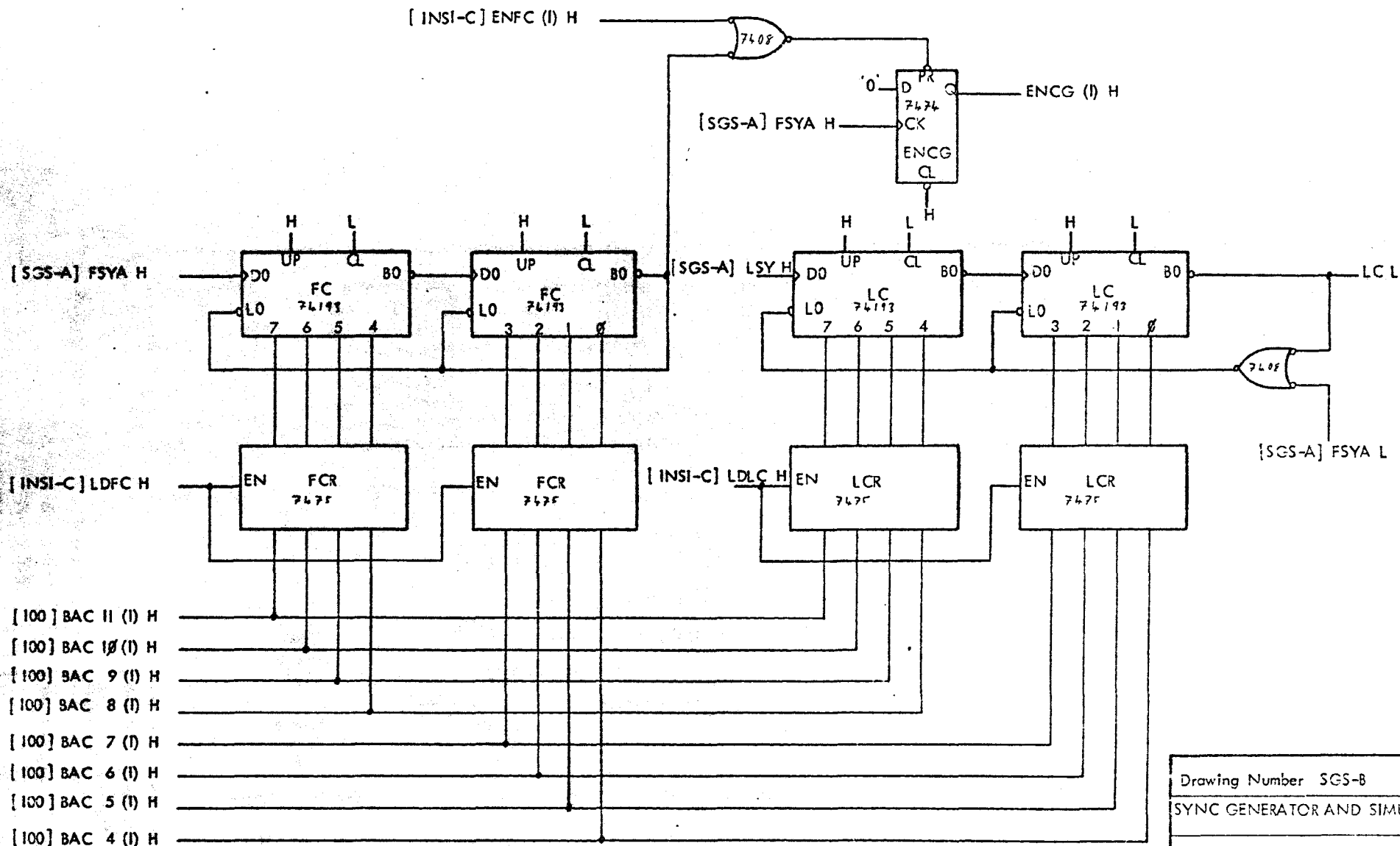


Figure 4.16 SGS-B. Sync generator and simulator - line count and field count functions.

Drawing Number	SGS-B
SYNC GENERATOR AND SIMULATOR	
Line Count and Field Count Functions	

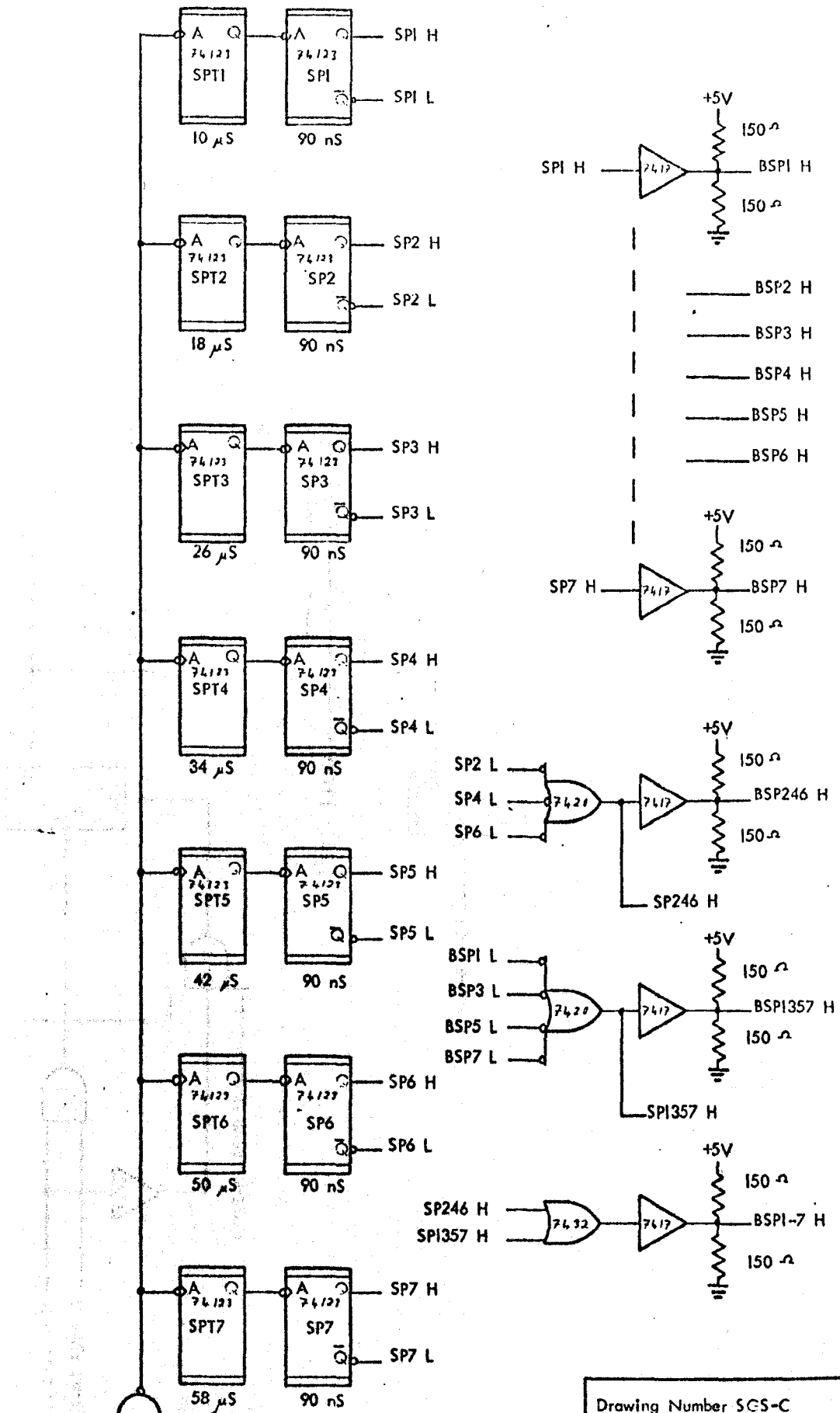


Figure 4.16 SGS-C. Sync Generator and Simulator - the simulator.

Drawing Number SGS-C
SYNC GENERATOR AND SIMULATOR
The Simulator

SI-C] ENSM (I) H
[SGS-B] LCL

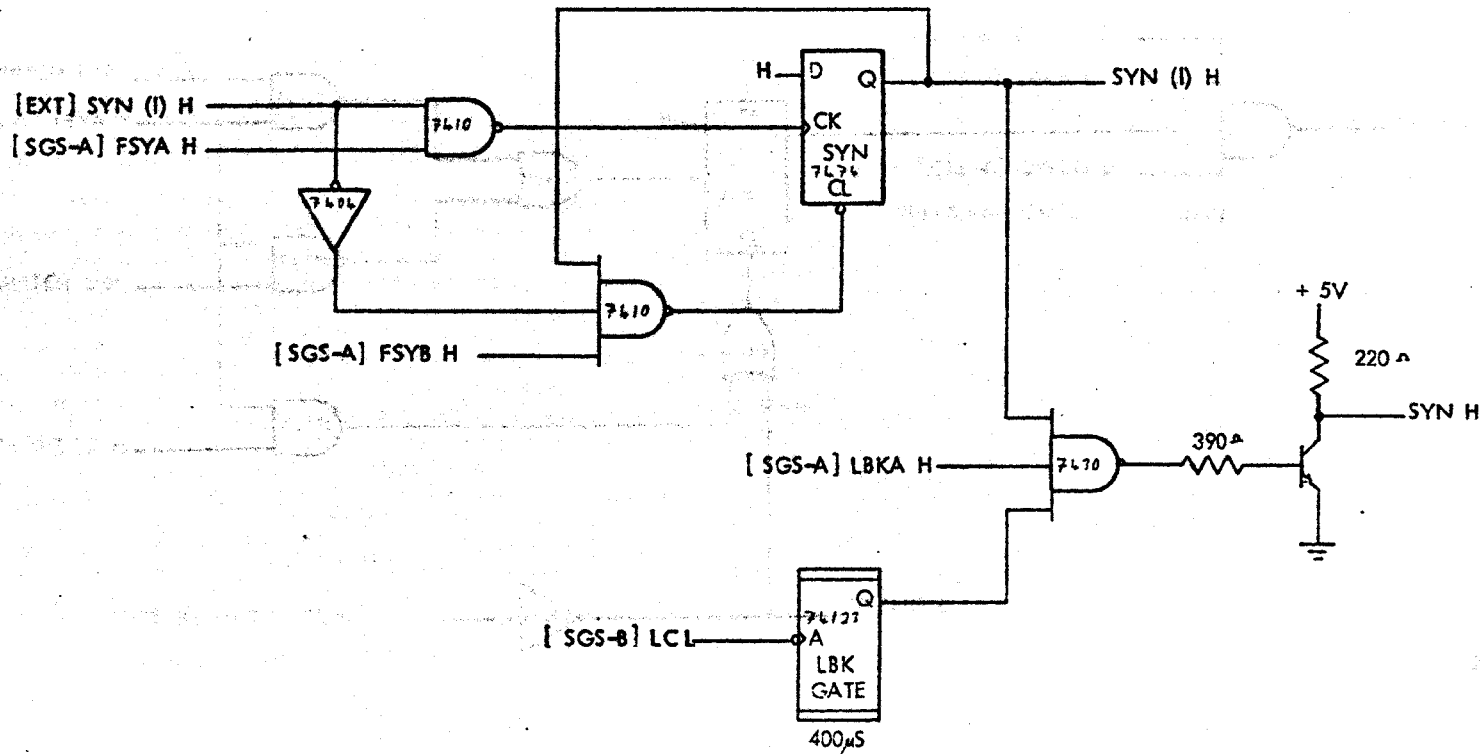


Figure 4.16 SGS-D. Sync generator and simulator - external synchronisation and sync pulse generator.

Drawing Number SGS-D
SYNC GENERATOR AND SIMULATOR
External Synchronisation and Sync Pulse Generator

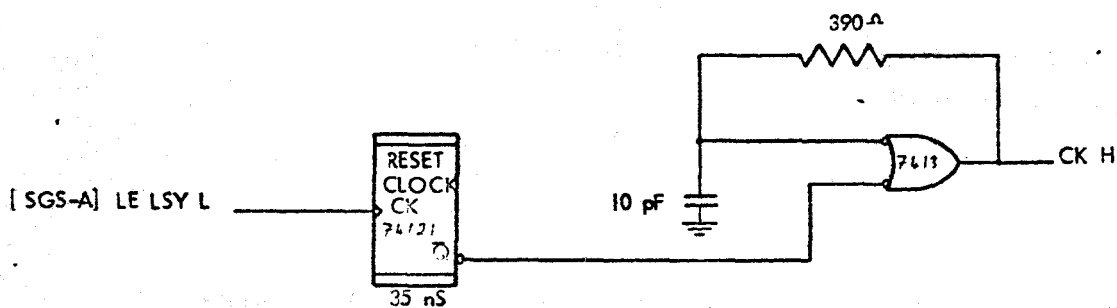
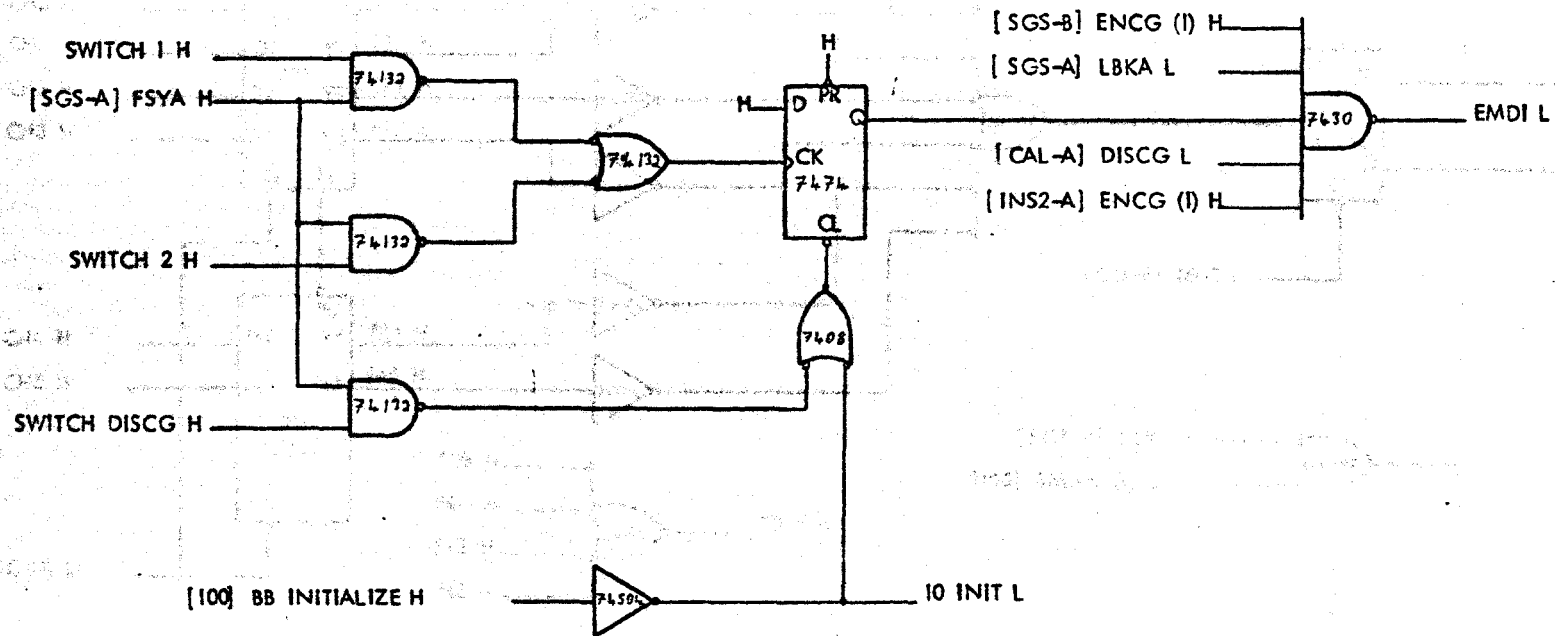


Figure 4.17 CG-A. Co-ordinate generator - enable logic and clock.

Drawing Number CG-A
CO-ORDINATE GENERATOR
Enable Logic and Clock

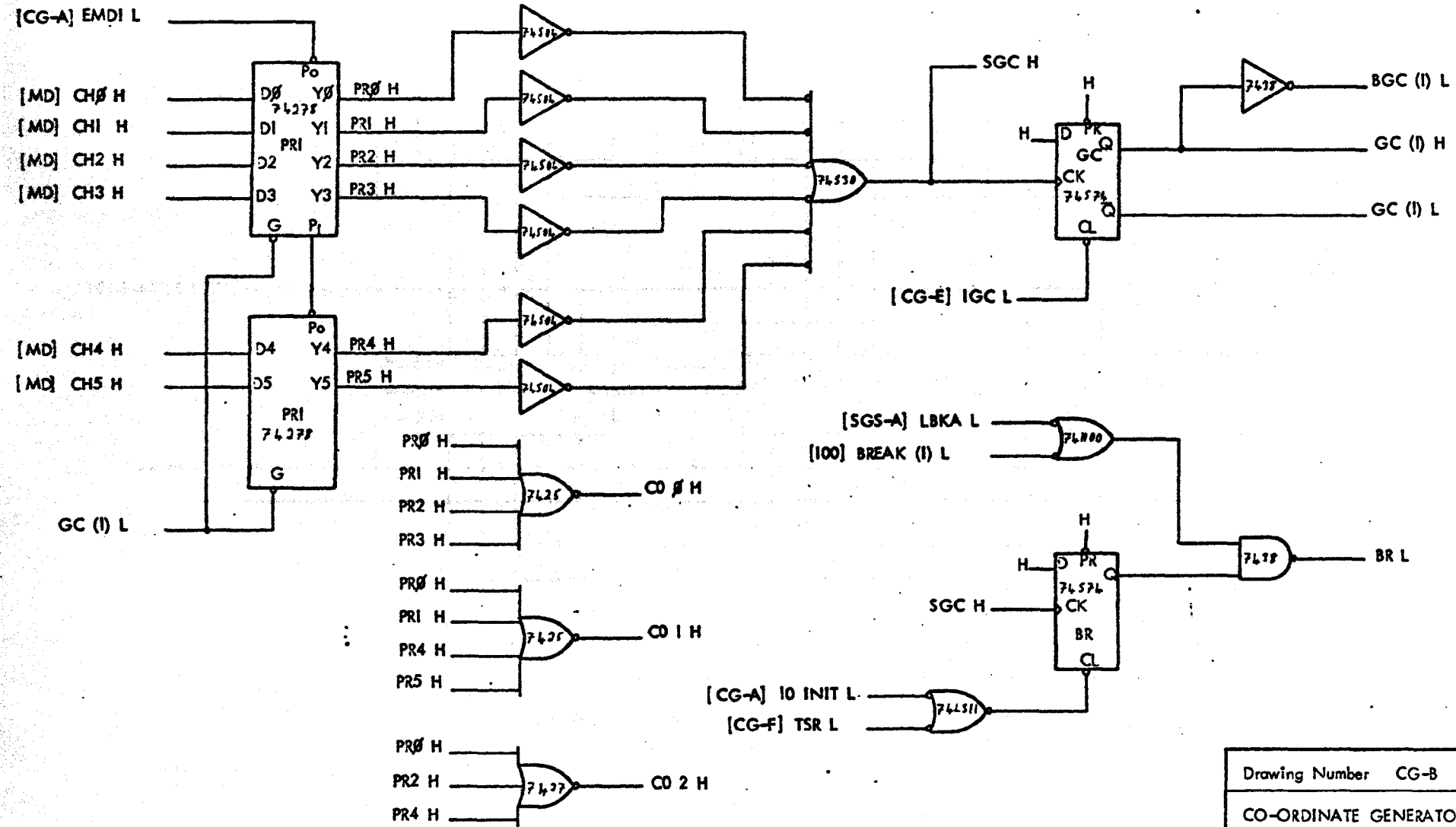


Figure 4.17 CG-B. Co-ordinate generator - priority register and coding.

Drawing Number	CG-B
CO-ORDINATE GENERATOR	
Priority Register and Coding	

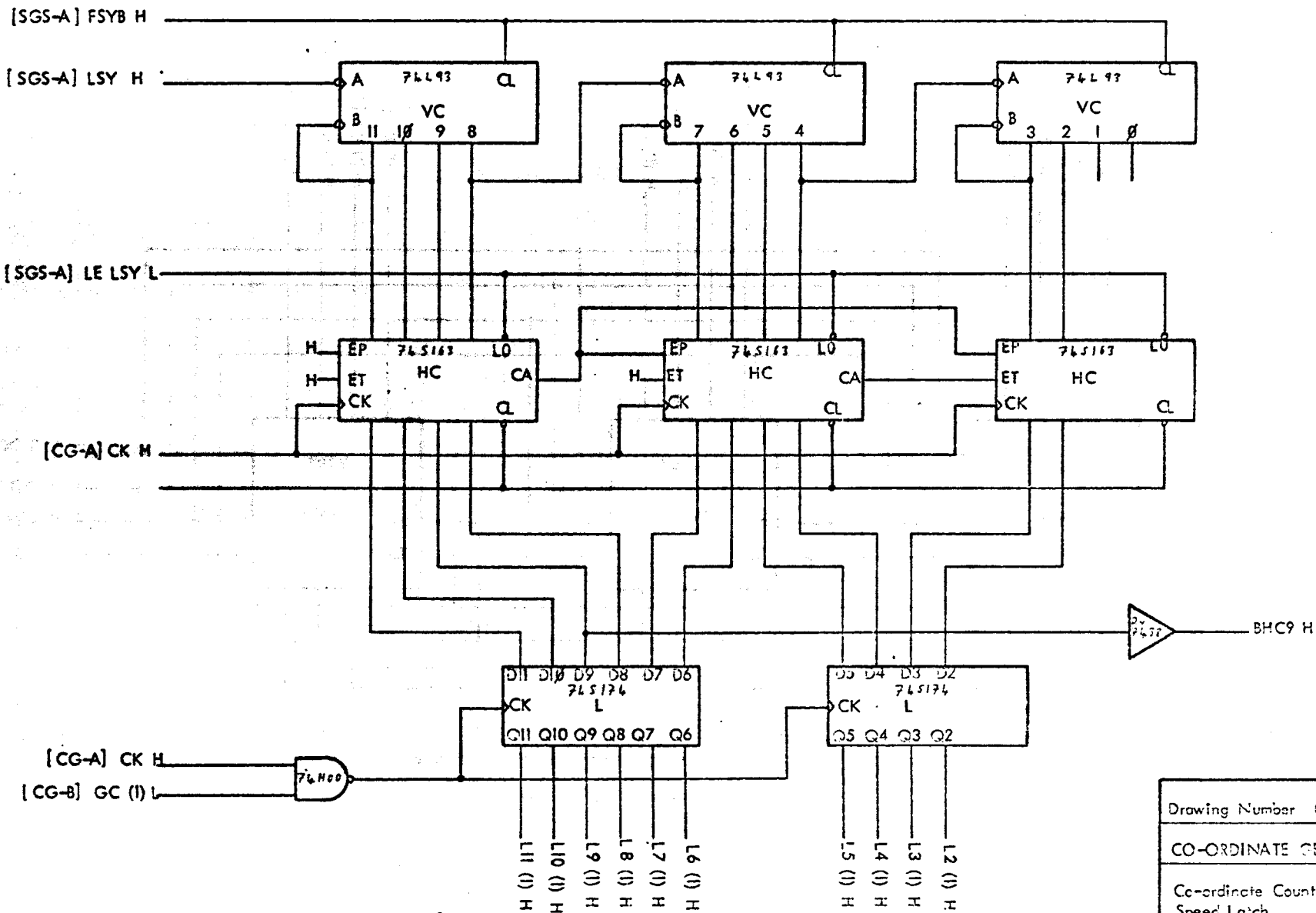


Figure 4.17 CG-C. Co-ordinate generator - co-ordinate counters and high speed latch.

Drawing Number	CG-C
CO-ORDINATE GENERATOR	
Co-ordinate Counters and High Speed Latch	

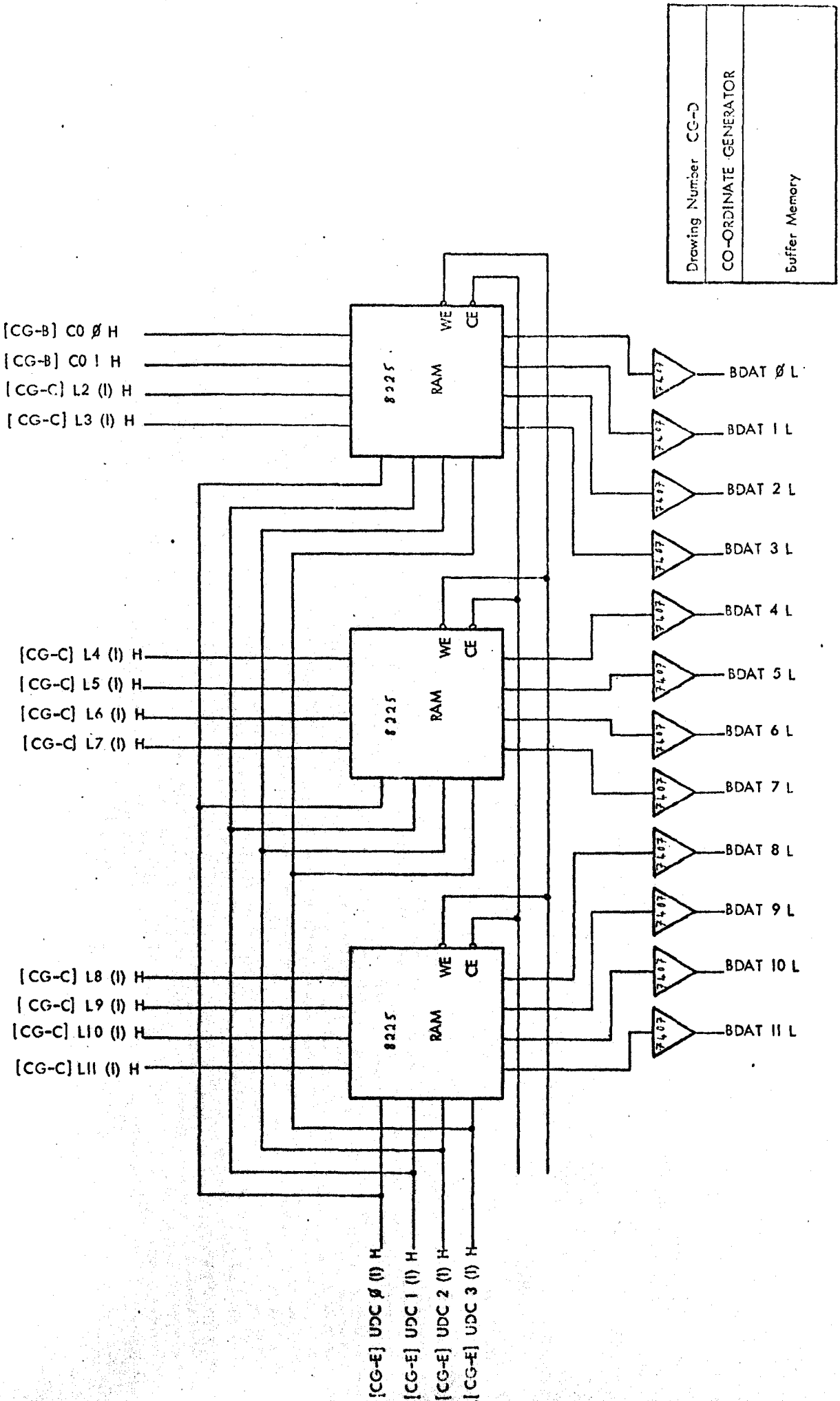


Figure 4.17 CG-D. Co-ordinate generator - buffer memory.

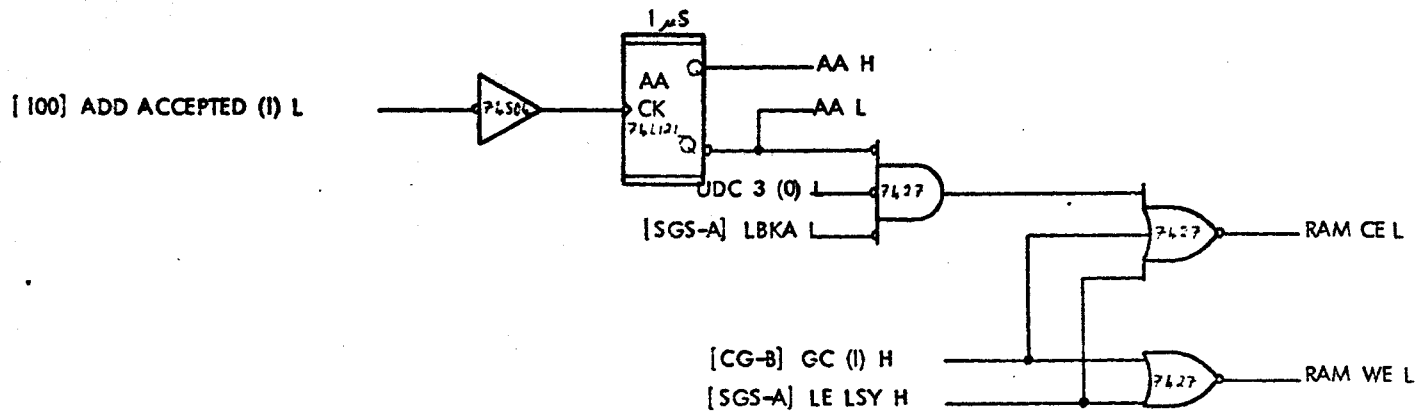
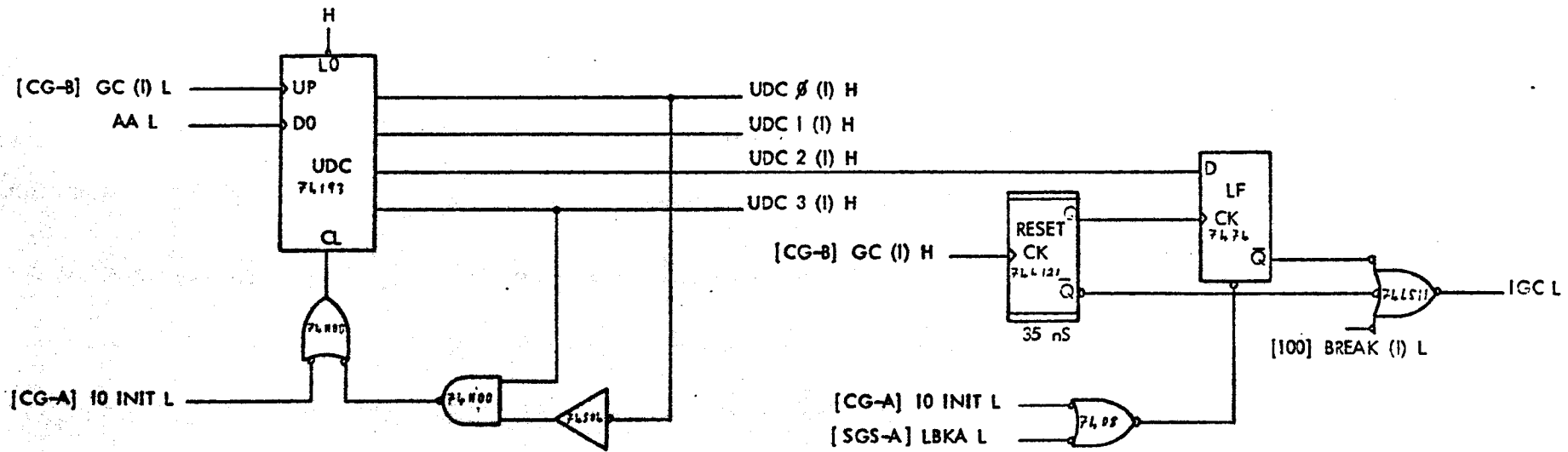


Figure 4.17 CG-E. Co-ordinate generator - buffer memory address and control logic.

Drawing Number CG-E
CO-ORDINATE GENERATOR
Buffer Memory Address and Control Logic

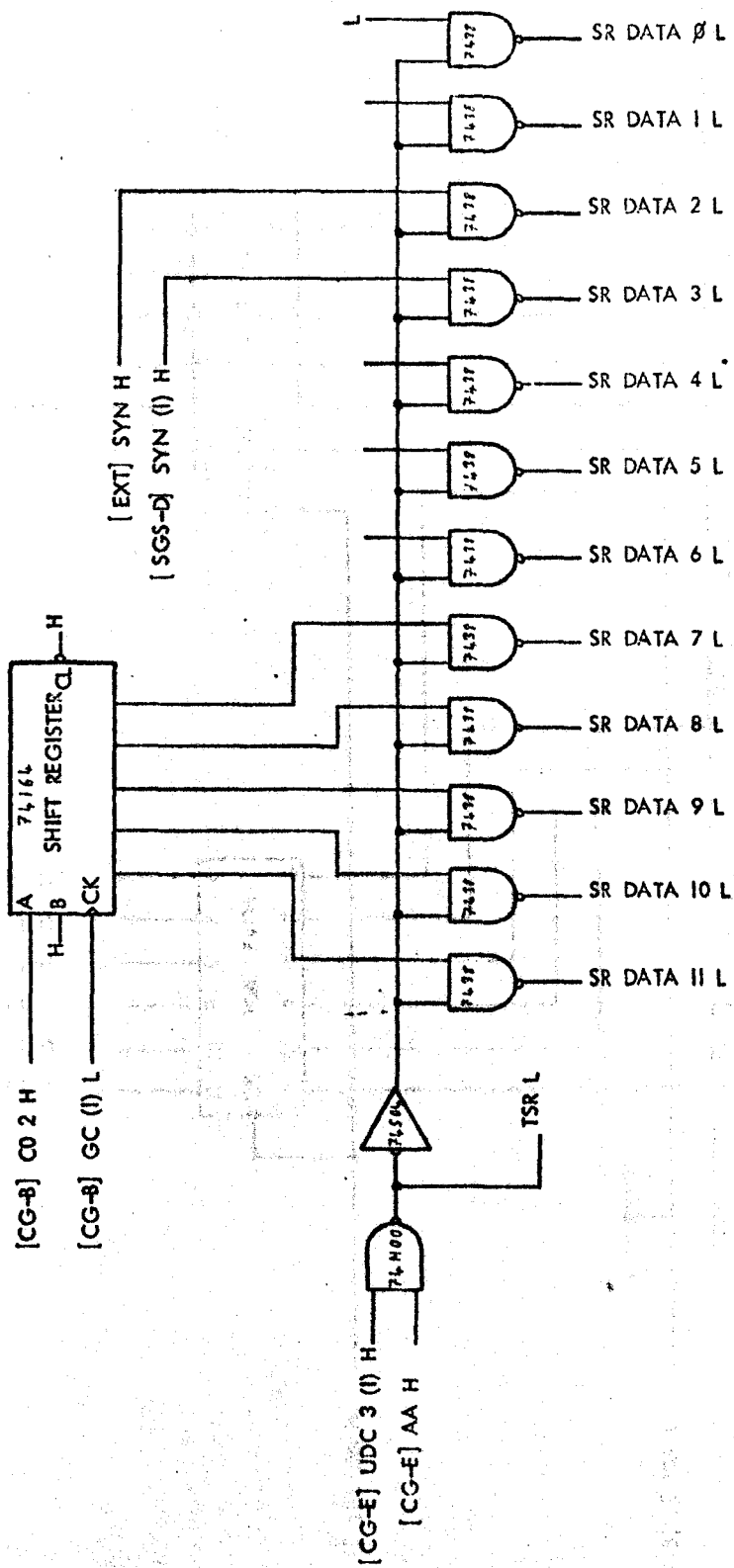


Figure 4.17 CG-F. Co-ordinate generator - additional code bit.

Drawing Number	CG-F
CO-ORDINATE GENERATOR	
Additional Code Bit	

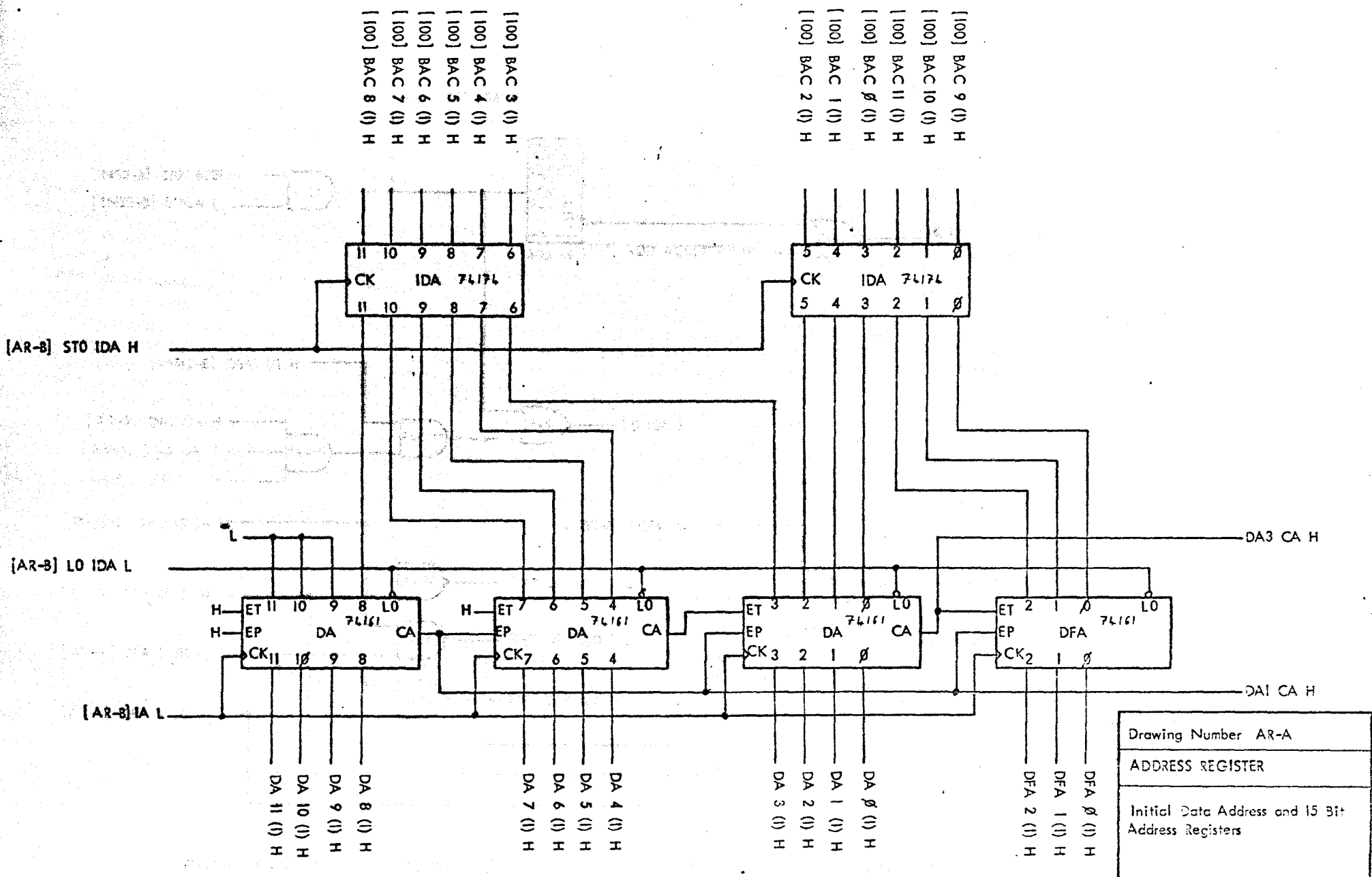


Figure 4.18 AR-A. Address register - initial data address and 15 bit address registers.

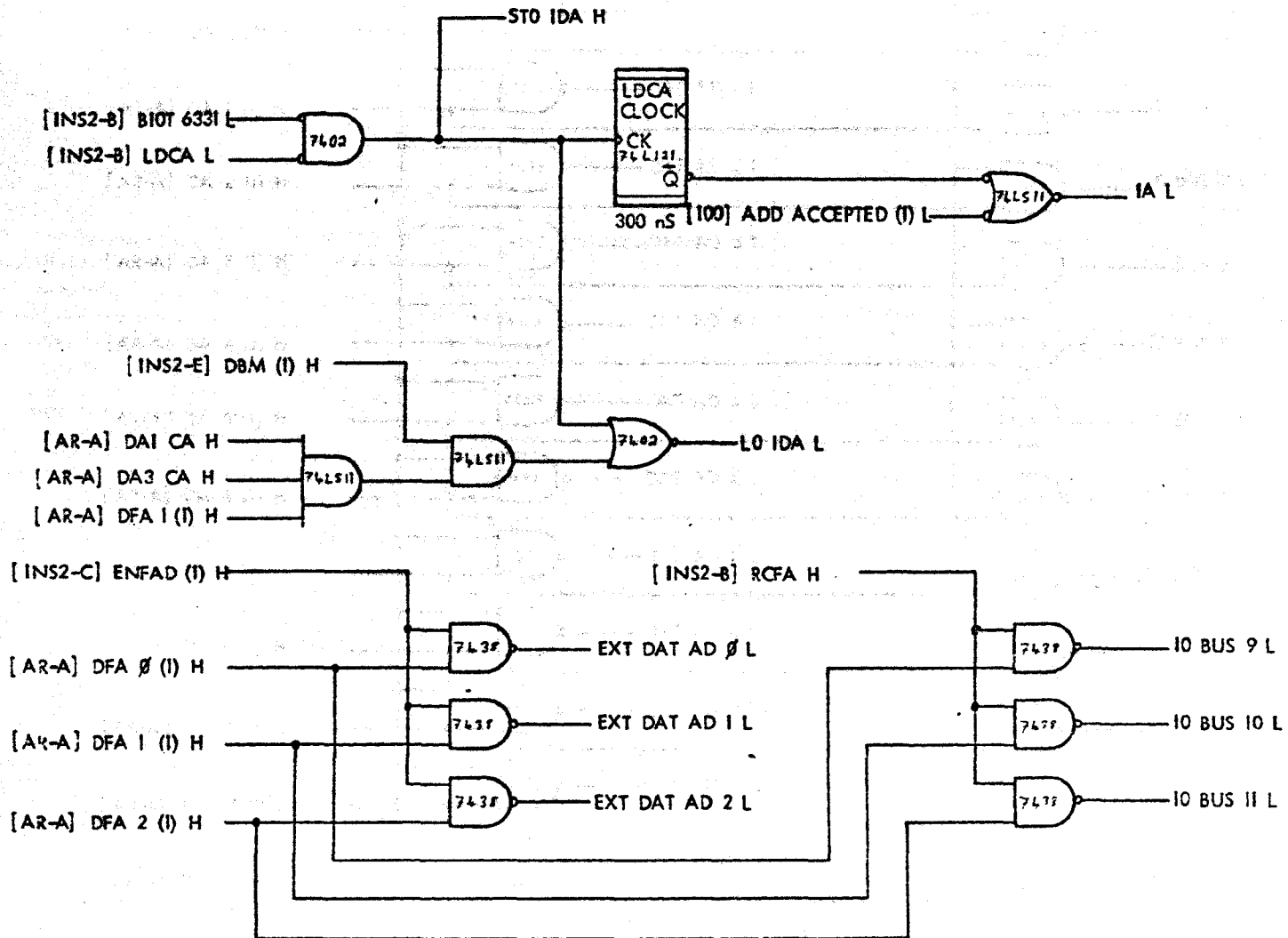


Figure 4.18 AR-B. Address register - address increment logic and computer bus drivers.

Drawing Number AR-B
ADDRESS REGISTER
Address Increment Logic and Computer Bus Drivers

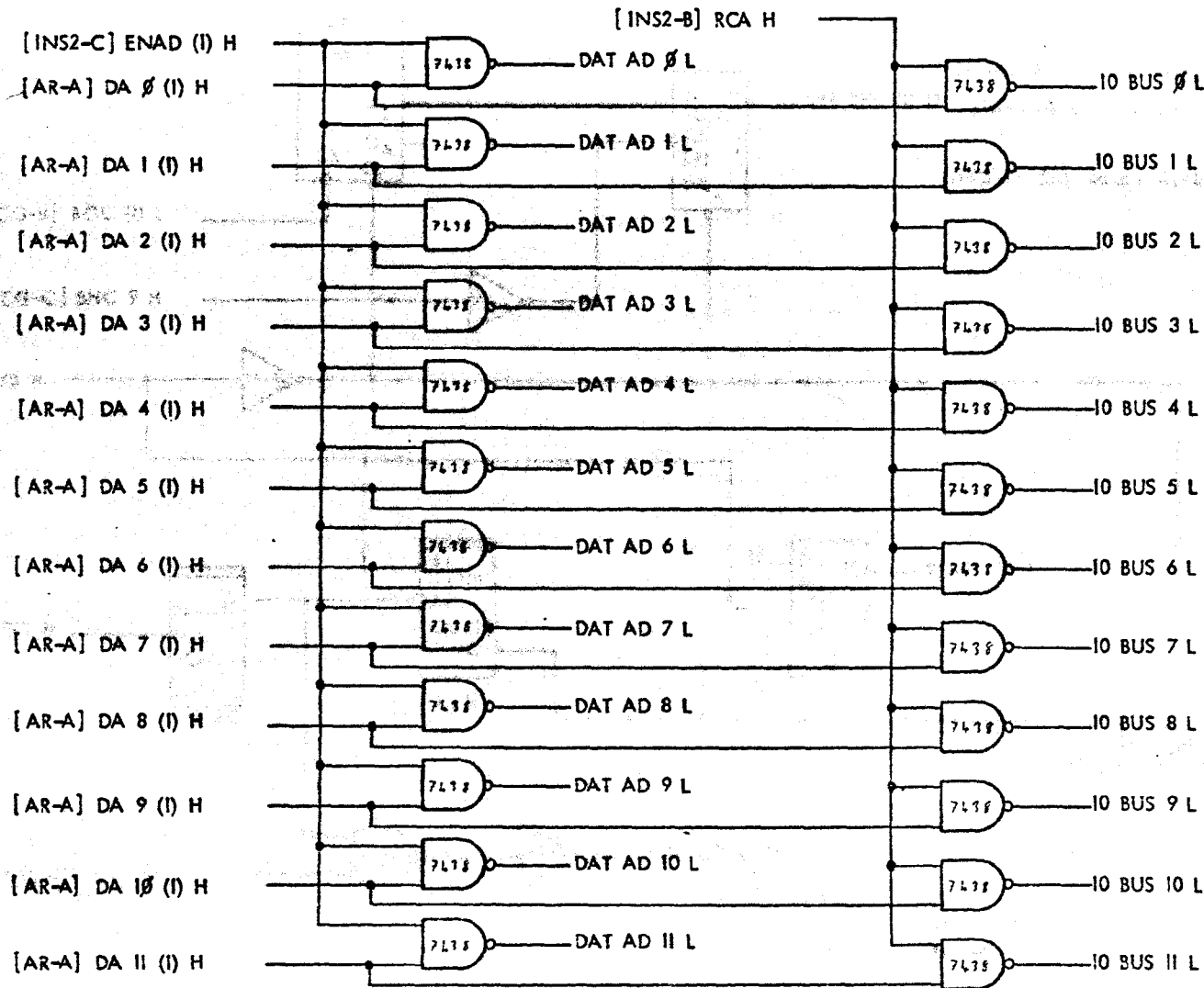


Figure 4.18 AR-C. Address register - computer bus drivers.

Drawing Number AR-C
ADDRESS REGISTER
Computer Bus Drivers

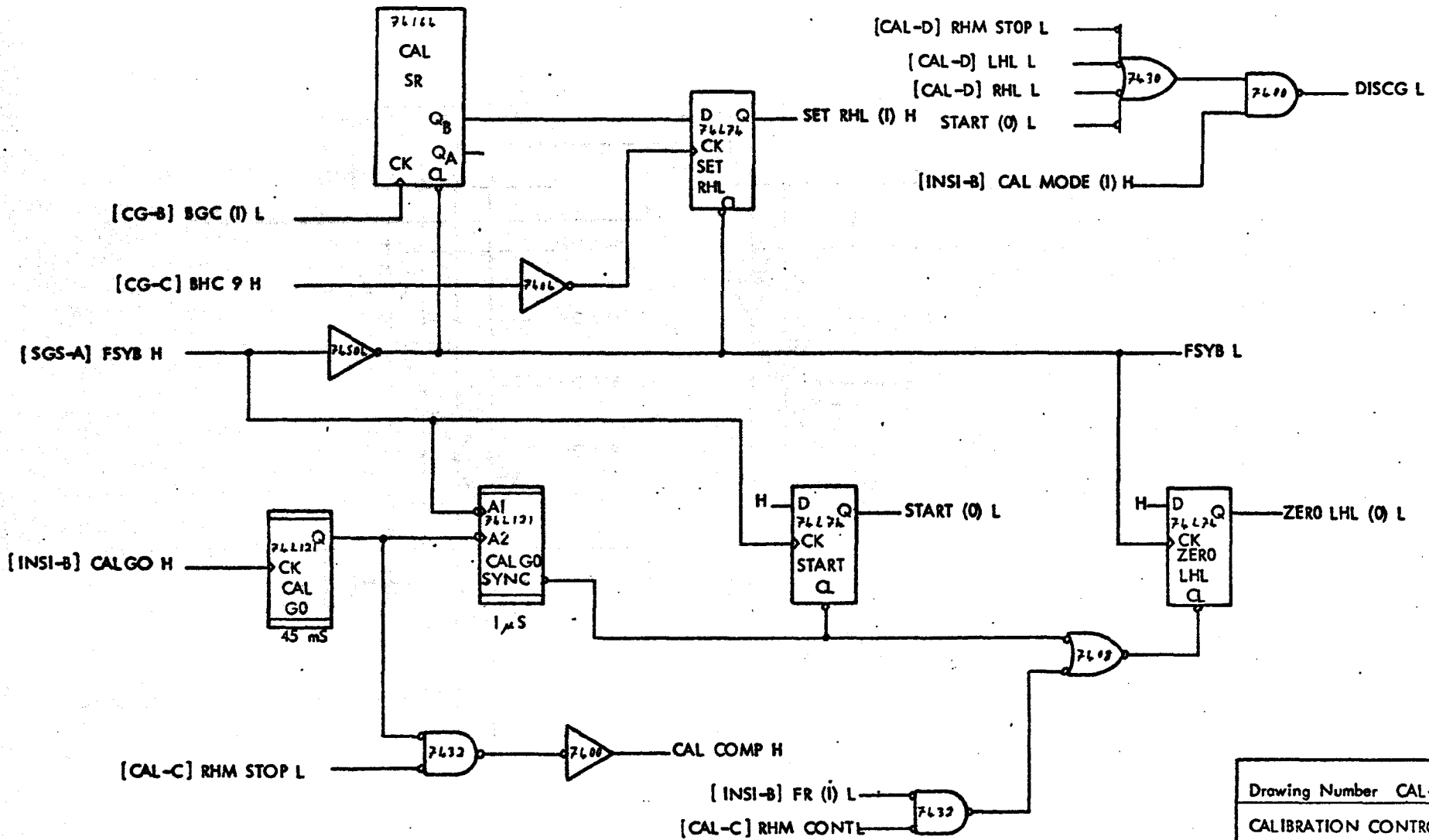


Figure 4.19 CAL-A. Calibration control - calibration initialisation and control logic.

Drawing Number CAL-A
CALIBRATION CONTROL
Calibration Initialisation and Control Logic

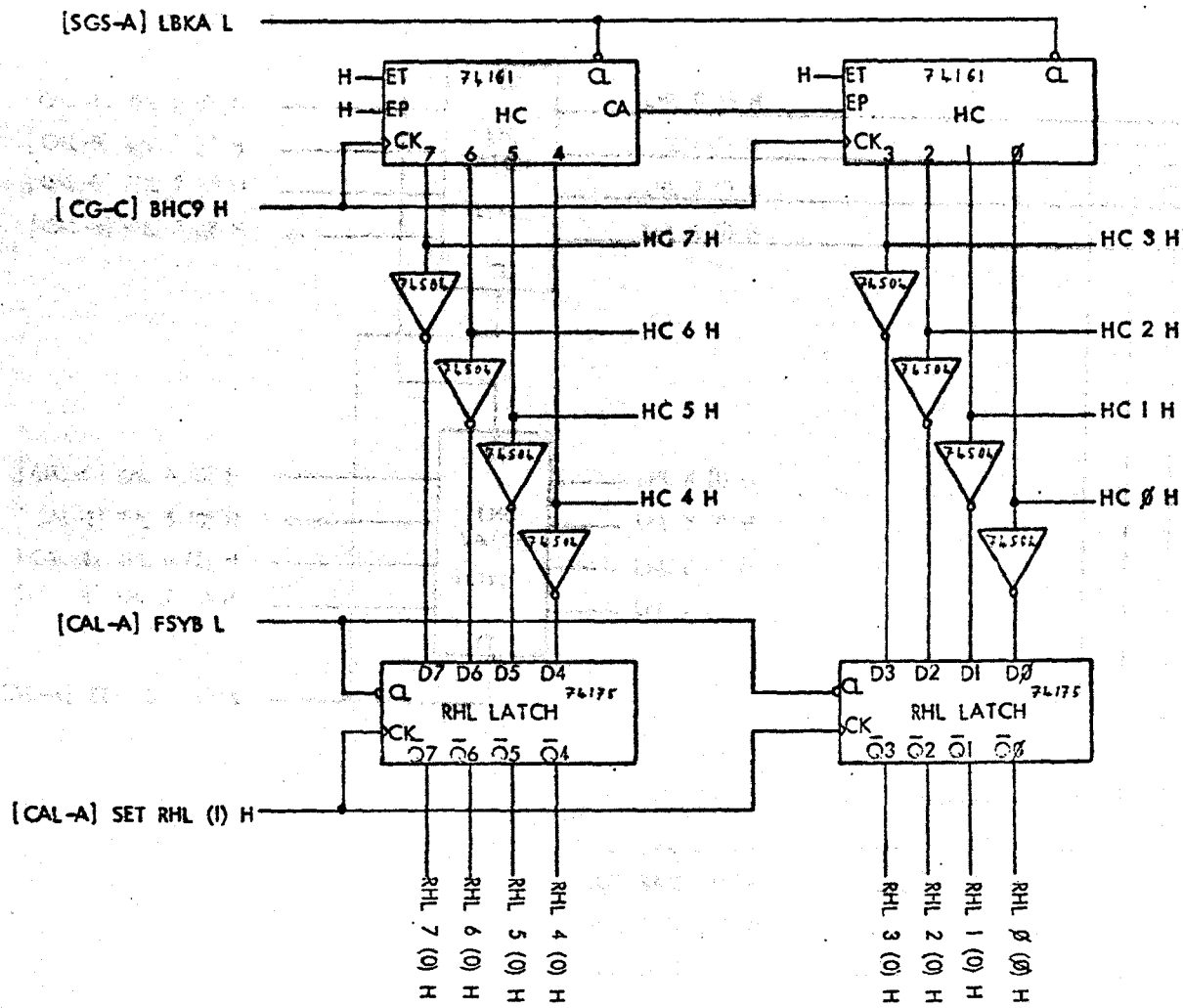


Figure 4.19 CAL-3. Calibration control - horizontal counter and RHL latch.

Drawing Number CAL-3
CALIBRATION CONTROL
Horizontal Counter and RHL Latch

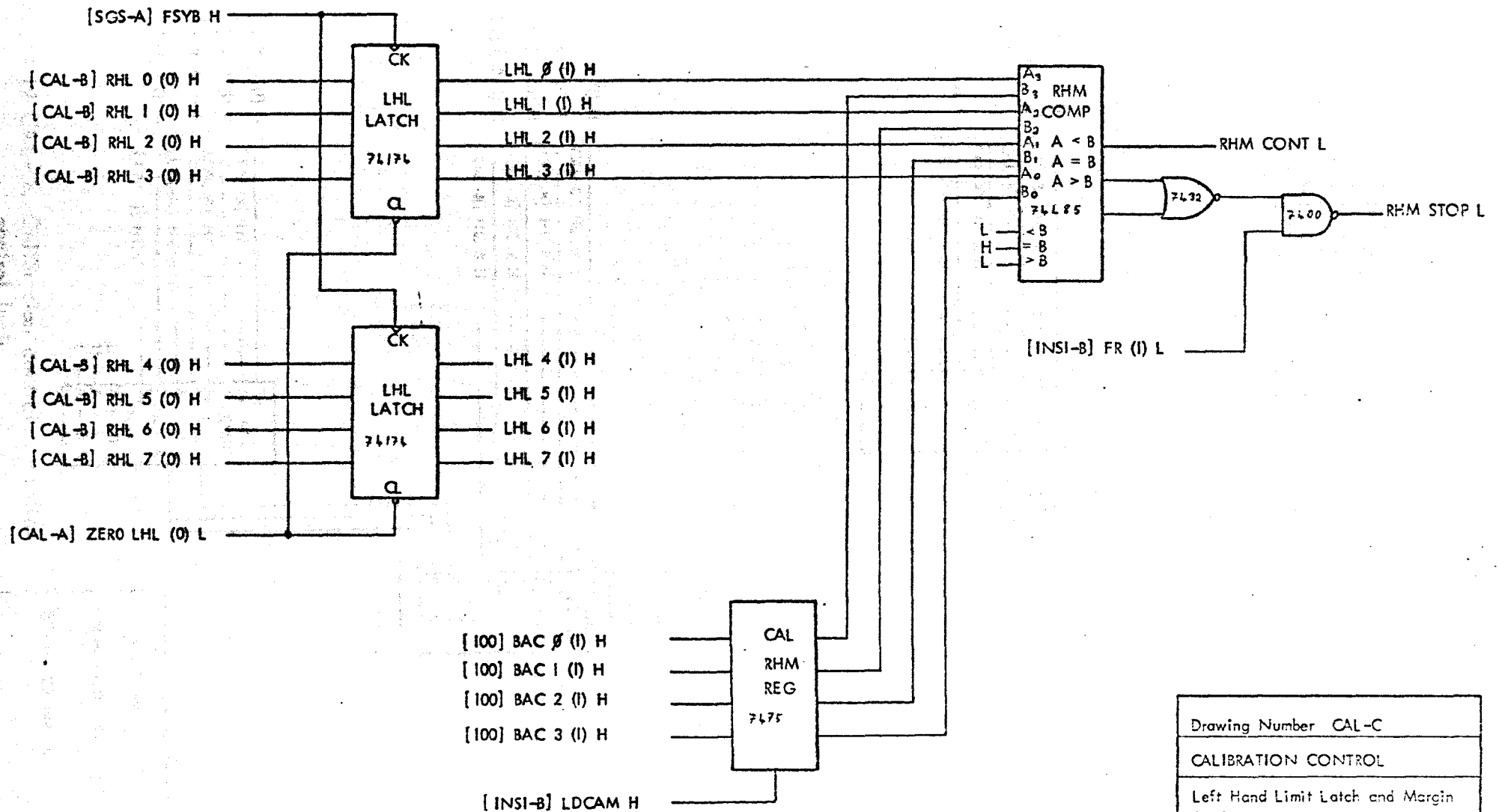
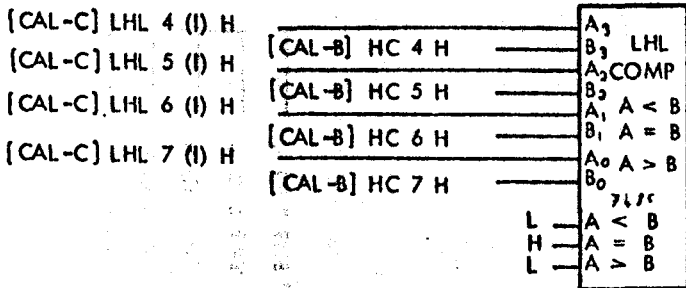
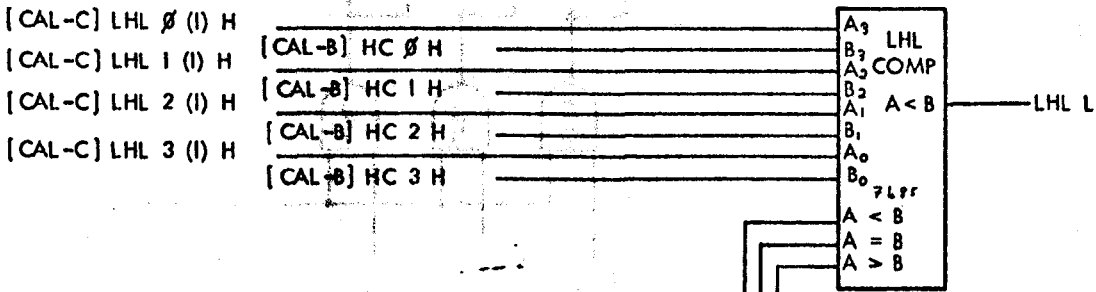
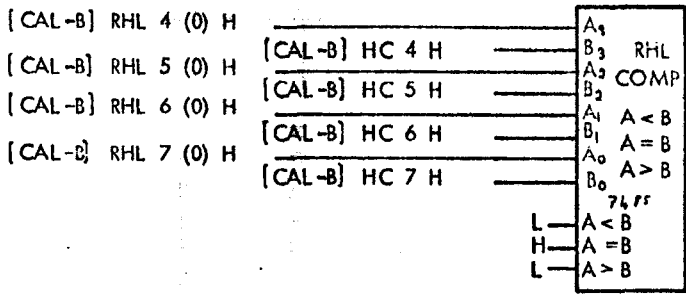
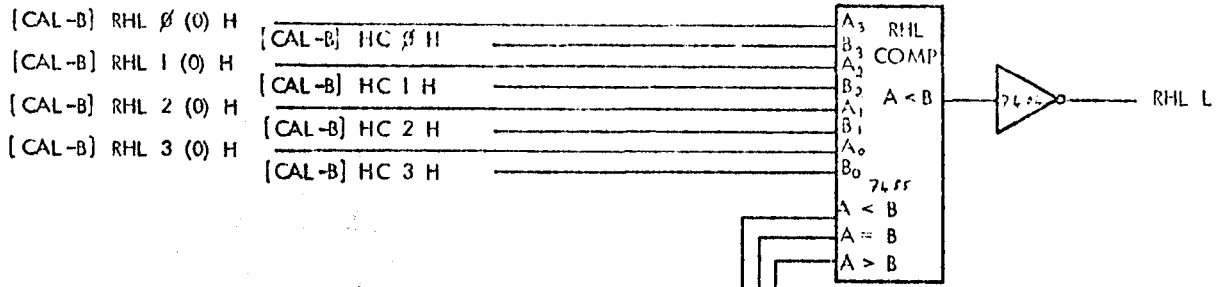


Figure 4.19 CAL-C. Calibration control - left hand limit latch and margin logic.

Drawing Number CAL-C
CALIBRATION CONTROL
Left Hand Limit Latch and Margin Logic



Drawing Number CAL-D
CALIBRATION CONTROL
Window Control Logic

Figure 4.19 CAL-D. Calibration control - window control logic.

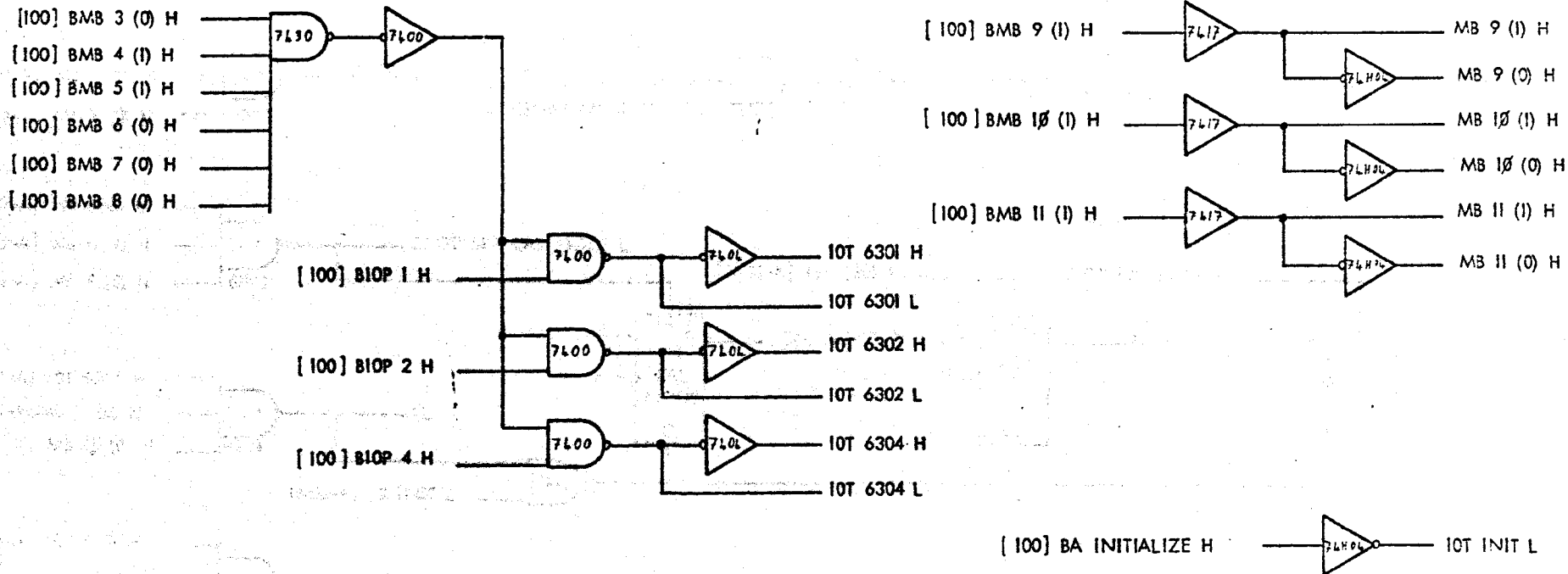


Figure 4.20 INSI-A. Instruction generator I - calibration control instructions 630X.

Drawing Number INSI-A
INSTRUCTION GENERATOR I
Calibration Control Instructions 630X

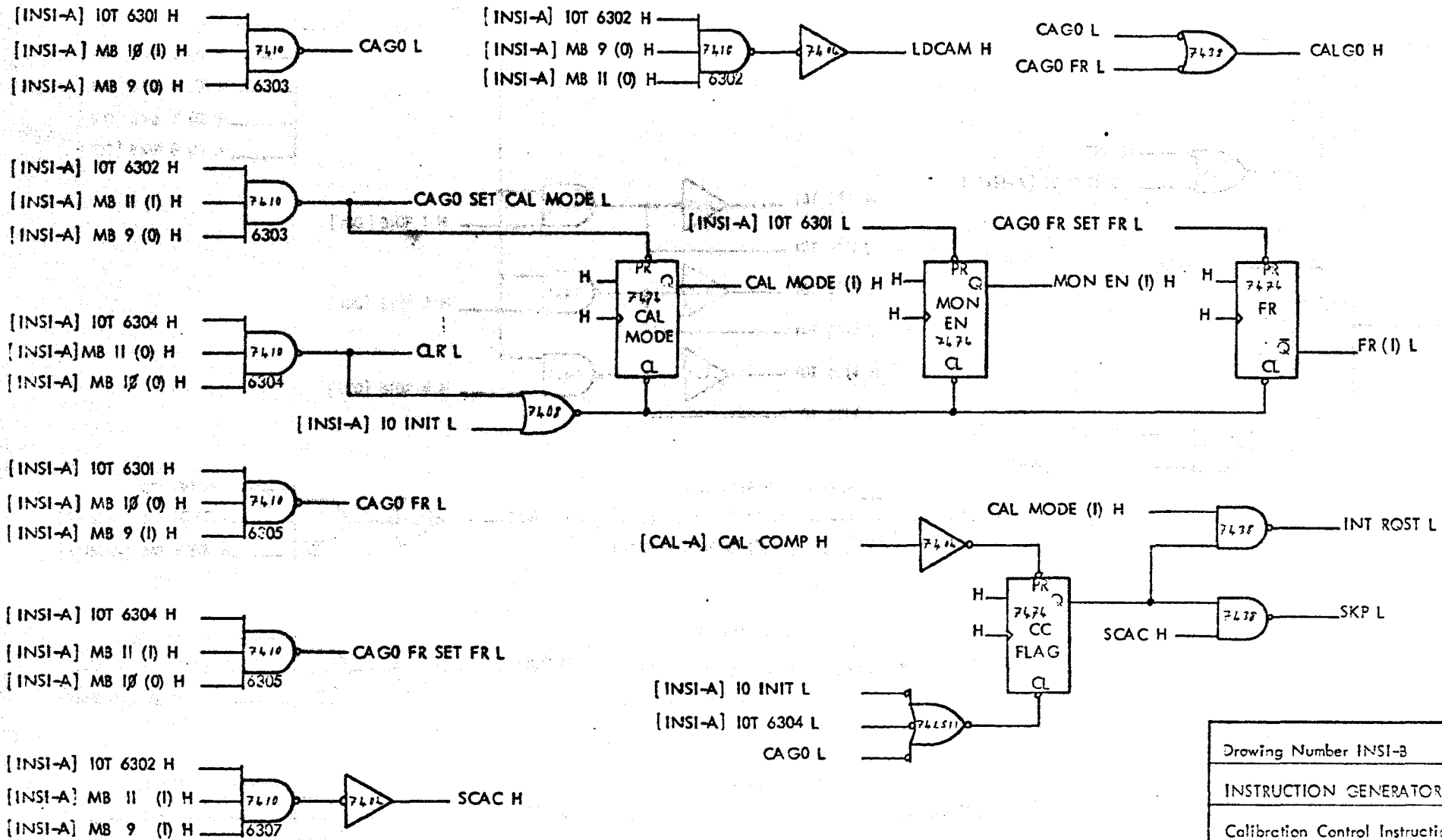
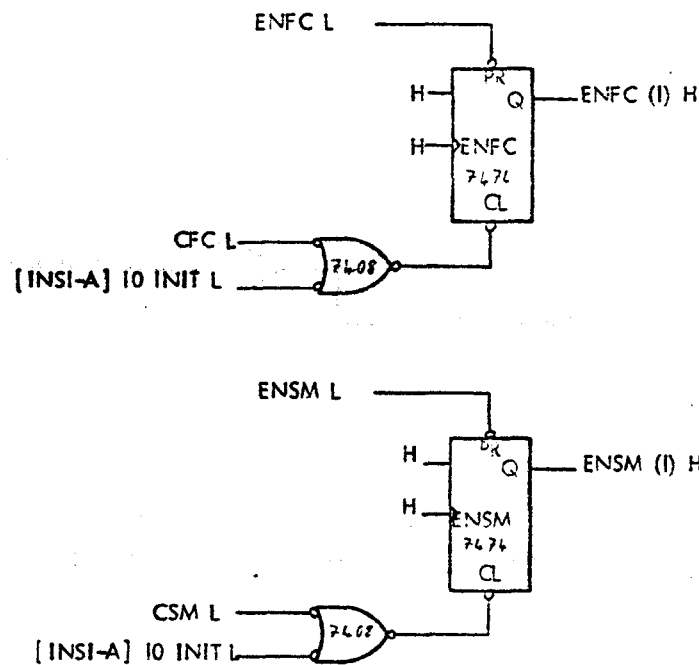
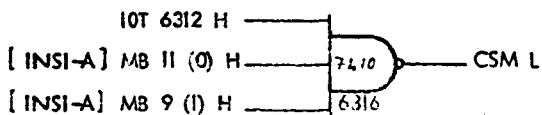
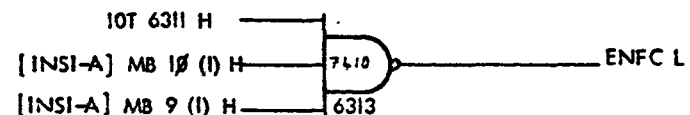
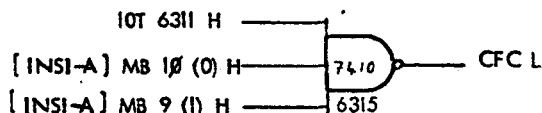
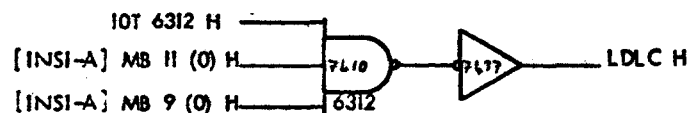
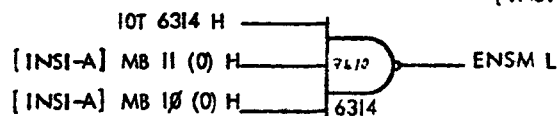
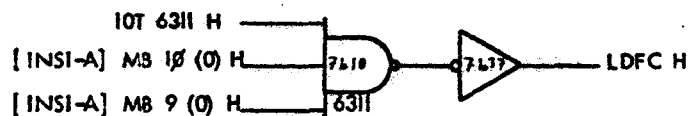
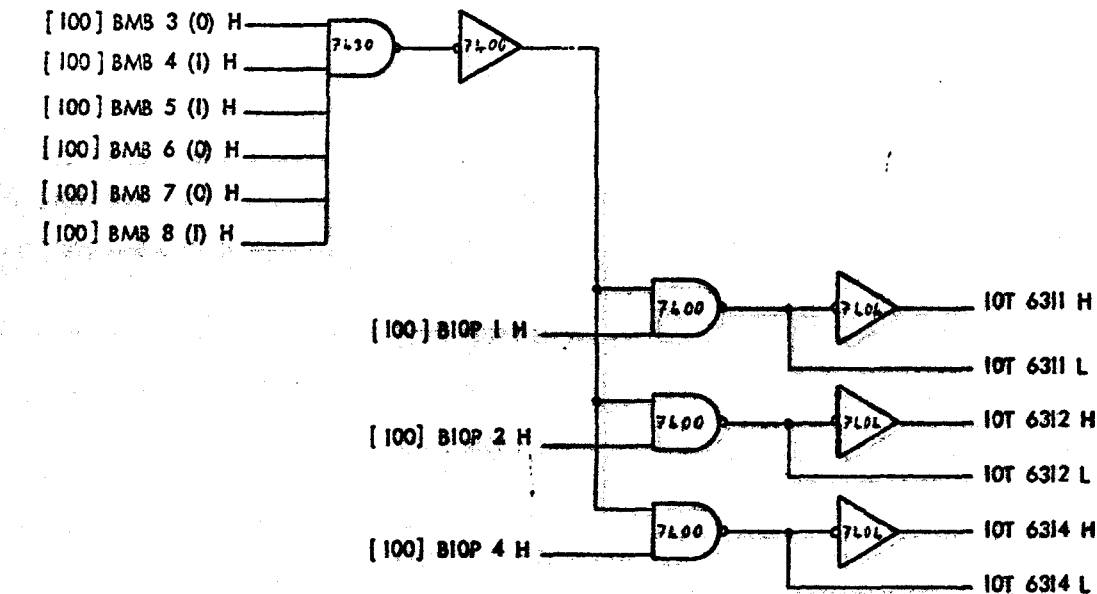


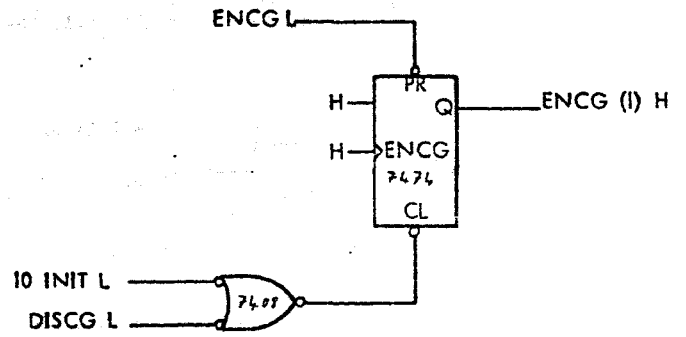
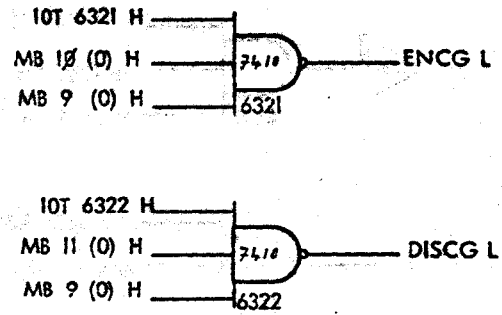
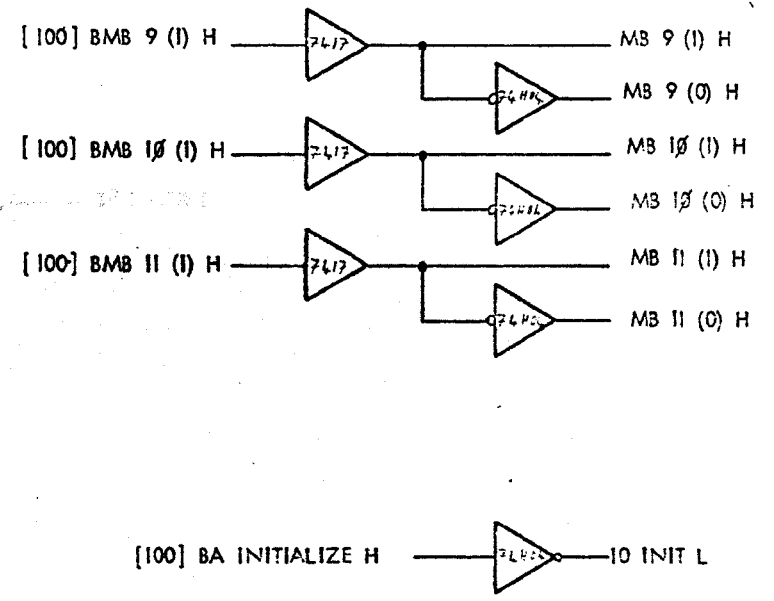
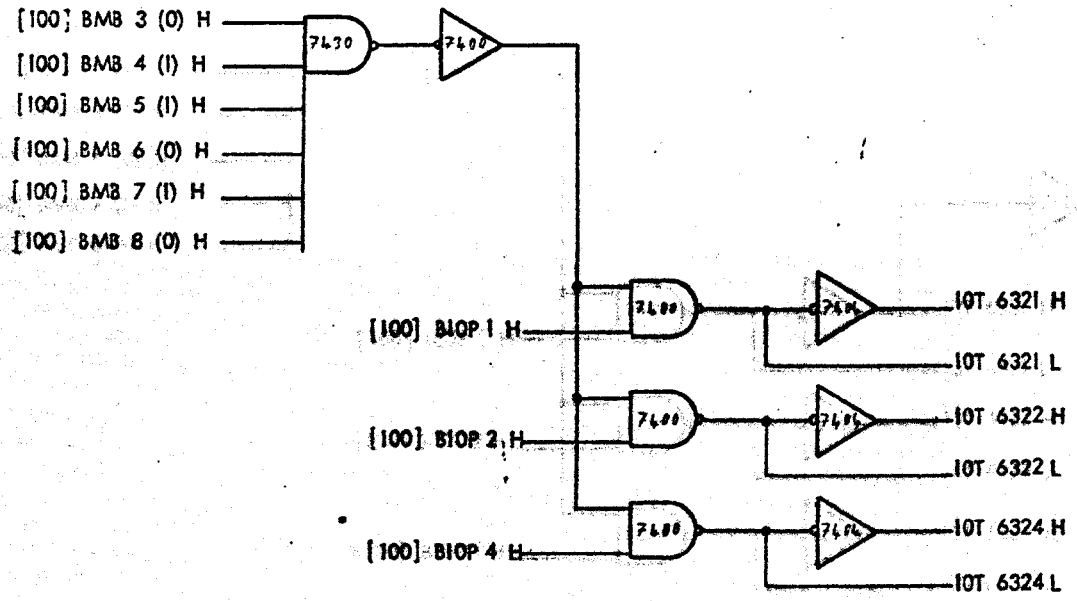
Figure 4.20 INSI-3. Instruction generator I - calibration control instructions 630X (continued).

Drawing Number INSI-3
INSTRUCTION GENERATOR I
Calibration Control Instructions 630X (continued)



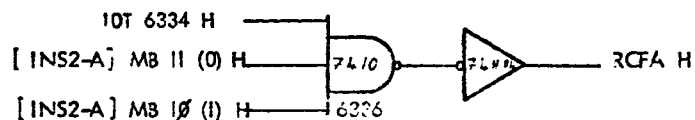
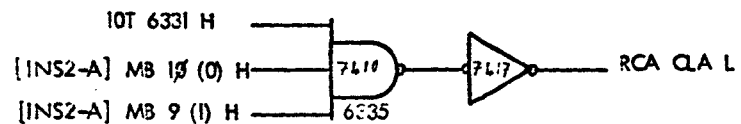
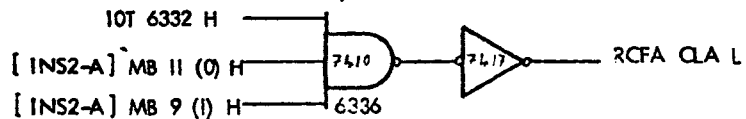
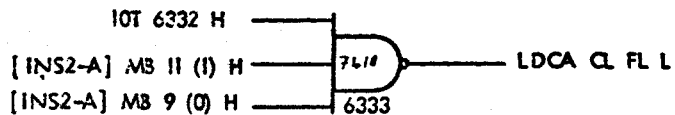
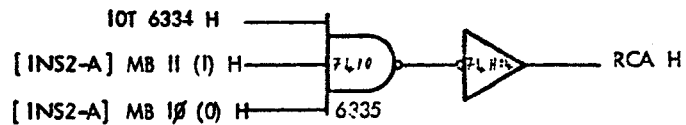
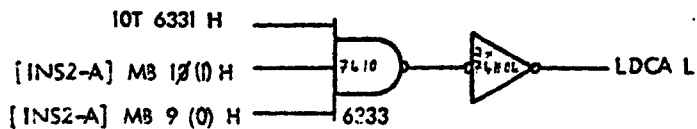
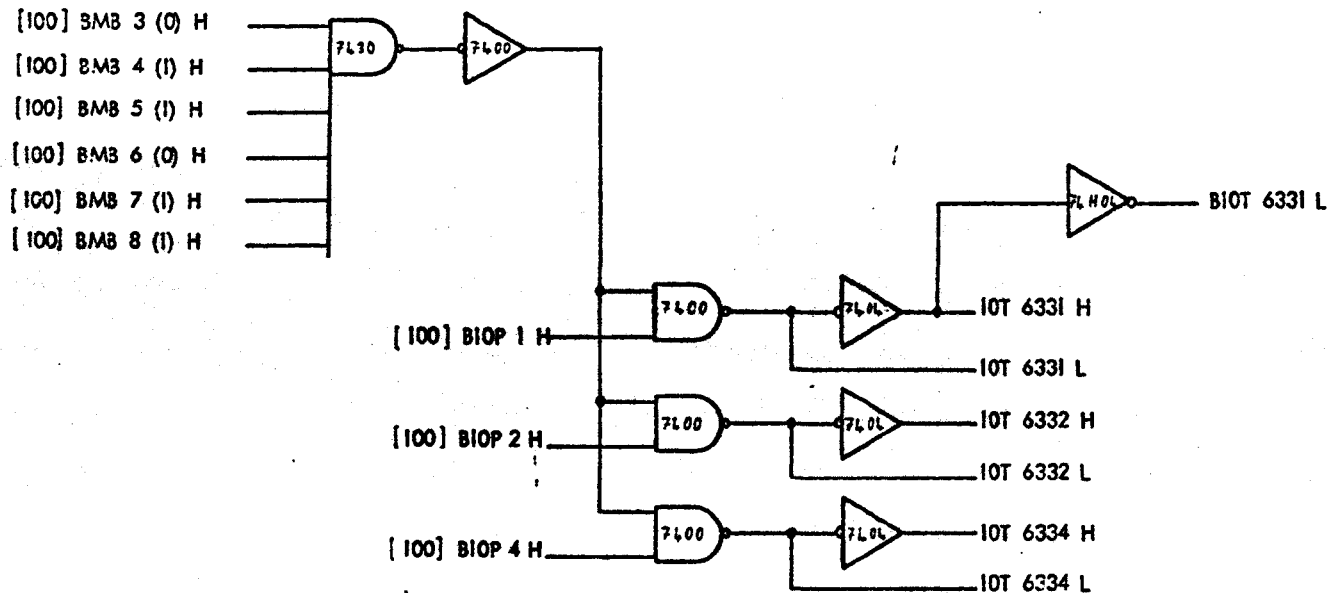
Drawing Number	INSI-C
INSTRUCTION GENERATOR I	
Sync Generator and Simulator Instructions. 631X	

Figure 4.20 INSI-C. Instruction generator I - sync generator and simulator instructions 631X.



Drawing Number INS2-A
INSTRUCTION GENERATOR 2
Co-ordinate Generator Instructions. 632X

Figure 4.21 INS2-A. Instruction generator 2 - co-ordinate generator instructions 632X.



Drawing Number INS2-3
INSTRUCTION GENERATOR 2
Address Register Instructions. 633X

Figure 4.21 INS2-3. Instruction generator 2 - address register instructions 633X.

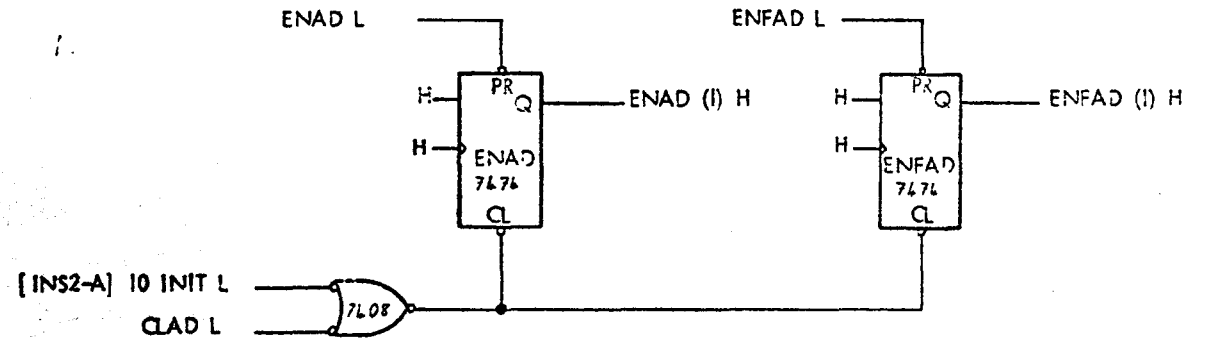
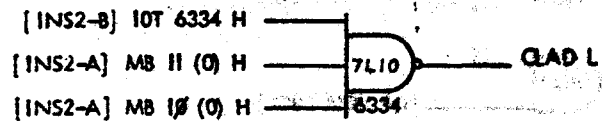
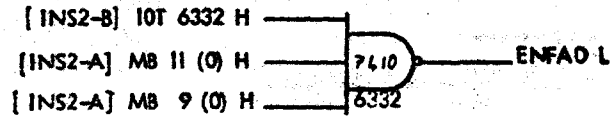
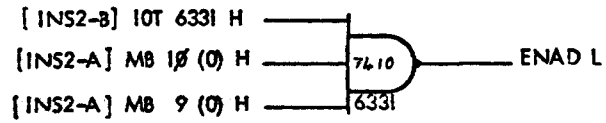


Figure 4.21 INS2-C. Instruction generator 2 - address register instructions 633X (continued).

Drawing Number INS2-C
INSTRUCTION GENERATOR 2
Address Register Instructions 633X (continued)

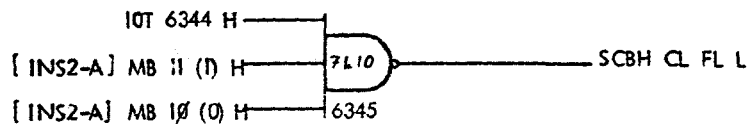
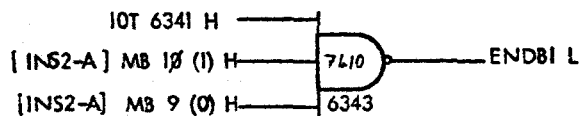
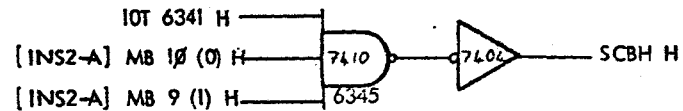
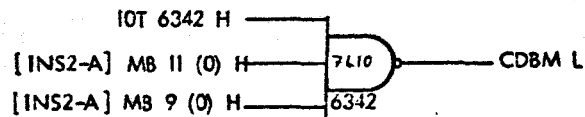
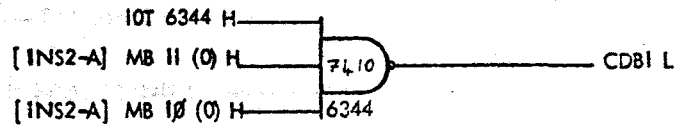
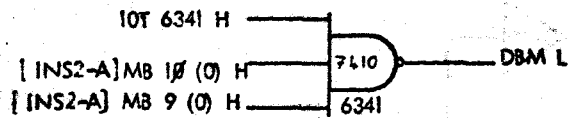
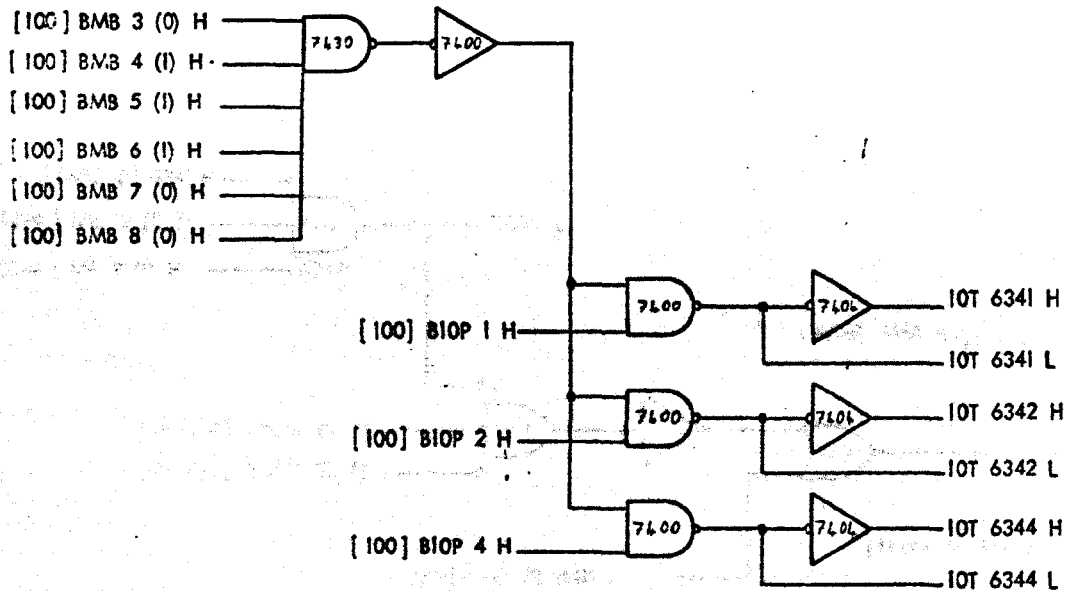


Figure 4.21 INS2-D. Instruction generator 2 - Address Register Instructions 634X.

Drawing Number INS2-D
INSTRUCTION GENERATOR 2
Address Register Instructions. 634X

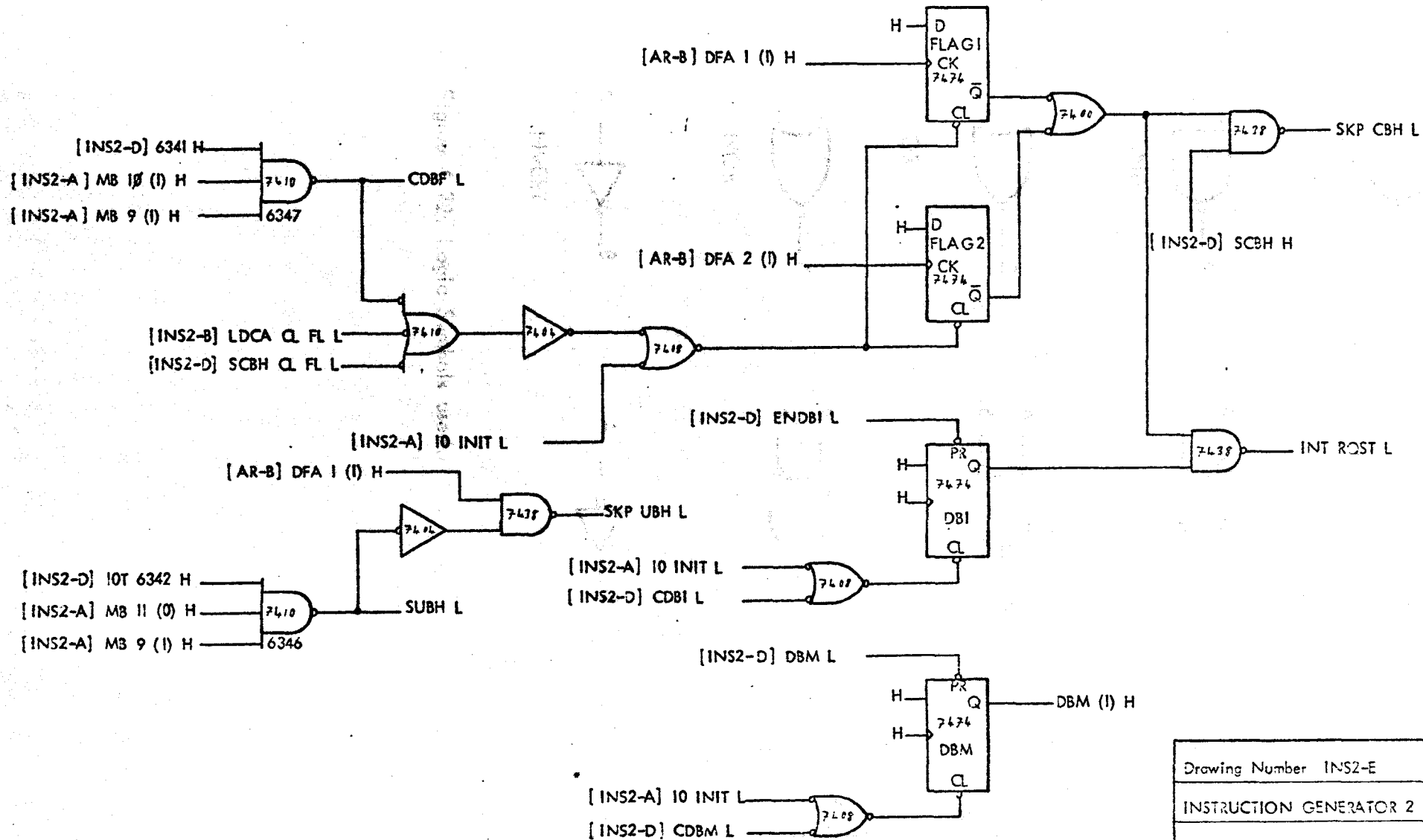


Figure 4.21 INS2-E. Instruction generator 2 - address register instructions 634X (continued).

Drawing Number INS2-E
INSTRUCTION GENERATOR 2
Address Register Instructions 634X (continued)

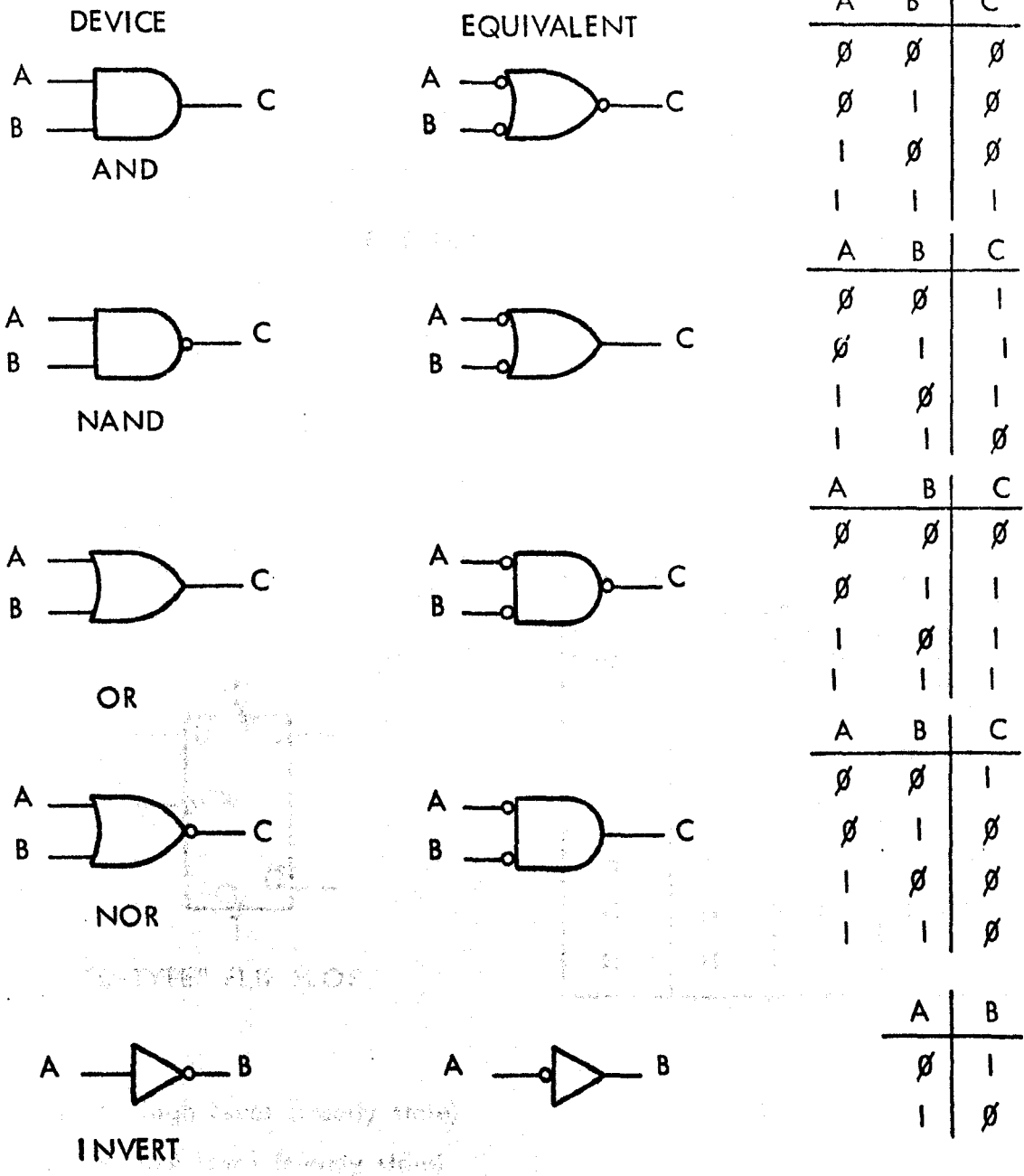


Figure 4.22: Logic Symbols used.

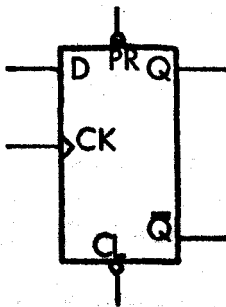
Logic symbols are used to represent the functions of logic gates. The configuration is not shown.

Figure 4.26 (continued)



EXCLUSIVE OR

A	B	C
∅	∅	∅
∅	1	1
1	∅	1
1	1	∅



"D-TYPE" FLIP FLOP

Inputs				Outputs	
Preset	Clear	Clock	D	Q	\bar{Q}
L	H	X	X	H	L
H	L	X	X	L	H
L	L	X	X	H*	H*
H	H	↑	H	H	L
H	H	↑	L	L	H
H	H	L	X	Q_0	\bar{Q}_0

H = high level (steady state)

L = low level (steady state)

↑ = transition, from low to high level

X = irrelevant

Q_0 = the level of Q before the indicated input conditions were established

* = This configuration is non stable.

Figure 4.22 (Continued)

CHAPTER 5
PROGRAMMING

- 5.1 Introduction
- 5.2 Instruction Set
 - 5.2.1 Address Register Instructions
 - 5.2.2 Co-ordinate Generator Instructions
 - 5.2.3 Sync Generator and Simulator Instructions
 - 5.2.4 Calibration Control Instructions
- 5.3 Programming Examples
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 - 5.5.1 Real Time Display
 - 5.5.2 Data Acquisition
 - 5.5.3 Calibration Data Acquisition
 - 5.5.4 Calibration
 - 5.5.5 Data Display

5.1 Introduction

The instruction set created to control the interface is described in this section. The object in providing these instructions was to allow flexible program control with the minimum of manual intervention. The description of the instruction set is followed by some programming examples. A method of decoding the co-ordinate data generated by the interface is shown and a summary of the software developed is given. Full details of this software will be found in Appendix A3.

5.2 Instruction Set

Machine language programs for the PDP 12 computer are assembled using the LAP6 - DIAL operating system, DEC (1970). To simplify the use of the interface Instruction Set, mnemonics have been created for each instruction by the program TVI MN, which is shown in Appendix A3.

In the description of the instructions the mnemonics for each instruction will be given, followed by the octal code, followed by the operation performed. These instructions are used directly in 8 mode, but must be preceded by the instruction IOB in linc mode programming.

5.2.1 Address Register Instructions

ENAD 6331 - Enable Address; the data address register of the interface is enabled onto the external address lines of the PDP 12 computer (12 bits) by setting the address enable flip flop.

ENFAD 6332 - Enable Field Address; the data field address register of the interface is enabled onto the external data extended address lines of the PDP 12 (3 bits) by setting the field address enable flip flop.

LDCA 6333 - Load Composite Address; the initial data address register of the interface is loaded with the contents of the accumulator. Bits 0 - 8 of the Data Address register are set to correspond to bits 0 - 8 of this initial address and the Data Field Address register is set to correspond to the remaining bits, 9, 10 and 11. Bits 9, 10 and 11 of the Data Address Register are set to zero. The contents of the accumulator remain undisturbed. This instruction

also clears the 'change buffer half' flags of the double buffer mode, after the initial address has been set.

CLAD 6334 - Clear Address and Field Address Enable flip flops; the interface data address is taken off the external address lines of the PDP 12.

RCA 6335 - Read Current Address; the accumulator is cleared and the contents of the 12 bit Data Address Register of the interface are read into the accumulator. The previous contents of the accumulator are lost.

RCFA 6336 - Read Current Field Address; the accumulator is cleared and the contents of the 3 bit Data Field Address Register are read into bits, 9, 10 and 11 of the accumulator.

DBM 6341 - Set Double Buffer Mode; the address register is set into the double buffer mode of operation. When the 15 bit address reaches 27777_8 the next address will be that held in the initial data address register.

CDBM 6342 - Clear Double Buffer Mode; the address register is reset to the normal mode of operation.

ENDBI 6343 - Enable Double Buffer Mode Interrupt; if the computer interrupt is turned on (I ON instruction) then an interrupt request will be made when the data buffer changes halves. The data buffer changes halves when the 15 bit address changes from 17777_8 to 20000_8 or from 27777_8 to 10000_8 .

CDBI 6344 - Clear Double Buffer Mode Interrupt; no interrupt request will be made when the buffer changes halves.

SCBH 6345 - Skip if Change Buffer Half flag is set; the next instruction in the program will be skipped if either of the change buffer half flags is set. The flags are then cleared.

SUBH 6346 - Skip if Upper Buffer Half is being filled; the next instruction in the program is skipped if the 15 bit address is 20000_8 or greater.

CDBF 6347 - Clear Flags; the change buffer half flags are cleared.

5.2.2 Co-ordinate Generator Instructions

ENCG 6321 - Enable Co-ordinate Generator; The marker detector inputs to the co-ordinate generator are enabled allowing co-ordinates to be generated and data transfer to take place via the single cycle data break facility.

DISCG 6322 - Disable Co-ordinate Generator; the inputs to the co-ordinate generator are disabled and the break request flag cannot be set, inhibiting any data transfer.

5.2.3 Sync Generator & Simulator Instructions

LDFC 6311 - Load Field Count Register; the 8 bit field count register is loaded with the contents of bits 4 to 11 of the accumulator. The contents of the accumulator are undisturbed.

LDLC 6312 - Load line Count Register; the 8 bit line count register is loaded with the contents of bits 4 - 11 of the accumulator. The contents of the accumulator are undisturbed.

ENFC 6313 - Enable Field Count; the field count function of the sync generator and simulator is enabled. The sampling rate of the co-ordinate generator will be controlled by the contents of the field count register.

ENSM 6314 - Enable Simulator; simulated marker pulses will be generated on every television scan line specified by the contents of the line count register.

CFC 6315 - Clear Field Count; the field count function is disabled and the co-ordinate generator will sample every television field. The contents of the field count register are undisturbed.

CSM 6316 - Clear Simulator; generation of simulated marker signals is disabled. The contents of the line count register are undisturbed.

5.2.4 Calibration Control Instructions

ENMON 6301 - Enable Monitor Output; this output provides a 1 volt signal at the scan time corresponding to the position of the window. The signal may be mixed with the video signal from the camera viewing the calibration grid.

LDCAM 6302 - Load Calibration Margin; bits 0 to 3 of the contents of the accumulator are loaded into the calibration right hand margin register. When the left hand co-ordinate of the calibration window exceeds the value held in this register the co-ordinate generator is disabled and the calibration complete flag is set.

CAG0 6303 - Calibration Go; the calibration control is set into operation. The co-ordinates of the window are set to the beginning of the television line

scan and collection of data will start at the beginning of the next television field (line 0). The calibration complete flag is cleared and the monitor output is enabled. The calibration mode flip flop is set which enables the interrupt. An interrupt will occur when calibration is completed if the interrupt facility in the PDP 12 has been switched on (ION).

CLCAL 6304 - Clear Calibration mode; all flags and mode flip flops in the calibration control are cleared.

CAGOFR 6305 - Calibration Go Free Run mode; the calibration control is set into operation as before except that the calibration complete flag is inhibited and the right hand margin becomes inoperative. The window will therefore operate continuously. This instruction is used for maintenance purposes.

CCAFL 6306 - Clear flag; the calibration complete flag is cleared.

SCAC 6307 - Skip if Calibration is Complete; the next instruction in the program is skipped if the calibration complete flag is set. The flag is then cleared.

5.3 Programming Examples

The first example program shows how the interface is initialised and set into operation to generate co-ordinate data. In this example sense switch 0 is used to start and stop data collection, and a limit is placed on the amount of data collected. If this limit is exceeded the program automatically stops data collection by disabling the co-ordinate generator, the program then enters a routine to process the collected data (not shown). The interface will start transfer of data at location 2000_8 of memory field 1 as defined by the composite address. The address limit is location 4000_8 of memory field 2 as defined by 'end memory address' and 'end field address'. This limit may be exceeded by a maximum of 31_{10} words allowed for by the instruction on lines 76 and 77. This example also demonstrates how a program may be run independently of data transfer. Interaction with the interface is provided by the subroutine (SAI) which monitors the locations in memory used by co-ordinate data.

The second example shows how a program may control some of the other parameters of the interface. In this example the Field Count Register is set to 005_8 and the Field Count mode can be enabled if sense switch 1 on the computer is set. This will limit the sampling rate of the Co-ordinate Generator

```

0000          *20
0001          /EX1 - EXAMPLE PROGRAM 1
0002          /
0003          /9-1-76
0004          /
0005          LODSYM          /LOAD INTERFACE
0006          /              MNEMONICS
0007          PMODE
0008          *20
0009          IOB              /TURN INTERRUPT OFF
0010          DISCG           /DISABLE CO-ORDINATE
0011          0020 6002      CLA CLL          /GENERATOR
0012          0021 6322      TAD CAL        /SET UP INITIAL ADDR
0013          0022 7300      LDCA           /IN ADDRESS REGISTER
0014          0023 1055      ENAD          /ENABLE ADDRESS LINES
0015          0024 6333      ENFAD
0016          0025 6331      LINC
0017          0026 6332      LMODE
0018          0027 6141      SVS 0          /ENABLE CG ?
0019          0030 0440      JMP .-1       /NO, WAIT
0020          0031 6030      IOB           /YES, ENABLE CG
0021          0032 0500      ENCG
0022          0033 6321      JMP SAI    /GO TO SUBROUTINE
0023          0034 6056      SNS I 0      /CONTINUE ?
0024          0035 0460      JMP .-2     /YES
0025          0036 6034      IOB           /NO, DISABLE CG
0026          0037 0500      DISCG
0027          0040 6322      IOB           /READ & SAVE CURRENT
0028          0041 0500      RCFA          /FIELD ADDRESS
0029          0042 6336      STC CFA
0030          0043 4053      IOB           /READ & SAVE CURRENT
0031          0044 0500      RCA          /MEMORY ADDRESS
0032          0045 6335      STC CA
0033          0046 4054      LLA I       /GENERATE I/O PRESET
0034          0047 1020      0020    /TO CLEAR ALL FLAGS
0035          0050 0020      ESP
0036          0051 0004      JMP PROC  /ENTER PROGRAM TO
0037          0052 6200      /      PROCESS DATA
0038          0053 0000      /
0039          0054 0000      /
0040          0055 2001      /
0041          0056 0000      /
0042          0057 0000      /SUBROUTINE TO READ CURRENT ADDRESS USED BY
0043          0060 0000      /INTERFACE AND CHECK AGAINST PRESET ADDRESS
0044          0061 0000      /LIMIT.
0045          0062 0000      /
0046          0063          LMODE
0047          0064 1000      SAI,   LLA          /SAVE RETURN ADDRESS
0048          0065 0000      0
0049          0066 4101      STC SAI0
0050          0067 0500      IOB           /READ CURRENT FIELD
0051          0070 6336      RCFA          /ADDRESS
0052          0071 1440      SAI           /=END FIELD ADDRESS ?
0053          0072 0102      EFA
0054          0073 6131      JMP SAI0      /NO, RETURN
0055          0074 0500      IOB           /YES, READ CURRENT
0056          0075 6335      RCA          /MEMORY ADDRESS

```

0076	0070	1560	RCL I	/SET 5 LEAST SIGNIF
0077	0071	0037	0037	/BITS TO ZERO
0100	0072	1440	SAF	/=END MEMORY ADDRESS
0101	0073	0100	EMA	
0102	0074	6101	J4P SA10	/NO, RETURN
0103	0075	1020	LDA I	/YES, ADD 2 TO RETURN
0104	0076	0000	2	/ADDRESS
0105	0077	1140	ADM	
0106	0100	0101	SA10	
0107	0101	6000	SA10, JMP 0	/RETURN
0110			/	
0111			/	
0112	0102	0000	HFA, 2	/END FIELD ADDRESS
0113	0103	4000	EMA, 4000	/END MEMORY ADDRESS
0114			/	
0115			/	
0116			/	
0117			/	
0120			/PROGRAM TO PROCESS DATA	
0121			*200	
0122	0200	0016	PROC, NOP	

NO ERRORS

CA	0054
CAD	0055
CAGJ	6303
CAGJFR	6305
CCAFI	6306
CDPF	6347
CDPI	6344
CDPM	6342
CFA	0053
CFC	6315
CLAD	6334
CLCAL	6304
CSM	6316
DB4	6341
DISCG	6322
HFA	0102
EMA	0103
FNAD	6331
ENCG	6321
FNDFI	6343
FNFAD	6332
ENFC	6313
FNMON	6331
FNSM	6314
LDCA	6333
LDCAM	6302
LDFC	6311
LILC	6312
PROC	0200
RCA	6335
RCFA	6336
SAI	0056
SA10	0101
SCAC	6307
SCFH	6345
SUBH	6346

```

0000
0001 *00 /EX2 - EXAMPLE PROGRAM 2
0002 /
0003 /12-1-76
0004 /
0005 LOLSVM /LOAD INTERFACE
0006 / MNEMONICS
0007 /
0010 PMODE
0011 *20
0012 0020 6002 IOB /TURN INTERRUPT OFF
0013 0021 6322 DISCG /DISABLE CG
0014 0022 7300 CLA CLL
0015 0023 1362 TAD CAL /SET UP INITIAL ADDR
0016 0024 6333 LDCA /IN ADDRESS REGISTER
0017 0025 7200 CLA
0020 0026 1360 TAD SLCT /SET UP LINE COUNT
0021 0027 6312 LLLC /REGISTER
0022 0030 7200 CLA
0023 0031 1361 TAD SFCT /SET UP FIELD COUNT
0024 0032 6311 LDLC /REGISTER
0025 0033 6341 DBM /SET DBL BUFFER MODE
0026 0034 6331 ENAD /ENABLE INTERFACE
0027 0035 6332 ENFAD /ADDRESS LINES
0030 0036 6141 LINC
0031 LMODE
0032 0037 0441 SNS 1 /ENABLE FIELD COUNT?
0033 0040 6043 JMP .+3 /NO
0034 0041 0500 IOB /YES
0035 0042 6313 EVFC
0036 0043 0442 SNS 2 /ENABLE SIMULATOR?
0037 0044 6047 JMP .+3 /NO
0040 0045 0500 IOB /YES
0041 0046 6314 ENSM
0042 0047 7440 SNS 0 /ENABLE CG?
0043 0050 6047 JMP .-1 /NO, WAIT
0044 0051 0500 IOB /YES
0045 0052 6321 ENCG
0046 0053 0460 SNS I 0 /CONTINUE SAMPLING?
0047 0054 6353 JMP .-1 /YES
0050 0055 0500 IOB /NO, DISABLE CG
0051 0056 6322 DISCG
0052 0057 6200 JMP NP /GO TO NEW PROGRAM
0053 /
0054 /
0055 /
0056 /
0057 0060 0177 SLCT, 0177 /LINE COUNT
0060 0061 0035 SFCT, 5 /FIELD COUNT
0061 0062 6302 CAL, 6002 /INITIAL ADDRESS 6002
0062 / IN MEMORY FIELD 2
0063 /
0064 /
0065 /
0066 *200
0067 0200 0016 NP, NOP
0070 /
0071 /

```

NO ERRORS

CAD 0062

CAGD	6303
CAGDFR	6305
CCAFI	6306
CDFP	6347
CDFI	6344
CDRM	6342
CFC	6315
CLAD	6334
CLCAL	6304
CSM	6316
DPM	6341
DISCG	6302
ENAD	6331
ENCG	6321
ENIBI	6343
ENFAD	6332
ENFC	6313
ENMON	6301
ENS4	6314
LECA	6333
LDCAM	6302
LDFC	6311
L DLC	6312
NP	0200
RCA	6335
RCPA	6336
SCAC	6307
SCRH	6345
SFCT	0061
SLCT	0060
SURH	6346

The above table lists the codes for the various data sets which are used in the analysis of the data. The codes are listed in the first column and the corresponding data set names are listed in the second column. The codes are listed in the order in which they appear in the data sets.

The data sets are listed in the order in which they appear in the data sets. The codes are listed in the first column and the corresponding data set names are listed in the second column. The codes are listed in the order in which they appear in the data sets.

3.4. A Method of Data Set

The data set is a list of data sets which are used in the analysis of the data. The data set is a list of data sets which are used in the analysis of the data. The data set is a list of data sets which are used in the analysis of the data.

such that co-ordinates will only be generated on every 5th television field (10 Hz). The Line Count Register is set to 177_8 and simulated marker pulses will be generated if the simulator is enabled (by setting sense switch 2). These simulated pulses will appear on two lines in each television field as defined by the contents of the Line Count Register; the combinations of pulses previously shown in Figure 3.9 will be available on each of these lines and may be used to provide inputs to the marker detectors. In this way co-ordinates of known values will be generated and may be used to test data processing programs, data displays etc. This program also shows how the Address Register can be used in the Double Buffer mode to provide a re-circulating data buffer in computer memory. The Initial Data Address register, in the interface is set to 6002_8 which is translated by the interface into a 15 bit starting address for the Address Register - location 6000 in memory field 2. When co-ordinate data is generated it will be transferred to computer memory starting at this location, and the Address Register will be incremented upwards. When location 777_8 of memory field 2 has been filled the interface will automatically reset the Address Register to the starting address held in the Initial Data Address register. By using the Address Register in this way the area in memory which the interface may access can be limited, in this case to 1024_{10} locations. The use of the Double Buffer mode to "double buffer" incoming data (so that data may be transferred to mass storage as it is received) is discussed in the next chapter.

The Co-ordinate Generator is enabled by setting sense switch \emptyset on the computer console. As soon as this is done the interface will begin to generate the co-ordinates of any marker signals detected in the video channel inputs. Generation of co-ordinates will continue until sense switch \emptyset is reset. The program could easily be expanded from this point to process data and, for example, to display the trajectories of markers in real time.

5.4 A Method of Decoding

The order and format in which data is stored in computer memory by the interface has been described in relation to the hardware in Chapter 3 and an illustration was given in Figure 3.7. In order to explain a method of decoding the data, the data format will be described in relation to programming. In

this section a "data block" refers to the data generated by the interface for one television line, and will therefore be of variable length (3 to 7 words). A "data record" is all the data generated by the interface during a particular test. The length of this record is limited by computer core memory size or, if data can be double buffered and transferred to disk storage, by available disk space.

Due to the way in which the Co-ordinate Generator operates the first word of any data block will always be a vertical co-ordinate, there may then be up to 5 horizontal co-ordinates followed by the shift register code word. This code word will always be followed by the vertical co-ordinate of the next data block, except for the last word in any data record. The address of this last word will, however, have been read and stored by the computer program using the appropriate interface instructions. This data format is illustrated in Figure 5.1.

The camera code for each Horizontal Co-ordinate (HC) is made up of 3 bits; two of which are in bits 0 and 1 of the HC word, and the least significant bit is contained in the Shift Register (SR) word. The bit format for a data block is shown in Figure 5.2. The position in the SR word of the additional code bit corresponding to each HC is clearly shown in this figure. To reconstruct the camera code for each HC the subroutine shown in Figure 5.3 and 5.4 may be used. This subroutine would be entered once for each horizontal co-ordinate, starting with HC1 as shown in Figure 5.2. The reconstructed camera code is left in the Accumulator on exit from the subroutine.

The method shown here is just one of several possible ways of reconstructing the camera code. The details of this method may be changed to suit other parts of a program, as has been done in some of the programs shown in Appendix A3.

5.5 Software Summary

All programs for data acquisition and interface control have been written in assembly language as have programs for calibration and data display, as this was the most suitable method available at the time. Full details of the programs summarized in this section will be found in Appendix A3, in the form of listings and, where appropriate, flow charts.

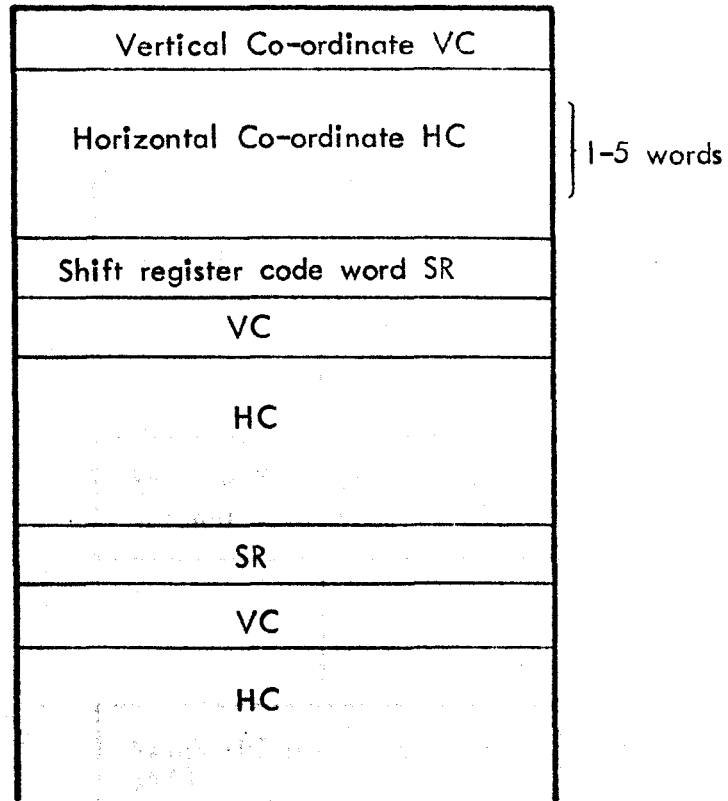


Figure 5.1. Data format for interface generated data.

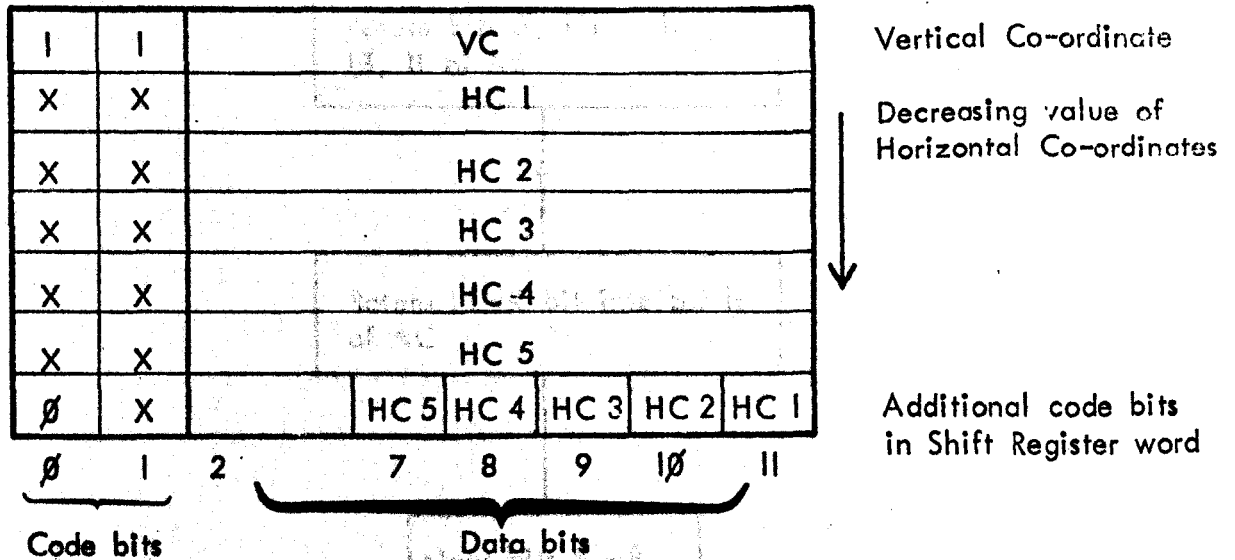


Figure 5.2. Data bits format for one data block of interface generated data.

Figure 5.3. Flow chart for program of Figure 5.1 to the program of Figure 5.2.

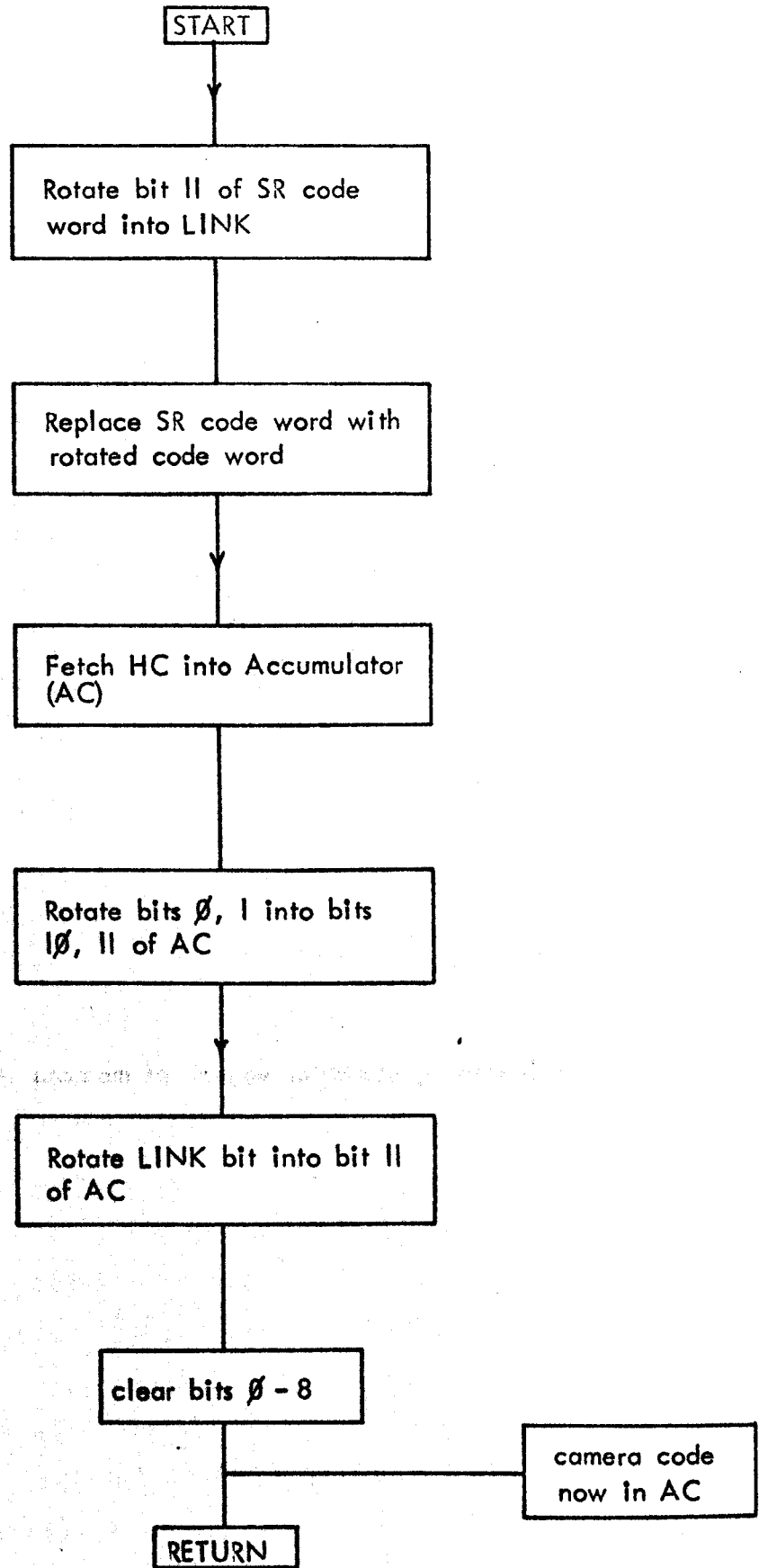


Figure 5.3. Flow chart for program of Figure 5.4. to obtain camera codes.

```

*20
      PMODE
(SCODE) 0000      /RETURN ADDRESS
      LINC
      LMOEF
      ROR I 1      /CODE BIT INTO LINK
      STC SR      /AND REPLACE SR
      ALL HC      /FETCH HC
      ROR 12      /CODE BITS INTO 10,11
      ROL I 1      /ADD THIRD CODE BIT
      RCL I      /CLEAR BITS 0-8
      7779
      PEP
      PMODE
      JMP I SCODE  /RETURN

```

Figure 5.4. Computer program to decode interface generated data.

5.5.1 Real Time Display

The program DYDIS (Dynamic Display) provides a real time display of data generated by the interface. Co-ordinate data is transferred, by the interface, to a 1,000 word re-circulating buffer in computer memory. The program then decodes the data and stores it in one of six display buffers according to camera code. Any one of these buffers may be selected for display at any time during the running of the program. The camera code of the data currently being displayed is included in the display. The interface simulator can be controlled and may be used to provide simulated inputs to the marker detectors to generate co-ordinates for display. The Field Count mode may also be selected to reduce the sampling rate of the interface. Because the generated co-ordinates are displayed in real time the data rate is limited in order to avoid display flicker, this limit has been found to be approximately 4,000 words/sec. If it is desired to generate and display a large number of points in one television field and a reduction in sampling rate is acceptable then the Field Count mode can be used.

5.5.2 Data Acquisition

The main task of acquiring co-ordinate data and decoding it is accomplished by the program DP (Data Process). This program controls the acquisition of data from the interface, decodes it and stores the decoded data on magnetic tape. Six of the analogue channels of the PDP 12 may also be sampled on receipt of clock pulses generated by the interface, and this data will also be stored on magnetic tape. The sampling rate for this facility is controlled by the contents of the line count register. Processed co-ordinate data is stored in the format:- television field number (time co-ordinate), followed by the vertical co-ordinate, followed by the horizontal co-ordinate. These three co-ordinates are provided for every point detected in the video signals of each camera in the system. Data from each camera is stored in a separate sequence of tape blocks. The end of each sequence, or camera record, being indicated

by at least three consecutive words of 7777_8 . The camera (channel) number is indicated by bits 1, 2 and 3 of the time co-ordinate word; and bit 0 of this word, if set, indicates that the analogue channels of the computer were sampled.

Other facilities provided by the program include the ability to set a limit on the amount of memory used by the interface, the interface is disabled when this limit is reached. The sampling frequency of the interface may be controlled by entering the Field Count mode, and the simulator may also be used by this program. The various options are selected and their parameters are controlled from the computer console switches and by presetting registers in the program.

5.5.3 Calibration Data Acquisition

The acquisition of a matrix of calibration points from a grid of markers placed in the field of view of the camera is accomplished by the program CALMTX (Calibration Matrix acquisition). This program operates the calibration control of the interface, to generate the co-ordinates of the grid of markers. This data is then decoded and stored on tape in the format:- vertical co-ordinate followed by horizontal co-ordinate, for each point detected in the video signal. No time (field) co-ordinate or camera code is stored as these parameters will not change during data acquisition. Having acquired the grid co-ordinates the program then goes on to reduce this data, by simple averaging, to one co-ordinate pair per grid point. The results of this data reduction are displayed so that the success of the grid acquisition can be verified. If the program is allowed to continue then the reduced grid data is re-organised into a calibration matrix for use with the program CAL (Calibrate). This matrix consists of two arrays, one for the vertical co-ordinates and one for the horizontal co-ordinates. The positions of the co-ordinates in the arrays correspond to the positions of the markers on the grid. The results of this data reorganisation are displayed and if satisfactory may be stored on tape.

5.5.4 Calibration

The calibration program, CAL, is designed to implement the first method of calibration outlined in section 3.3.5. The locations on tape of the calibration matrix and the data to be calibrated are provided in response to questions

displayed on the VDU (Video Display Unit). The starting block on tape to which calibrated data is to be written is also provided in this way. Once calibration of the data has been completed these questions are repeated and a new set of data may be calibrated. The program processes the data, television field by television field. First of all the data for one complete field is examined and co-ordinate pairs which meet a proximity criteria are averaged to provide one pair per marker. The subsequently reduced data is then calibrated against the calibration matrix. The flow chart and listing for this program in Appendix A3, show how this is done.

5.5.5 Data Display

A display program (DISC-CAL) provides a means of displaying calibrated data. 1024 co-ordinate pairs are read from tape into a display buffer, 512 of these pairs may be displayed at any one time. Selection of the starting point of the display is made by setting a potentiometer (analogue channel 0) on the computer. The position in the display of a cursor is controlled by a second potentiometer (analogue channel 4), and the camera number, field number, and the values of the vertical and horizontal co-ordinates of the point indicated by this cursor are displayed in the top left corner. If the analogue channels were sampled this is indicated by the display of the letters FP (Force Plate). A typical display produced by this program is shown in Figure 5.5. A second version of this program (DISF-CAL) displays a cursor for every point in the field selected by the potentiometer setting of analogue channel 4. The field number is displayed in the top left corner as shown in Figure 5.6.

Three other programs based on the above display programs have been written. These versions allow uncalibrated data to be displayed on the VDU. The data is scaled to fit the grid dimensions of the VDU. The first version, DISC, displays co-ordinate data together with a read out of the co-ordinates and other information of the point in the display indicated by a cursor. The second version, DISF, is similar except that a cursor for every point in a selected field is shown on the display. The third version, DISG, is for

displaying grid data acquired by the program CAL-MTV. This data is organized in horizontal and vertical co-ordinates and does not include a field word. A cursor is provided to select individual points in the display and produce a read out of the co-ordinates. The memory location

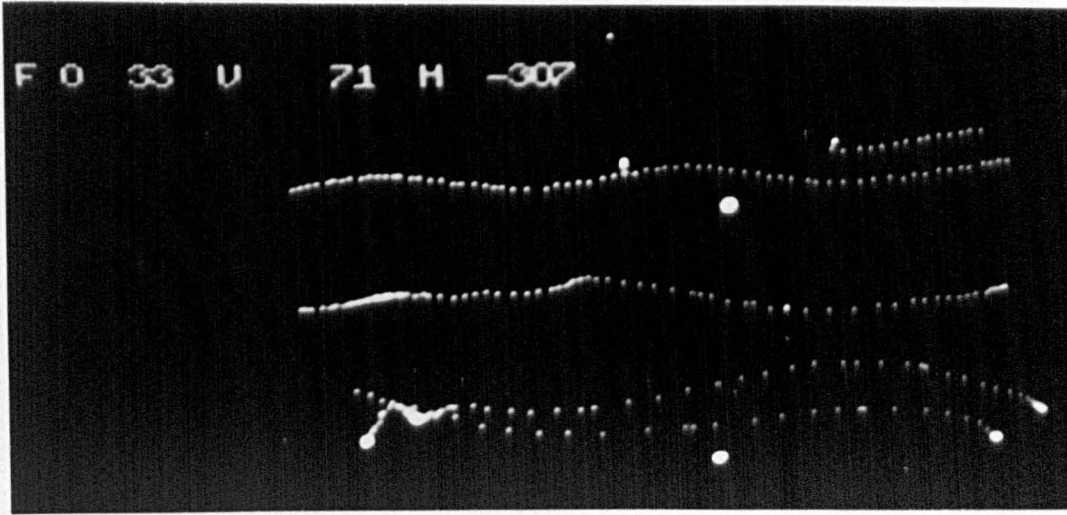


Figure 5.5. Computer generated display produced by the program DISC-CAL from marker trajectory data. The figures are a read out of co-ordinates and camera channel number for the point indicated by the cursor.

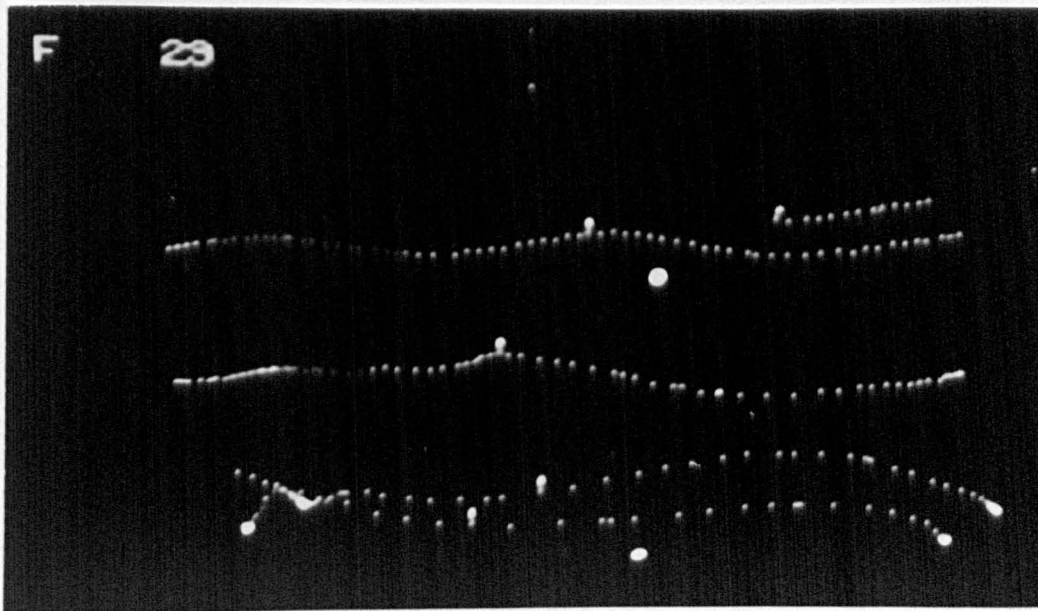


Figure 5.6. Computer generated display produced by the program DISF-CAL for the same data as in the previous figure. The cursors show the points generated during television field 29, the subject is walking from right to left.

displaying grid data acquired by the program CALMTX. This data is organised in horizontal and vertical co-ordinate pairs and does not include a field word. A cursor is provided to select individual points in the display and produce a read out of the co-ordinates. The memory location (with the beginning of the initial tape block considered as location zero) of the point is also indicated. This program would provide the basis of an editing facility to remove unwanted points from grid data.

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6.2	Objectives
6.3	Program Description
6.4	Hardware
6.5	Software
6.6	Implementation
6.7	Effect of Input
6.8	Implementation of Input
6.9	Conclusion
6.10	Future Work

CHAPTER 6

RESULTS AND DISCUSSION

- 6.1 Introduction
- 6.2 Interface Design
- 6.3 System Performance
 - 6.3.1 Stability
 - 6.3.2 Moving Markers
 - 6.3.3 Extraneous Light Sources
 - 6.3.4 Errors and Resolution
- 6.4 Calibration
- 6.5 Gait Data Acquisition and Marker Trajectory Identification
- 6.6 Simultaneous Data Processing and Data Rates
- 6.7 Effect of Interface on Overall Computer System
- 6.8 Recommended Modifications and Additions
- 6.9 Conclusions
- 6.10 Future Work

6.1 Introduction

This chapter describes how the system was tested in all aspects of its design and in its suitability for human locomotion analysis. Measurement errors and methods of calibrating the data are discussed. The co-ordinates generated by the interface for the trajectories of markers placed on a walking subject are presented and a means of identifying markers from co-ordinate data is described. Some modifications to the basic design are suggested to give improved performance. The performance of the system is summarised and comparisons are made with other methods. The applications of the system are discussed and recommendations for future work are given. A photograph of the interface and its connecting cables to the PDP 12 is shown in Figure 6.1. A subject with markers in place is shown in Figure 6.2, the photograph was taken from behind the television camera light source in normal room lighting and gives an impression of the brightness of the markers compared with skin tones.

6.2 Interface Design

The efficacy of the interface design was evaluated using computer programs and the simulator. Initially programs similar to those shown in the examples of section 5.3 were used. These programs allowed the interface to operate under constant conditions, when the simulator was used to provide the marker detector inputs. It was thus possible to check that registers in the interface had been loaded with the correct data, that control logic was functioning correctly and that data transfers were taking place. Several important factors were revealed during the initial testing phase, which resulted in minor modifications to the original design.

The first modification was to terminate some of the signal lines from the PDP 12 computer. It was found that noise on the IOT pulses ([100] BIOP 1-4) and the initialise lines ([100] BA and BB INITIALIZE) could occasionally cause false operations. This problem was solved by terminating these lines with the circuit shown in Figure 6.3, as recommended in Digital (1972). The other signal lines from the PDP 12 were also extremely noisy, and although this did

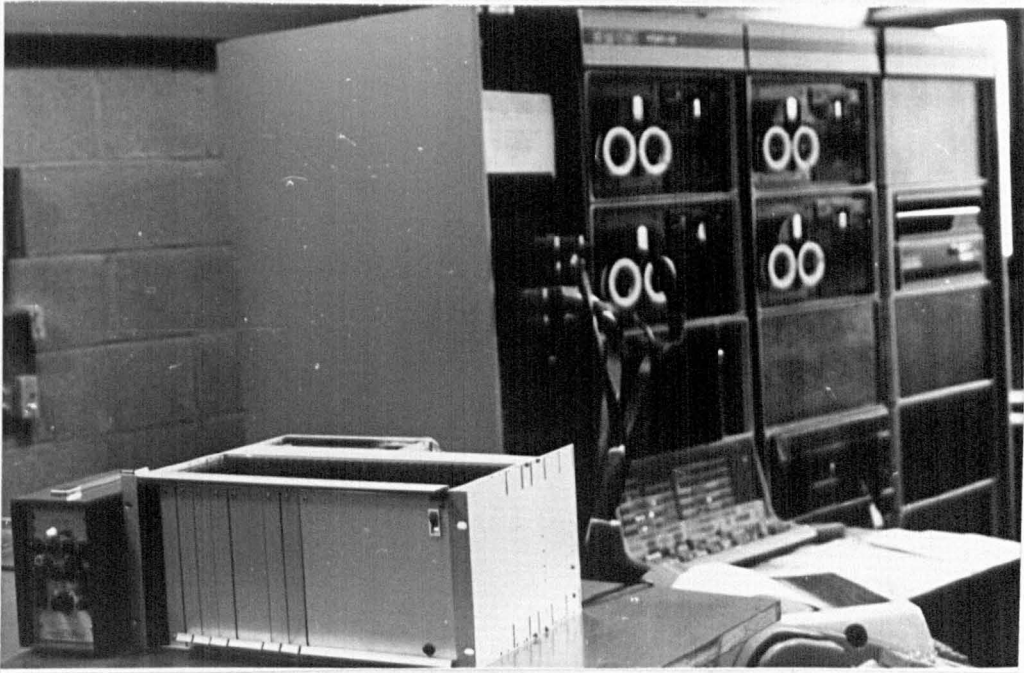


Figure 6.1. Television interface connected to the PDP 12.

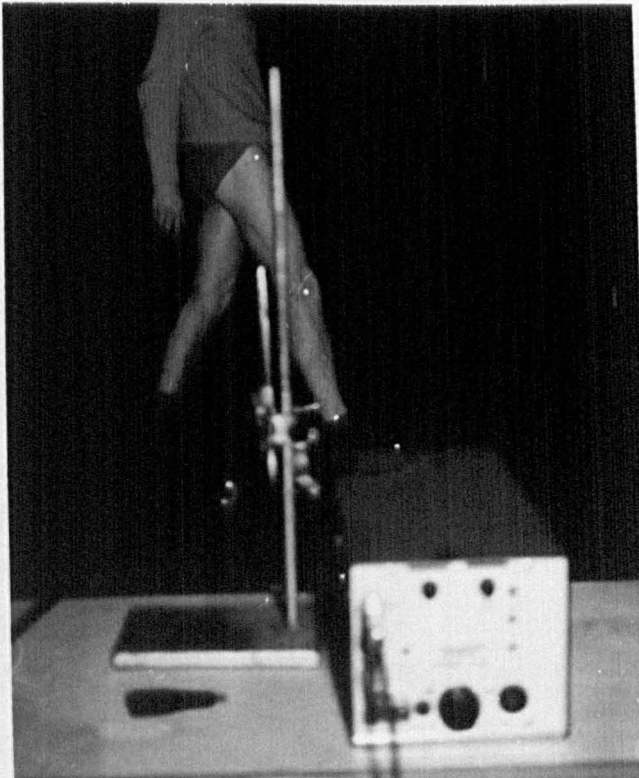


Figure 6.2. Subject with reflective markers on anatomical landmarks, the lighting is positioned to obtain maximum brightness from the markers as seen by the television camera.

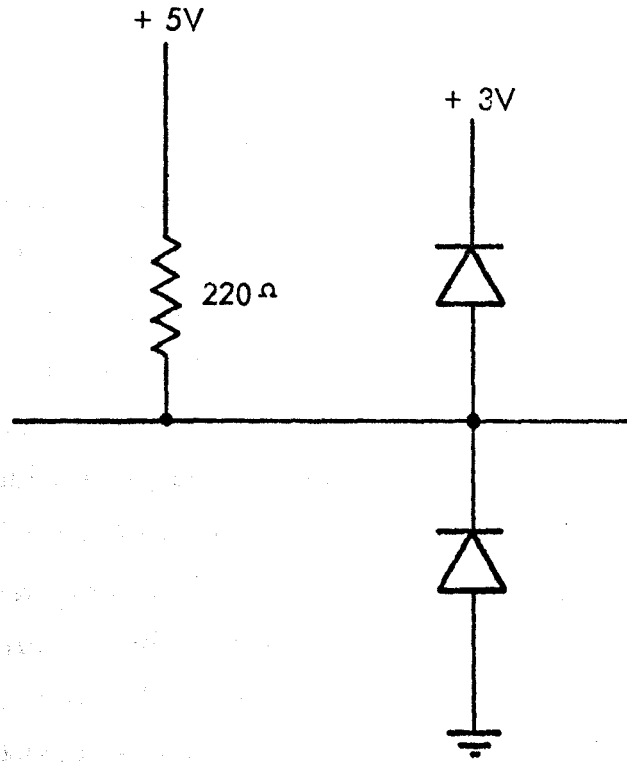


Figure 6.3. Terminating circuit for computer bus lines to reduce noise.

not appear to present any problem they were terminated with similar circuits as a precaution (Accumulator lines [100] BAC 0-11, Memory Buffer lines [100] BMB 0-11, Data Break control lines [100] BREAK and [100] ADD ACCEPTED). The signals on these lines were "cleaned up" considerably by terminating, and thus the chance of false generation of logic signals in the interface was reduced.

The second of these modifications concerned the timing of the data transfer to computer memory. Originally the address accepted signal ([100] ADD ACCEPTED (0) H) on the PDP 12 Data Break control lines was used to strobe co-ordinate data onto the 10 bus data lines. The falling edge of this signal was also used to down count the address of the random access buffer memory in the Co-ordinate Generator, to access the next co-ordinate word in readiness for the next data transfer cycle. According to the "Single Cycle Data Break Input Transfer Timing Diagram", reproduced in Figure 6.4, this should have been satisfactory; however it was found that data was not being stored in computer memory. It will be seen from this timing diagram that data must be specified by time TP2 and will be strobed into a Central Processor register at TP3. The Address Accept pulse starts at TP1 and ends at TP3, and should therefore present the interface data on the 10 bus at the correct times. Furthermore the internal computer logic signal in the Central Processor which ends the Address Accept pulse also causes the data presented on the 10 bus to be stored. As there is a typical delay of 43 nS in the interface before the data is removed from the 10 bus there should have been ample time for the data to be stored in the Central Processor (there would also be additional time caused by the connecting cable delays). Consultation with Digital Equipment Corporation engineers confirmed that this method of operation, although not normal practice, should have been satisfactory. The problem was avoided by using the logic described in section 4.4.5, which extended the duration of the Address Accepted pulse. An alternative solution, which provides all the necessary control signals required in the interface, is shown in Figure 6.5.

The final modification required was to the horizontal Co-ordinate Counter in the Co-ordinate Generator. Originally three SN 74161 binary counters

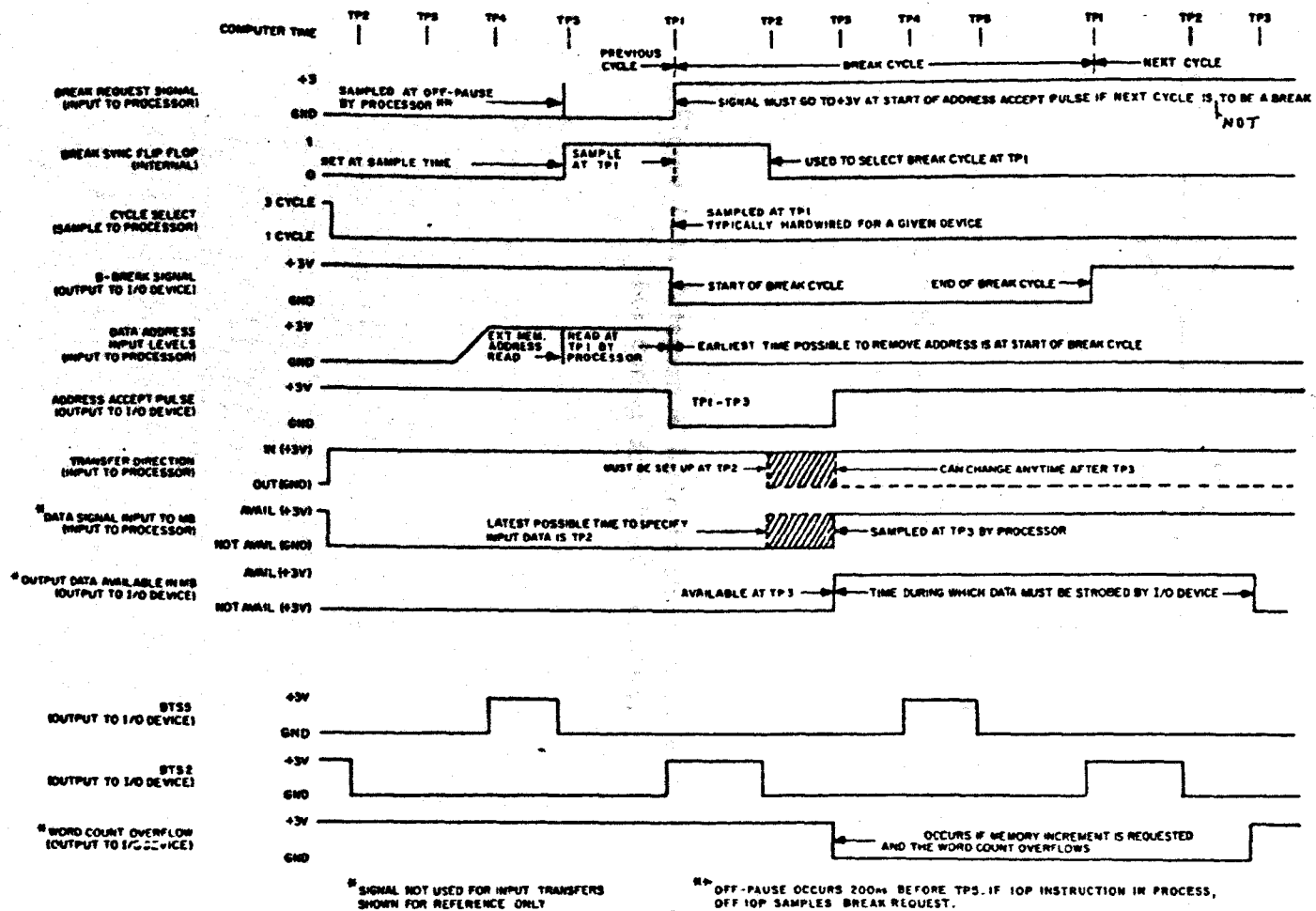


Figure 6.4. Single cycle data break input transfer timing diagram. (From Digital (1971)).

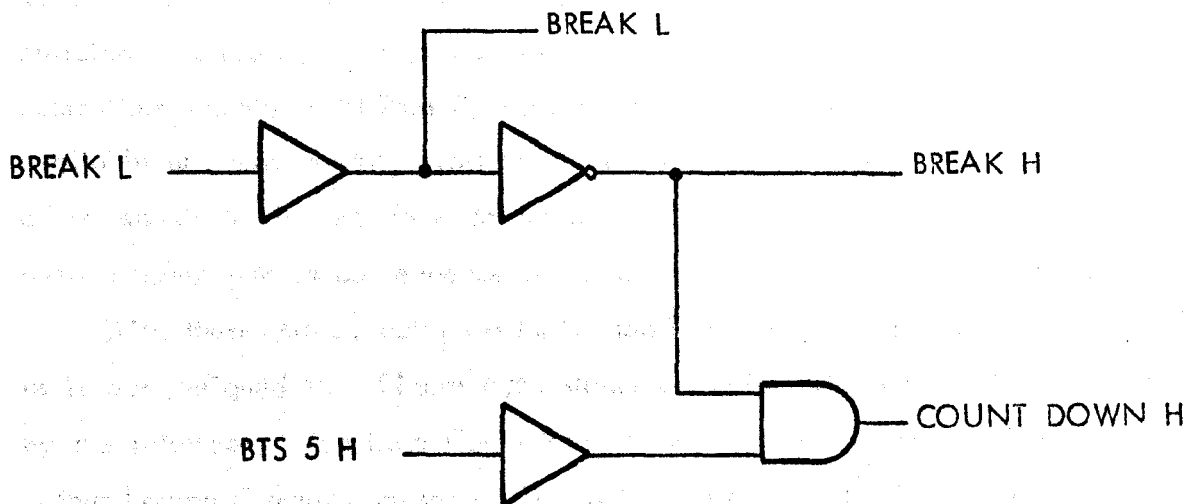


Figure 6.5. Alternative logic to strobe data on to computer bus during data break transfers.

were used in cascade. From the manufacturer's published characteristics for this device it was reasonable to expect them to operate at the design frequency of 20 MHz (Texas Instruments (1973a and 1973b), a typical maximum operating frequency of 25 MHz when used in cascade being quoted. However it was found that the devices would not operate at 20 MHz and a modification had to be made. This could have taken the form of "carry anticipate" circuitry as described in Texas Instruments (1973a) but a pin compatible counter (S.N 74S163) with higher frequency capabilities became available and was directly substituted as described in section 4.4.3. In all other aspects of the interface design a "worst case" design approach had been adopted and in consequence no other problems of this nature occurred.

With these minor, but important, modifications the interface functioned as it was designed to. Figure 6.6. shows a partial print out of data generated by the interface. The Line Count Register was set to 37_8 and a simulator output having 7 marker pulses on it provided the input to the marker detector of channel \emptyset . The first column of the print out is the location in memory of the vertical co-ordinate which is shown in the next column. The next five columns are the horizontal co-ordinates of five marker pulses and the final column contains the Shift Register code word. The interface was generating co-ordinates on every television field, and the data generated over two fields is shown here. It can be seen that the horizontal co-ordinates remain almost constant as expected. The slight variations are due to one of two causes. Where there is only a 1 bit change then this could be due to the uncertainty caused by a marker pulse falling between two counts. Any greater change is most likely due to the fact that the simulated pulses are generated by monostable multivibrators with pulse durations of up to $42 \mu S$, a slight variation in this time would produce such a change in co-ordinate value. Such variations are not critical and do not impair the usefulness of the simulator. The stability of the Co-ordinate Generator when used with camera inputs is discussed in the next section. It can also be seen from this print out that, although the simulator is set to generate pulses on every 37_8^{th} line, the first line in each television field on which co-ordinates appear is line 41_8 . This

0541	6041	1113	0750	0461	0244	0041	1400
0550	6100	1114	0751	0462	0245	0042	1400
0557	6137	1114	0751	0462	0245	0042	1400
0566	6176	1113	0750	0461	0244	0041	1400
0575	6235	1114	0751	0462	0245	0042	1400
0604	6274	1113	0750	0461	0245	0041	1400
0613	6333	1113	0750	0461	0245	0041	1400
0622	6372	1114	0751	0462	0245	0042	1400
0631	6431	1114	0751	0462	0245	0042	1400
0640	6470	1113	0750	0461	0245	0041	1400
0647	6531	1114	0751	0462	0246	0042	1400
0656	6570	1113	0750	0461	0245	0041	1400
0665	6627	1114	0751	0462	0246	0042	1400
0674	6666	1113	0750	0461	0245	0041	1400
0703	6725	1113	0750	0461	0245	0041	1400
0712	6764	1113	0750	0461	0245	0041	1400
0721	7023	1114	0751	0462	0246	0042	1400
0730	7062	1113	0750	0461	0244	0041	1400
0737	7121	1114	0751	0462	0246	0042	1400
0746	7160	1114	0751	0462	0245	0042	1400
0755	6041	1113	0750	0461	0244	0041	1400
0764	6100	1114	0751	0462	0245	0042	1400
0773	6137	1114	0751	0462	0245	0042	1400
1002	6176	1113	0750	0461	0244	0041	1400
1011	6235	1114	0751	0462	0245	0042	1400
1020	6274	1113	0750	0461	0245	0041	1400
1027	6333	1113	0750	0461	0245	0041	1400
1036	6372	1114	0751	0462	0245	0042	1400
1045	6431	1114	0751	0462	0245	0042	1400
1054	6470	1113	0750	0461	0245	0041	1400
1063	6531	1114	0751	0462	0246	0042	1400
1072	6570	1113	0750	0461	0245	0041	1400
1101	6627	1116	0750	0461	0245	0041	1400
1110	6666	1113	0750	0461	0245	0041	1400
1117	6725	1113	0750	0461	0245	0041	1400
1126	6764	1113	0750	0461	0245	0041	1400
1135	7023	1113	0750	0461	0245	0041	1400
1144	7062	1113	0750	0461	0245	0041	1400
1153	7121	1114	0751	0462	0245	0042	1400
1162	7160	1113	0750	0461	0245	0041	1400

Figure 6.6. Print out of data generated by the system for simulated marker inputs. The first column shows the memory location of the vertical co-ordinate which is in the next column. The next five columns are the horizontal co-ordinates for five markers and these are followed by the shift register code word in the last column.

is due to the fact that the Line Counter is reset at the beginning of each field by the field sync. This print out also shows that further co-ordinate generation was inhibited on each line after five markers had been detected.

The program DYDIS is used extensively to check correct system operation, again by using the simulator to provide marker detector inputs. Figures 6.7 and 6.8 are photographs of the display produced while this program is running. Four simulated marker signals on each of 8 television lines in every field are being generated (data rate of 2400 words/sec). Three of the markers are on channel 0 and one marker is on channel 1 shown in Figures 6.7 and 6.8 respectively.

6.3 System Performance

The ability of the system to generate co-ordinates of markers and provide repeatable measurements is shown here. The material used for markers and the positioning of light sources has been discussed in section 2.2.1. The same degree of lighting has been used for all the tests reported here. This consisted of a single Malham SE23 lamp fitted with a 500W tungsten halogen bulb positioned as close as possible to the television camera lens. This configuration was used for each camera where two cameras were used. Normal room lighting was maintained.

6.3.1 Stability

To ascertain that the system could provide repeatable measurements a stability test was carried out. Stationary markers were used mounted on a board in the configuration shown in Figure 6.9. The distances between markers are also shown in this figure. The field of view covered by the camera in the plane of this board was 2.8 m by 2.1 m and the marker size required would be 7.7 mm calculated as shown in section 2.2.2. It was found, because there is a certain degree of flare, that a marker of 5 mm diameter could cover one or two television lines and this size of marker was used for the stability test. A thirty minute warm up time was allowed and then the co-ordinates of the stationary markers were generated by the interface. The interface was set to sample the spatial position of the markers once every 255th television field

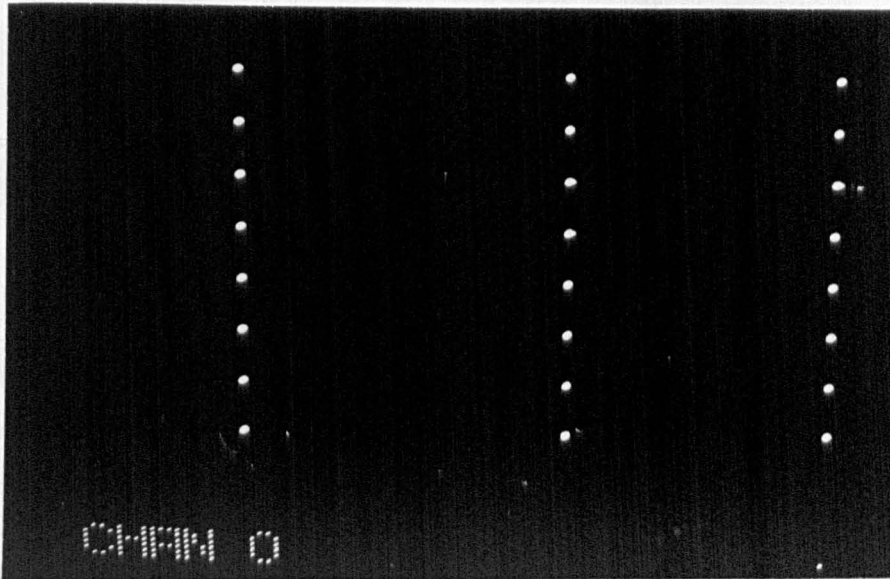


Figure 6.7. Real time computer display of data generated by the interface using the simulator. Channel \emptyset data.

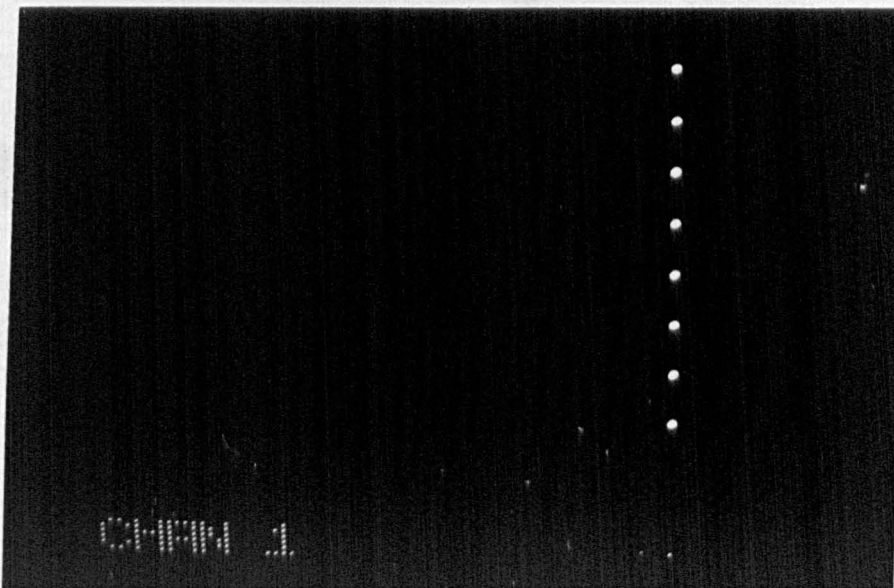


Figure 6.8. Data generated for channel 1 at the same time as that for channel \emptyset shown above.

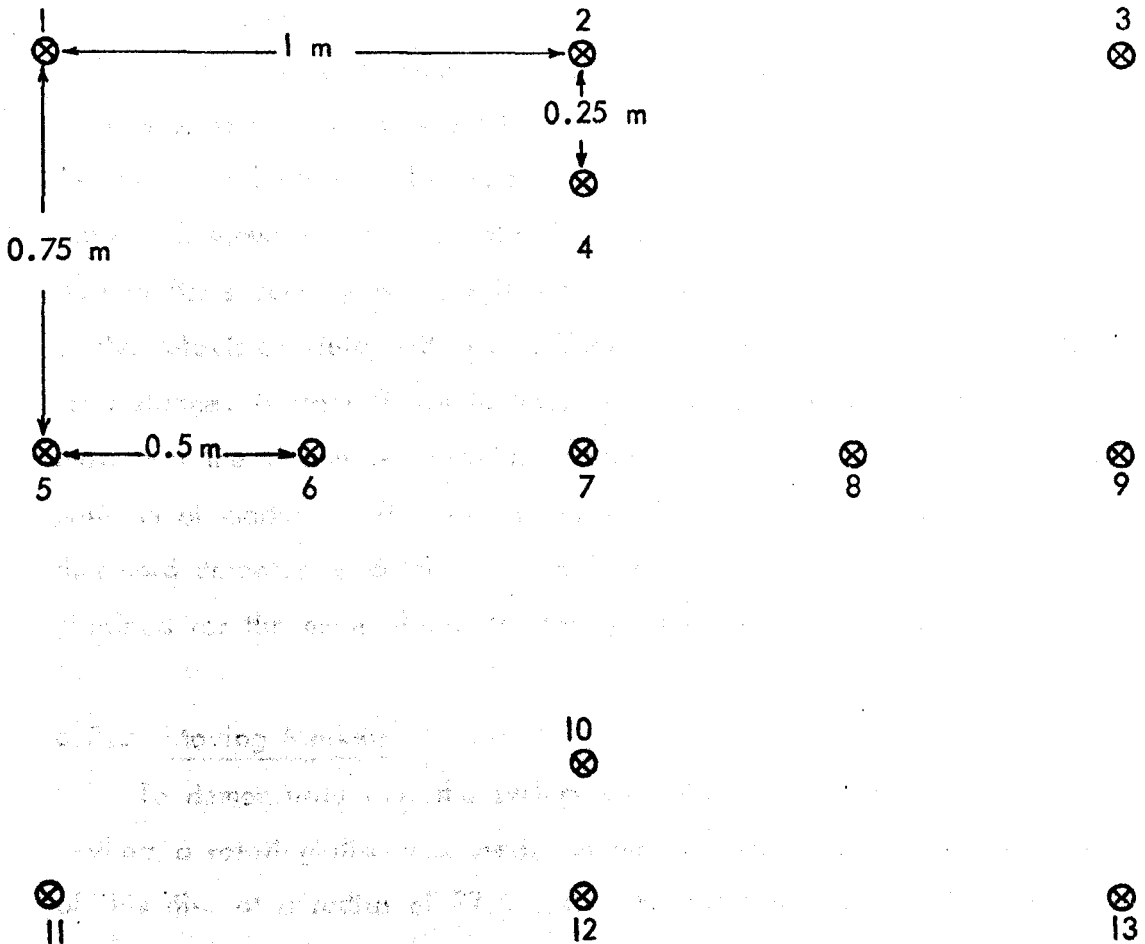


Figure 6.9. Configuration of markers used to test the stability of the system.

(5.1 sec). Sixteen sets of co-ordinates were obtained and then the system was left for two and a quarter hours after which time a further sixteen sets of co-ordinates were obtained. The co-ordinates generated for each marker were then compared. Almost perfect agreement was found between each set of data. The co-ordinates generated for markers 10, 11, 12 and 13 are shown in Tables 6.1 and 6.2, similar agreement was found between the repeated measurements for the other markers. Table 6.1 shows the co-ordinates for the "even" television field (vertical co-ordinates between 0 and 312) and Table 6.2 shows the co-ordinates for the "odd" fields. These particular markers have been chosen for illustration because they occur towards the end of the television field and any malfunction in the vertical co-ordinate counter, for instance, is more likely to have occurred by this time. These results show that the system can provide repeatable measurements of the spatial position of markers. The results shown above were obtained using a simple threshold detector to detect markers in the video signal. Similar results were obtained for the more elaborate marker detector described in section 2.2.4.

6.3.2 Moving Markers

To demonstrate that the system can generate the co-ordinates of moving markers a rotating disc was used. A marker was placed near the outer edge of this disc at a radius of 22.5 cm. The field of view of the camera in the plane of the disc was 3.2 m by 2.4 m and a marker size of 7 mm diameter was used. The disc was rotated by a synchronous electric motor through a variable reduction gearbox. Figure 6.10 is a plot of the co-ordinates generated by the interface for one complete revolution of the disc. The vertical co-ordinate has been scaled, the scaling factor being found from the diameter of the disc in horizontal co-ordinate units (142) and in vertical co-ordinate units (54) giving a scaling factor of 2.63. It can be seen that one complete revolution of the disc was made in 19 television fields or 380 mS (2.63 revs/sec). The marker will, therefore, reach a peak velocity in the x and in the y directions of 3.7 m/S and a peak acceleration of 61.4 m/S^2 . It can be seen that at least one pair of co-ordinates was generated for the marker in every television field. The velocities and accelerations reached by the marker

TV Field	10		11		12		13	
0	239	504	275	150	273	504	270	860
2	239	504	275	150	273	504	270	860
4	239	504	275	150	273	504	270	860
6	239	504	275	150	273	504	270	860
8	239	504	275	150	273	504	270	860
10	239	504	275	150	273	504	270	860
12	239	504	275	150	273	504	270	860
14	239	504	275	150	273	504	270	860

Co-ordinates generated after 30 minute warm up period.

TV Field	10		11		12		13	
0	239	504	276	151	273	504	271	858
2	239	504	276	151	273	504	271	859
4	239	504	276	151	273	504	271	858
6	239	504	276	150	273	504	271	858
8	239	504	276	151	273	504	271	858
10	239	504	276	151	273	504	271	858
12	239	504	276	151	273	504	271	859
14	239	504	276	150	273	504	271	858

Co-ordinates generated 135 minutes later.

Table 6.1. Co-ordinates generated by the interface for markers 10, 11, 12 and 13 of Figure 6.9 during "even" television fields, showing the stability of the system. The interface was set to sample at a slow rate, one field every 5.1 secs.

TV Field	10		11		12		13	
1	551	504	587	151	585	503	583	860
3	551	504	587	151	585	503	583	860
5	551	504	587	151	585	503	583	860
7	551	504	587	151	585	503	583	860
9	551	504	587	151	585	503	583	860
11	551	504	587	151	585	503	583	860
13	551	504	587	151	585	503	583	860
15	551	504	587	151	585	503	583	860

Co-ordinates generated after 30 minute warm up period.

TV Field	10		11		12		13	
1	552	504	588	151	586	503	583	859
3	552	504	588	151	586	503	583	859
5	552	504	588	151	586	503	583	859
7	552	504	588	151	586	503	583	859
9	552	506	588	151	586	503	583	859
11	552	504	588	151	586	503	583	859
13	552	504	588	151	586	503	583	859
15	552	504	588	151	586	503	583	859

Co-ordinates generated 135 minutes later.

Table 6.2. Co-ordinates generated by the interface for markers 10, 11, 12 and 13 of Figure 6.9 during "odd" television fields, showing the stability of the system. The interface was set to sample at a slow rate, one field every 5.1 secs.

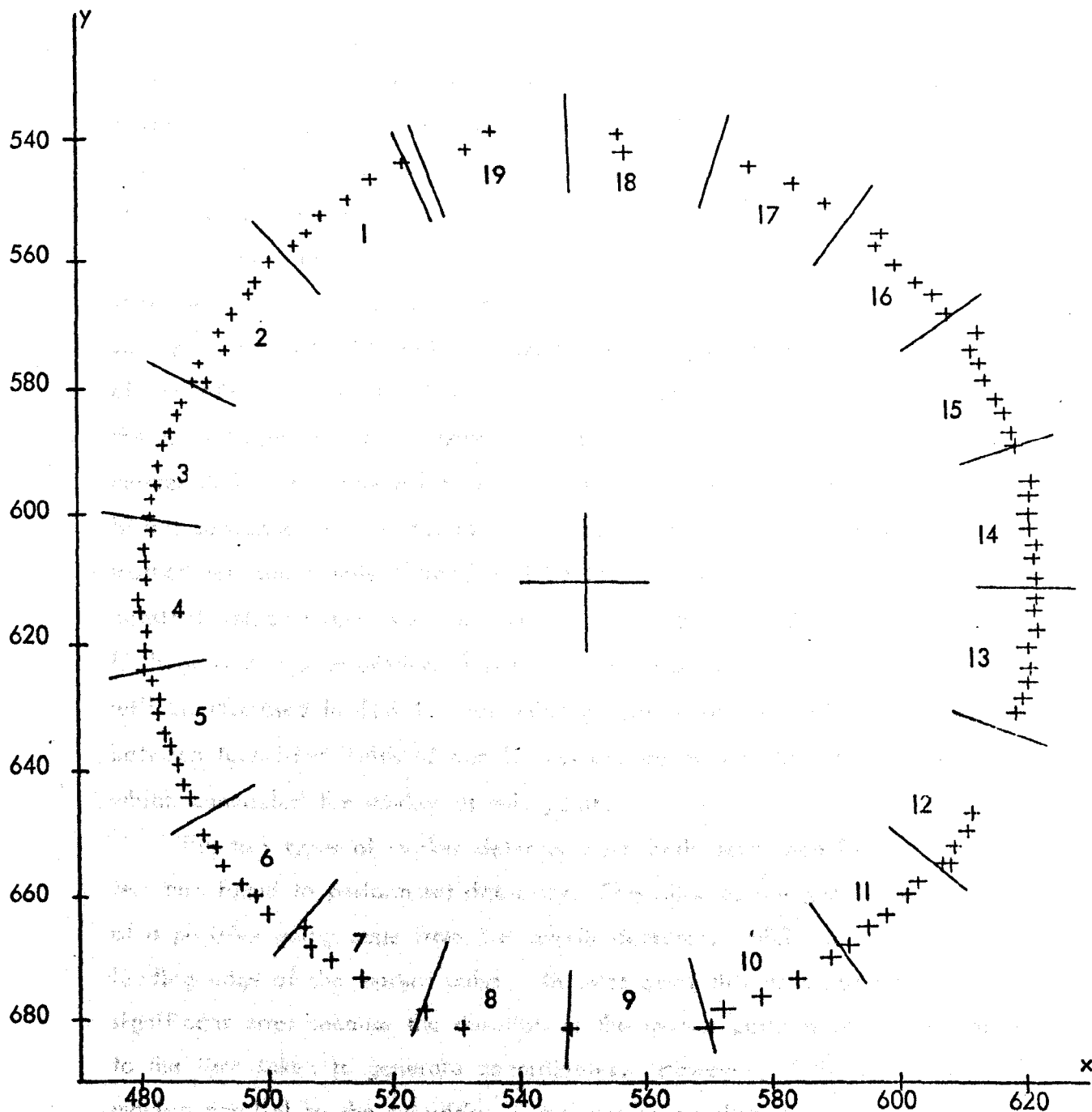


Figure 6.10. Plot of data generated by the system for a marker on the periphery of a rotating disc. The vertical co-ordinates (y) have been scaled and the television field numbers, during which each part of the trajectory was formed, are indicated.

3.5.3 Synthesised Light Sources

Co-ordinates will be generated for numerous light sources, of varying brightness in the field of view of the camera. The trajectory of each source is going to be the number of units, as indicated in Figure 6.10, of 20 units.

compare favourably, as a realistic test for a locomotion measurement system, with the maximum velocities and accelerations reported by Winter (1974) and shown in Table 6.3. The fastest moving marker in this table is the heel marker (5) for fast walking (average cadence of 114 strides/min), which reaches average maximum vertical and horizontal velocities of 1.76 m/s and 3.78 m/s respectively.

The co-ordinate data generated by the interface for the rotating markers shows up an interesting characteristic of the television camera. This is the storage property of the pick up tube target. It can be seen that the sequence of co-ordinates generated for the marker in each television field is a part of the total trajectory of the marker. That is, it is the path traversed by the marker during the time taken to scan one complete television field (20 ms). If this sequence of co-ordinates is averaged then the average position of the marker between sample instants will be found. If the marker is moving with constant velocity then there will be no error caused by this averaging process. If there is any acceleration, however, then there will be an error, which will be discussed in 6.3.4. The missing portion of the marker trajectory between television fields 12 and 13 was caused by the disc mounting assembly which concealed the marker at this point.

The two types of marker detector have both been used in this type of test and found to perform satisfactorily. Co-ordinates are generated on receipt of a positive going edge from the marker detector, which corresponds to the leading edge of the marker pulse. In most cases this does not lead to any significant error because the duration of the marker pulse is short and similar to the time taken to generate co-ordinates. However, if the marker is moving parallel to the television camera scanning lines then co-ordinates will be generated for the leading edge of the trajectory only. In Figure 6.10 it can be seen that this has probably happened on some television fields (8 and 9 for example). This would be a source of measurement error, however the logic shown in Figure 6.11 could be used and then a series of horizontal co-ordinates would be generated for a trajectory lying parallel to the line scan.

6.3.3 Extraneous Light Sources

Co-ordinates will be generated for extraneous light sources of sufficient brightness in the field of view of the camera. Precautions can be taken to ensure that the number of such sources is minimised or eliminated by removing

Average cadences: Fast—114/min. Normal—93/min. Slow—82/min

Marker No.	Speed of walk	No. of subjects	Displacement Range (cm)	Vertical (Y)		Max. acceleration (cm/sec ²)		Horizontal (X)			
				Max. velocity (cm/sec)		Up	Down	Min.	Max.	Min.	Max.
				Up	Down	Up	Down	Min.	Max.	Min.	Max.
1	Fast	8	5-9(1-2)	34(7)	32(10)	57(180)	50(190)	75(20)	154(35)	-48(190)	46(170)
	Normal	12	5-8(0-7)	31(4)	27(6)	53(120)	39(70)	66(16)	126(12)	-38(90)	36(60)
	Slow	8	4-9(0-7)	25(5)	19(5)	36(84)	28(50)	46(13)	104(15)	-40(90)	31(60)
2	Fast	8	8-9(1-8)	55(7)	41(11)	80(160)	82(340)	26(15)	235(52)	-98(530)	127(430)
	Normal	12	8-11(1-9)	46(7)	36(10)	66(150)	62(100)	23(11)	201(15)	-76(250)	106(210)
	Slow	8	7-11(1-2)	35(9)	25(7)	43(140)	41(100)	19(7)	156(30)	-63(170)	69(150)
3	Fast	8	7-4(1-2)	49(9)	34(14)	72(190)	69(310)	15(11)	244(57)	-94(440)	138(260)
	Normal	12	6-6(0-9)	38(8)	31(9)	55(100)	49(100)	14(9)	204(17)	-77(230)	106(120)
	Slow	8	6-2(1-1)	29(8)	22(7)	37(120)	37(60)	12(7)	167(26)	-60(150)	80(150)
4	Fast	8	17-3(1-0)	105(26)	12(33)	128(480)	192(880)	0(0)	34(82)	-303(1390)	159(610)
	Normal	12	16-8(1-5)	97(14)	99(9)	132(610)	149(210)	0(0)	294(27)	-193(220)	117(210)
	Slow	8	16-9(1-7)	81(21)	87(14)	80(220)	130(290)	0(0)	239(34)	-147(310)	83(180)
5	Fast	8	25-0(1-8)	17(46)	16(41)	171(700)	289(1200)	0(0)	378(93)	-339(1460)	200(870)
	Normal	12	24-4(1-7)	147(13)	143(10)	135(200)	225(270)	0(0)	323(28)	-249(360)	134(160)
	Slow	8	23-9(2-4)	123(19)	123(19)	105(280)	169(290)	0(0)	266(38)	-187(390)	99(210)
6	Fast	8	11-9(1-2)	96(14)	91(13)	169(510)	201(580)	0(0)	408(103)	-326(1590)	254(530)
	Normal	12	11-8(1-6)	91(11)	77(8)	123(200)	164(200)	0(0)	338(29)	-224(330)	207(260)
	Slow	8	11-3(1-7)	85(19)	74(21)	98(210)	125(250)	0(0)	277(42)	-170(380)	153(360)
7	Fast	8	11-9(1-7)	100(16)	106(23)	155(430)	269(570)	0(0)	425(108)	-304(1360)	366(1270)
	Normal	12	12-6(2-7)	98(18)	98(6)	121(180)	232(400)	0(0)	351(30)	-210(210)	280(280)
	Slow	8	12-9(2-3)	87(16)	78(21)	107(280)	193(510)	0(0)	286(42)	-155(370)	221(380)

Table 6.3. Linear trajectory data of anatomical landmarks during locomotion. Presented by Winter (1974).

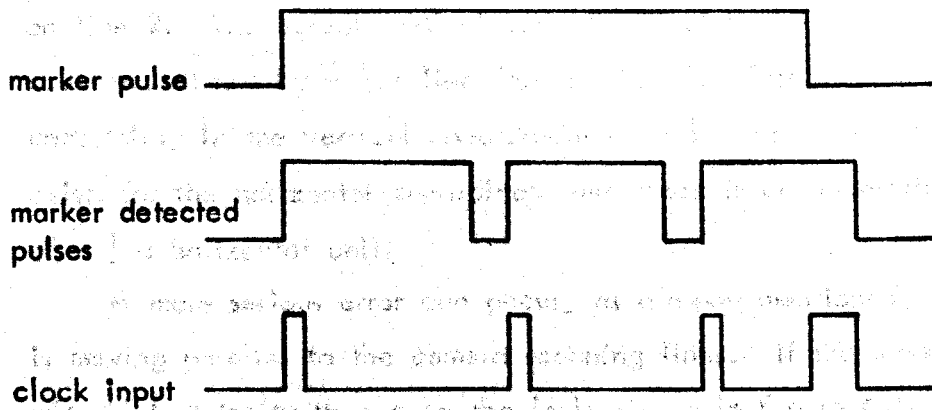
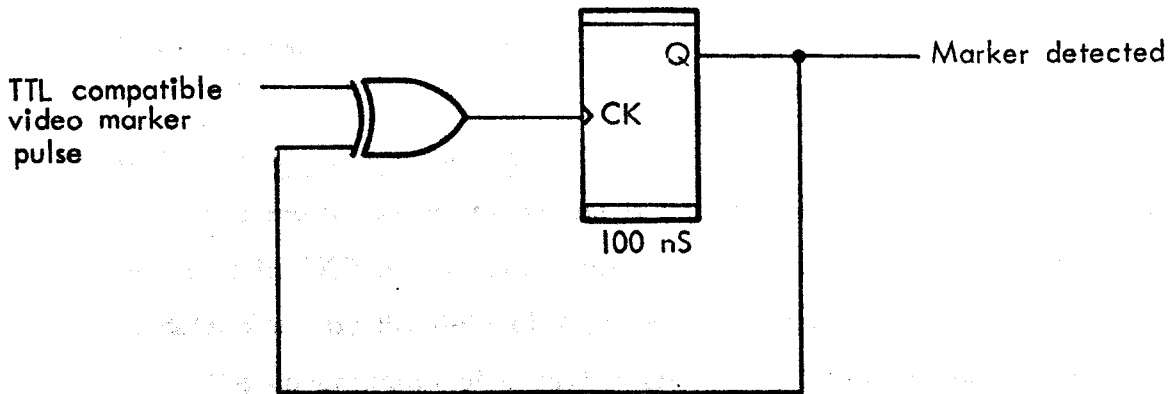


Figure 6.11. Logic and timing diagram to regenerate horizontal co-ordinates for long marker pulses.

shiny objects, and by the use of drapes. The camera lens aperture is set to obtain maximum contrast between markers and background. If co-ordinates of extraneous light sources are generated then they can be eliminated in subsequent data processing to be described in section 6.5, their only effect is to increase the data rate.

6.3.4 Errors and Resolution

The resolution of the system is one part in 292 in the vertical scan and one part in 1000 in the horizontal scan. The resolution in terms of distance is determined by the field of view of the camera.

The co-ordinates of a marker are generated when the video signal level caused by the marker rises above a pre-determined threshold. Figure 6.12 shows three markers all of which would be given the same vertical co-ordinate, because the signal level caused by each only rises above the threshold level on line 2. The actual vertical co-ordinate of the centres of markers 1 and 3, however, differs by $\pm \frac{1}{2}$ a line from marker 2. This difference represents an uncertainty in the vertical co-ordinate of $\pm \frac{1}{2}$ a line. A similar situation exists for the horizontal co-ordinate and there is an uncertainty in its position of $\pm \frac{1}{2}$ a horizontal unit.

A more serious error can occur, as already mentioned, when a marker is moving parallel to the camera scanning lines. If the marker is moving with high velocity then even the logic shown in Figure 6.11 would not provide enough co-ordinates to cover the entire trajectory. The magnitude of this error depends on the horizontal field of view of the camera and on the marker velocity. If co-ordinates are only generated at the beginning of the trajectory then the error in average position of a marker moving at 5 m/S horizontal velocity could be as high as 5 cm in a 20 mS period between samples. This error could be reduced by designing logic to generate the co-ordinates at the beginning and at the end of a trajectory. Alternatively, since in locomotion the highest components of velocity are in the horizontal direction (Winter (1974)), the camera could be turned on its side and high speed horizontal trajectories would then cross the scanning lines.

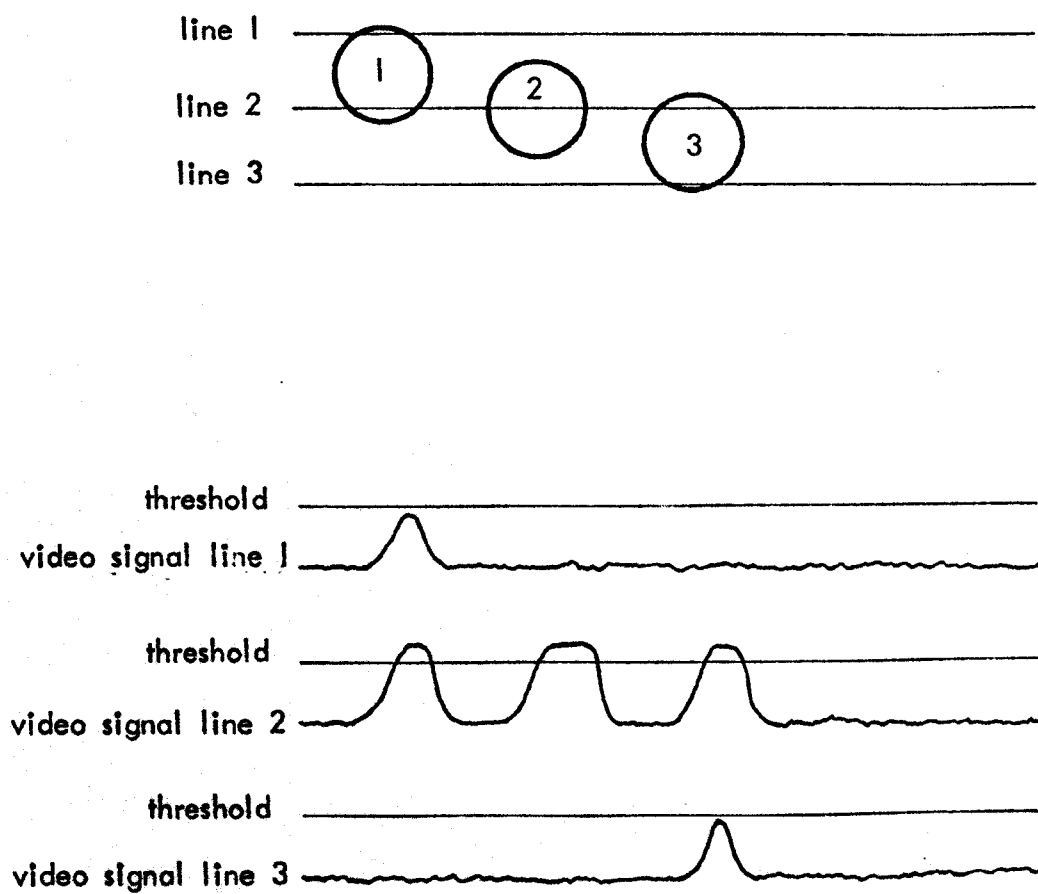


Figure 6.12. Three markers which would all be given the same vertical co-ordinate (line number).

The error caused by averaging a marker trajectory between sample instants is proportional to the acceleration of the marker during this interval and is given by $-1/8 a t^2$ (for constant acceleration), where a is the acceleration and t is the time between sampling instants. For this system t is 20 mS so the error becomes $-0.05 a \cdot 10^{-3}$. For an acceleration of 50 m/S^2 an error of 2.5 mm would arise from the averaging process. Since such high accelerations are only occasionally reached it would seem that averaging of marker trajectories is an acceptable procedure for most applications.

There is an inherent phase difference in sampling instants for different markers. This depends on their spatial relationship to each other with respect to the television camera scanning raster. The position of a marker in mid field, for example, would be sampled some 10 mS later than a marker at the beginning of the field. The range of vertical movement of anatomical landmarks tends to be restricted in locomotion and there would, therefore, be little phase error between samples for the same marker. Corrections can be made for phase errors between markers because the position in the scanning raster of a marker is related to time.

Phase errors and errors due to averaging of trajectories could be eliminated by the use of a rotating shutter. Such a method is used in the Dutch system Ingen Schenau (1973). The shutter consists of an opaque disc with a transparent window, it rotates in synchronism with the television field synchronising pulse so that the window is positioned over the camera lens during the field blanking period. The size of the window is chosen so that the camera is exposed to the field of view for approximately 1 mS. The light pattern picked up is stored on the camera target plate until it is scanned. The disadvantage with using this method is that a camera tube with a very rapid response is required in order to register the reduced light levels in the short exposure time. Such tubes tend to be much more expensive than the normal Vidicon tube.

The television camera scanning raster is not perfectly linear and hence errors will arise due to these non-linearities. The manufacturer's specification quotes a maximum non-linearity of $\pm 1\%$ in the line scan and $\pm \frac{1}{2}\%$ in the

field scan, with the total error not occurring in less than half a line or half a field (KGM (1971)). A corresponding non-linearity will be introduced into the co-ordinates of markers detected in the video signal. Corrections can be made for this error by referring measurements to a calibration grid, either directly or by the use of equations as discussed in section 3.3.5.

Errors due to parallax can be corrected, if the system is used in a three dimensional configuration, by the same techniques that are used in cine film analysis (e.g. Paul (1967)).

6.4 Calibration

Data can be calibrated by generating the co-ordinates of the spatial position of markers at a known separation. This was effectively done to plot out the trajectory of the marker on the rotating disc of Figure 6.10, and it can be seen that the circle has been faithfully reproduced.

It has been possible to acquire the co-ordinates of a grid of markers using the Calibration Control, but it has been necessary to include a sloping row of markers at the top of the grid. The top row of markers is used to set the limits for the Calibration Control window referred to in section 3.3.5. For the control to function correctly markers must either slope downwards from left to right with respect to the television camera scan line or be horizontal to this line. If the markers slope upwards then the control will set the wrong window limits. This situation was catered for in the design by incorporating a "free run mode" and a monitor output. The control would be operated in this mode and the camera rotated until it could be seen on the monitor that the control was functioning correctly. It was found, however, that it is inconvenient to rotate the camera and that the control could operate successfully off a sloping row of markers. An example of a grid of points generated by the interface using this control is shown in Figure 6.13. A new design for a simplified Calibration Control is discussed in section 6.8.

The operation of the calibration program CAL was successful when a simulated calibration matrix was used to calibrate real data. Some 2000 co-ordinate pairs were reduced to one co-ordinate pair per marker in each

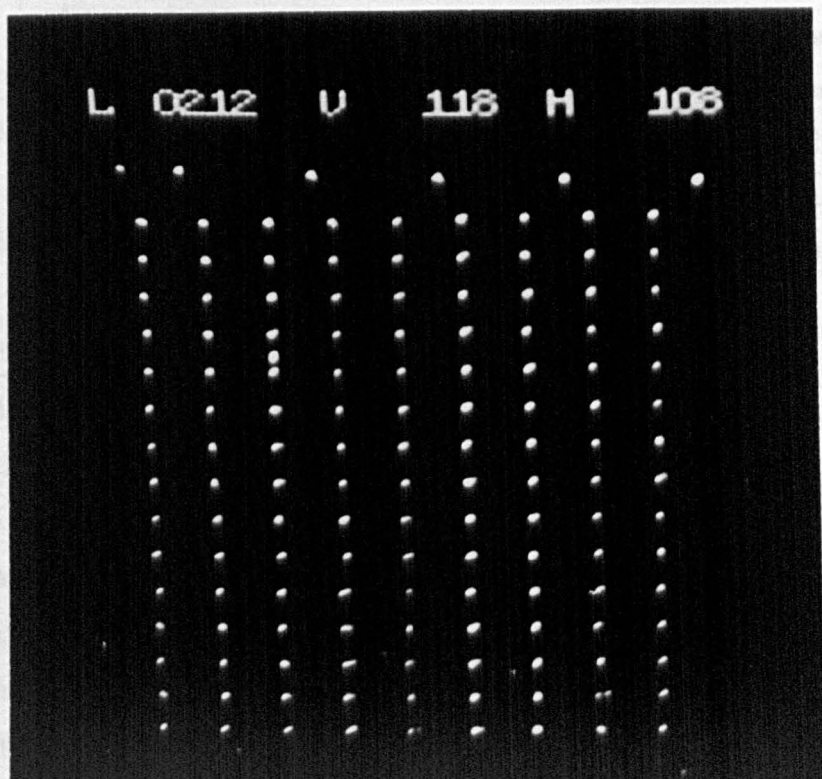


Figure 6.13. Data points generated by the interface for a grid of markers using the calibration control. The sloping row of markers used to set up the window limits can be seen at the top.

television field and calibrated against the calibration matrix in a matter of seconds. The part of the grid data acquisition program (CAL MTX) which organised the grid data generated by the interface into a calibration matrix was not however successful. One factor which occasionally caused malfunction of the program was that the number of co-ordinates collected for the grid markers was underestimated. The program had been written to cater for a maximum of 2000 co-ordinate pairs and this limit was sometimes exceeded. Also no facility was provided by this program to select out unwanted co-ordinates, such as those generated for the sloping row of markers, which would lead to errors in building the calibration matrix. Such a facility could be provided very effectively by utilising the visual display unit of the PDP 12.

6.5 Gait Data Acquisition and Marker Trajectory Identification

In the first example of gait data generated by the system data was collected simultaneously for two television channels and the force plate. One of the television channels was connected to a simulator output which generated a marker pulse half way down the television field (line 160). The second television channel was connected to a camera output to record the side view trajectories of markers placed on a subject's limb. Markers were placed over the hip, knee and ankle joints and on the foot. Some of the data generated for these markers is shown in Figure 6.14 the read out for the cursor showing that the force plate was also sampled during the television field indicated. This is a display produced on the PDP 12 visual display unit by the program DISC (Display with cursor). The number of points displayed is limited by the computer program to 512, this limitation being necessary to avoid display flicker. The co-ordinates shown are unprocessed in that they have not been averaged to produce one co-ordinate pair per marker in each television field. Data that has been processed in this way (by the program CAL) is shown in Figure 6.15; this data was also calibrated against a simulated calibration matrix. Here the complete trajectory is shown, and also a stationary marker that was positioned in the field of view of the camera to provide a check on the stability of the system during this test. No significant change was observed in the co-ordinates generated for this marker. At the same time that

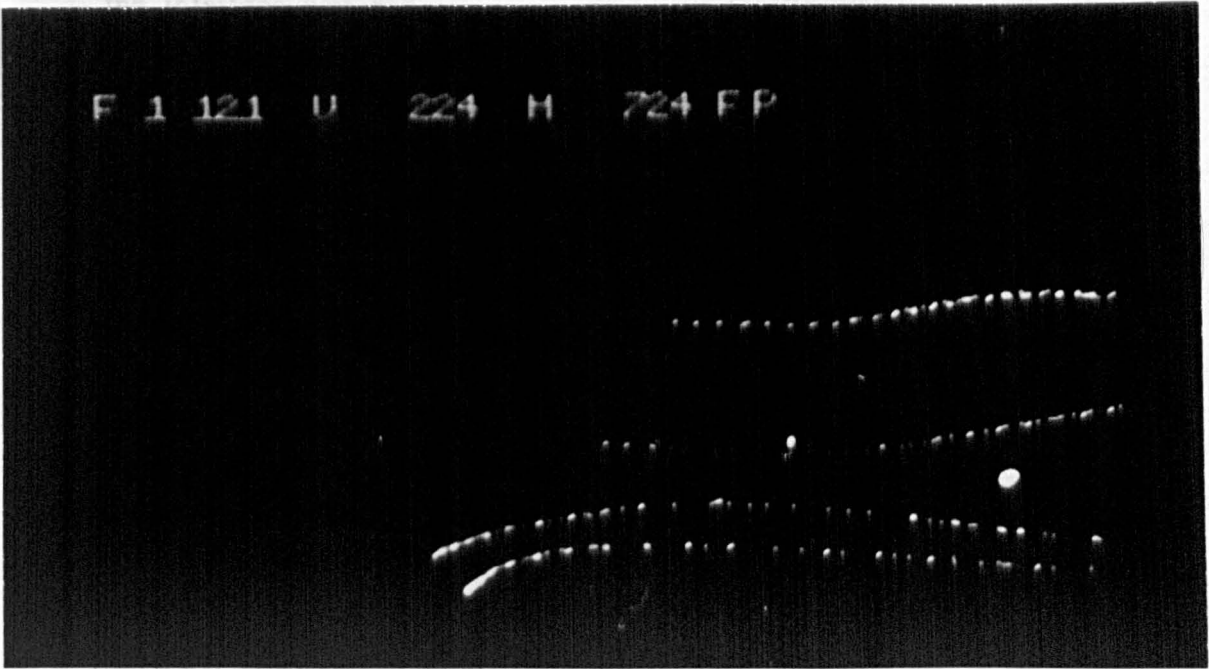


Figure 6.14. Computer display of unprocessed data showing part of the trajectories of markers placed on a subject's limb. Markers were placed on the hip, knee, ankle and foot.

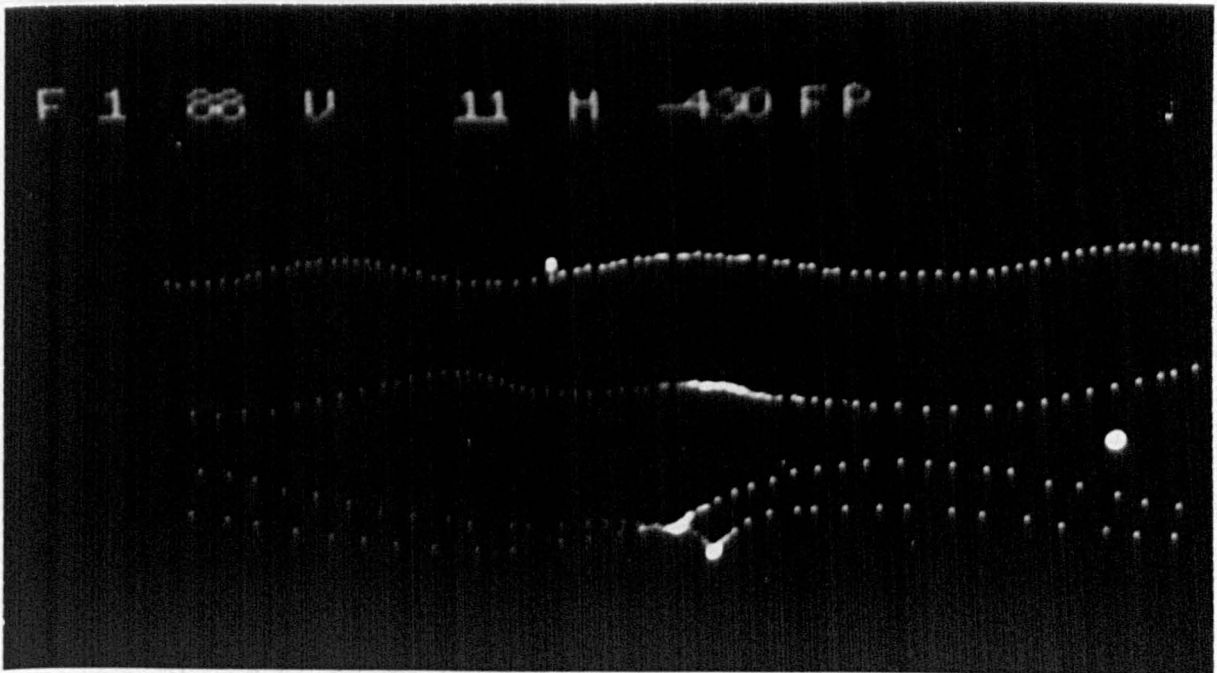


Figure 6.15. Data of Figure 6.14 averaged to produce one co-ordinate pair per marker per television field and calibrated against a simulated calibration matrix. The bright spot is a stationary marker placed in the field of view to check stability.

the interface was generating co-ordinate data the computer was sampling the six analogue channels of the force plate. This data is shown in Figure 6.16.

The combination of data inputs described above tested a number of aspects of the interface design. The internal logic signals which generate the simulated marker pulse also generate the clock pulses which are used to initiate analogue to digital conversions of the force plate transducer signals. This means that co-ordinate data is actually being generated and transferred to computer memory between analogue channel samples. This showed that even time critical computer programs can be successfully run during interface operation. Also the multi-channel capability of the interface is illustrated by this test.

The data generated by the interface consists of a set of co-ordinate pairs for each television field. Co-ordinates may occasionally be generated for extraneous light sources in the field of view of the camera, and trajectories of markers may cross, making marker identification difficult. Obviously it is necessary to define which co-ordinates belong to which markers so that the data can be analysed. Marker identification can be achieved by the application of a linear extrapolation routine such as that reported by Ingen Schenau (1973). With this method the first two points of a marker trajectory are identified and then the routine automatically locates the remaining points. This is done by making an estimate of the expected marker positions from the two previous positions, a boundary is placed around this estimate and a search is made for a co-ordinate pair within this area. The estimated x co-ordinate is found from

$$x_{t+2} = x_t + 2(x_{t+1} - x_t)$$

where t is the time co-ordinate.

The estimated y co-ordinate is found from a similar equation.

The process of extrapolation and search is illustrated in Figure 6.17. The extrapolation assumes that velocity of the marker is constant and accelerations are allowed for by the search boundary. The data from the test discussed above has been processed in this way, the trajectory for each marker being identified and stored separately. The resulting data was plotted on the

Figure 6.16. Ground reaction force data at the same time as the data shown in Figure 6.15.

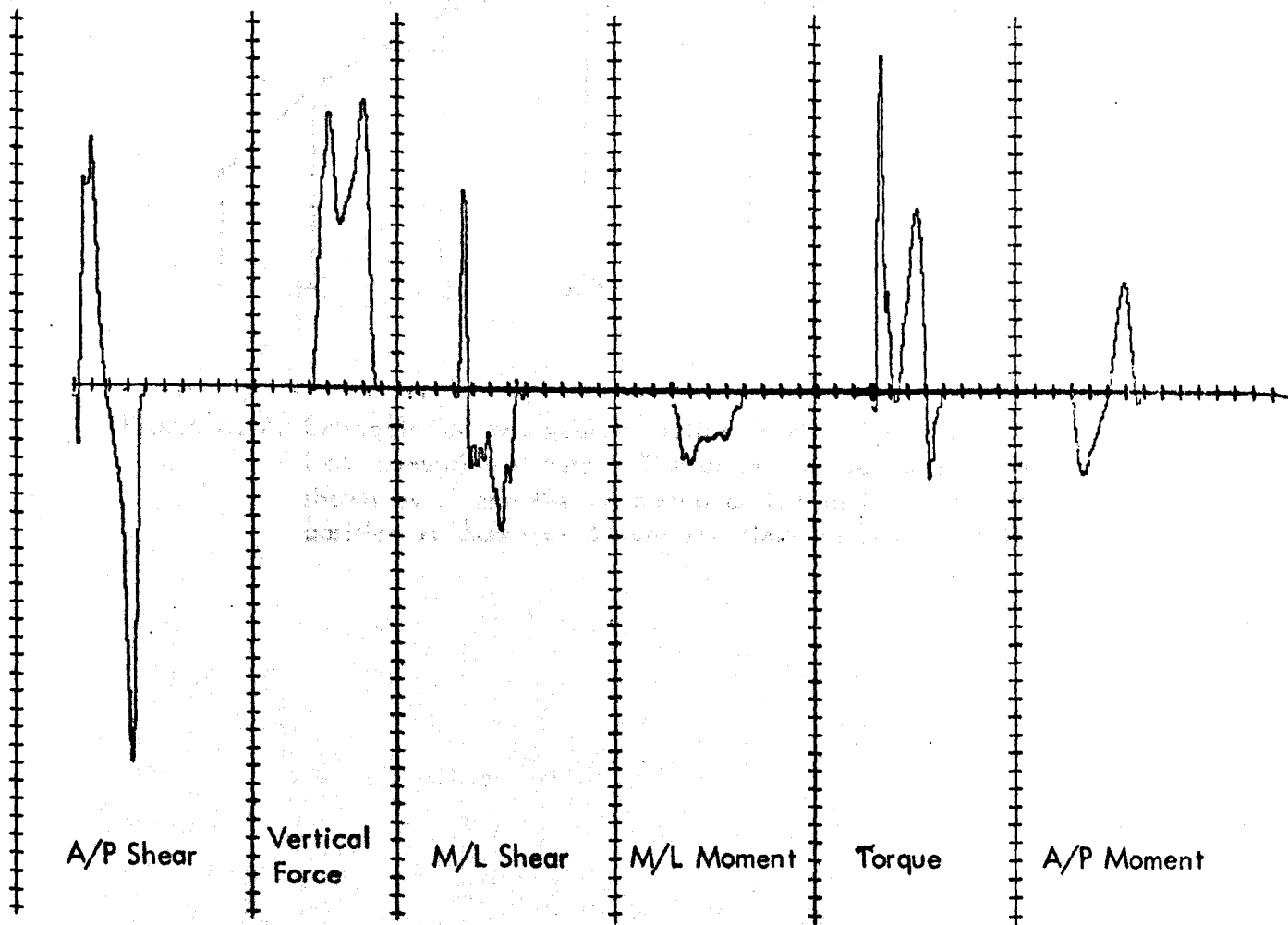


Figure 6.16. Ground reaction force data from the force plate acquired at the same time as the interface generated the co-ordinate data shown in Figure 6.15.

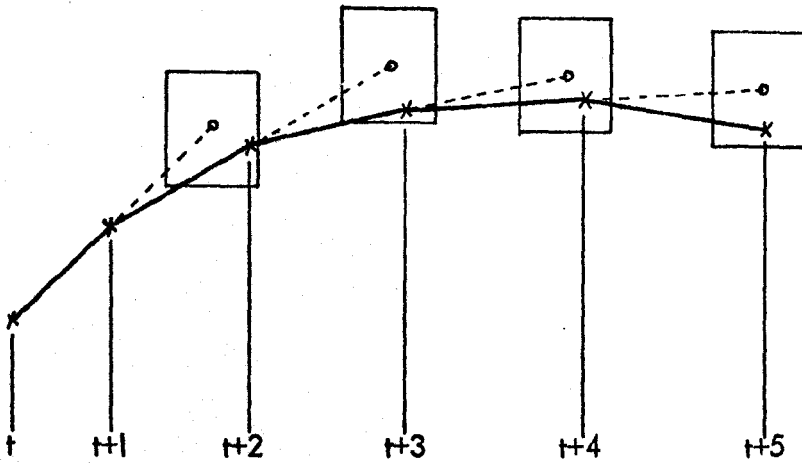


Figure 6.17. Extrapolation and search routine used to identify markers from co-ordinate data. The actual marker position is shown by X and the estimated or extrapolated marker position is shown at O over the time intervals t to $t+5$.

computer graph plotter and this plot is shown in Figure 6.18. The co-ordinates in every second television field have been joined together to show the relative instantaneous spatial position between markers. The stationary marker was filtered out by the extrapolation routine.

To successfully identify marker co-ordinates, using the procedure described above, care must be exercised in the marker configuration used on the subject. The co-ordinates shown in Figure 6.19 represent the side view data generated for a subject with a total of 10 markers. Markers were positioned over the hip, knee and ankle joints, on the pelvis and on the foot. Four of the markers were intended for the front view camera only and covered the pelvis, knee and ankle; as shown in Figure 6.20. It can be seen in Figure 6.19 that front view markers were occasionally picked up by the side view camera (as indicated by the cursors). At certain phases of the gait, marker identification, by the simple routine described above, for the ankle marker was impossible. Identification of the knee marker was satisfactorily accomplished despite similar pick up of the front view marker. It might be possible to develop a more sophisticated trajectory identification routine to solve this problem, however a simple solution can be provided by suitable marker positioning. The ankle joint marker for the front view camera could be shifted up the shank to lie a constant distance above the joint, and then pick up by the side view camera would not present a problem. A similar solution would be to provide a single marker for the ankle joint which could be seen at all times by both front and side view cameras. The size of the search boundary in the marker identification routine is defined by the expected accelerations and velocities that can be reached in the x and y directions. The size of this boundary in turn defines how close markers can be placed to each other. Markers with high expected accelerations and velocities will require large search areas and this should be noted when positioning markers. Similarly care should be taken to ensure that markers are not hidden from view if at all possible. In the tests illustrated above the subject walked with arms folded across his chest to avoid obscuring hip and pelvis markers. If this is considered undesirable then it would be necessary to provide secondary markers to be used when the primary marker was obscured.

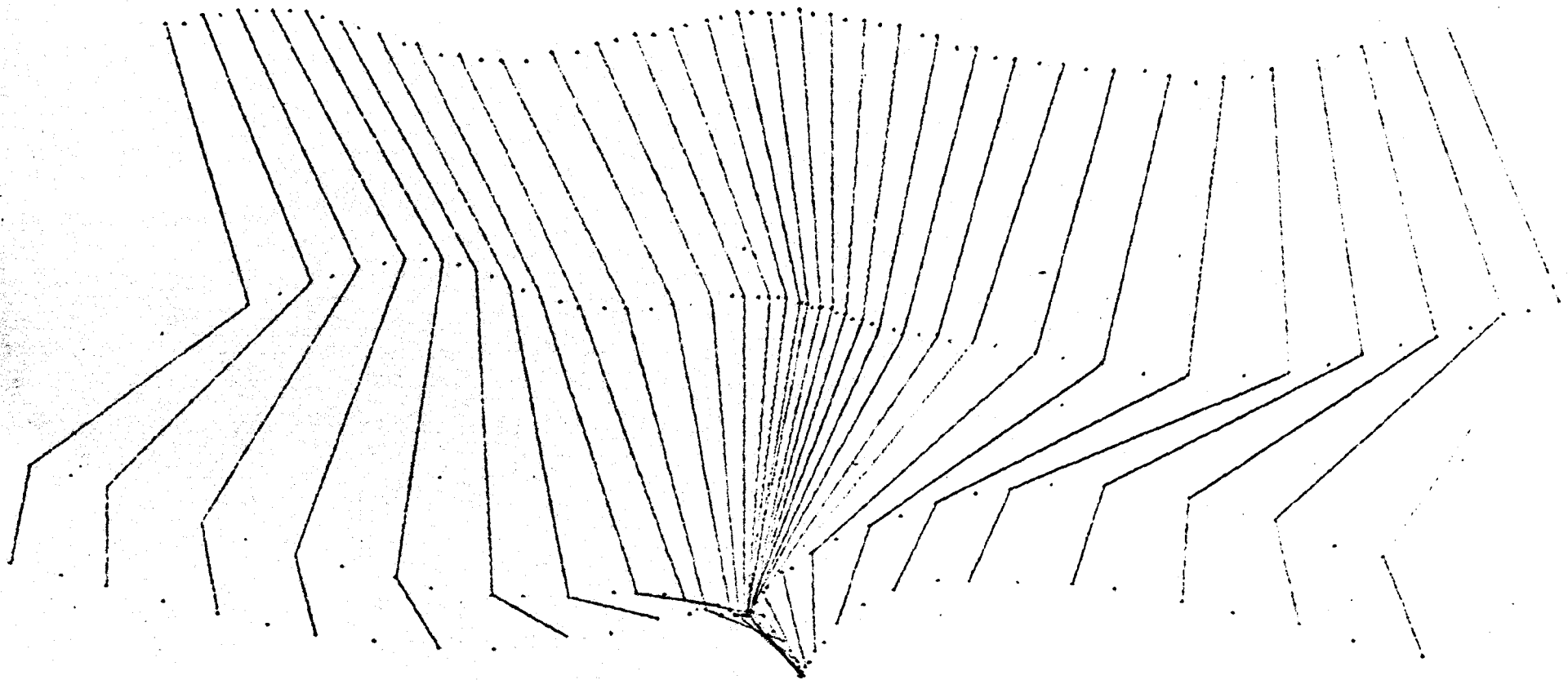


Figure 6.18. Computer plot of data generated by the system. Markers in every second television field are joined together.

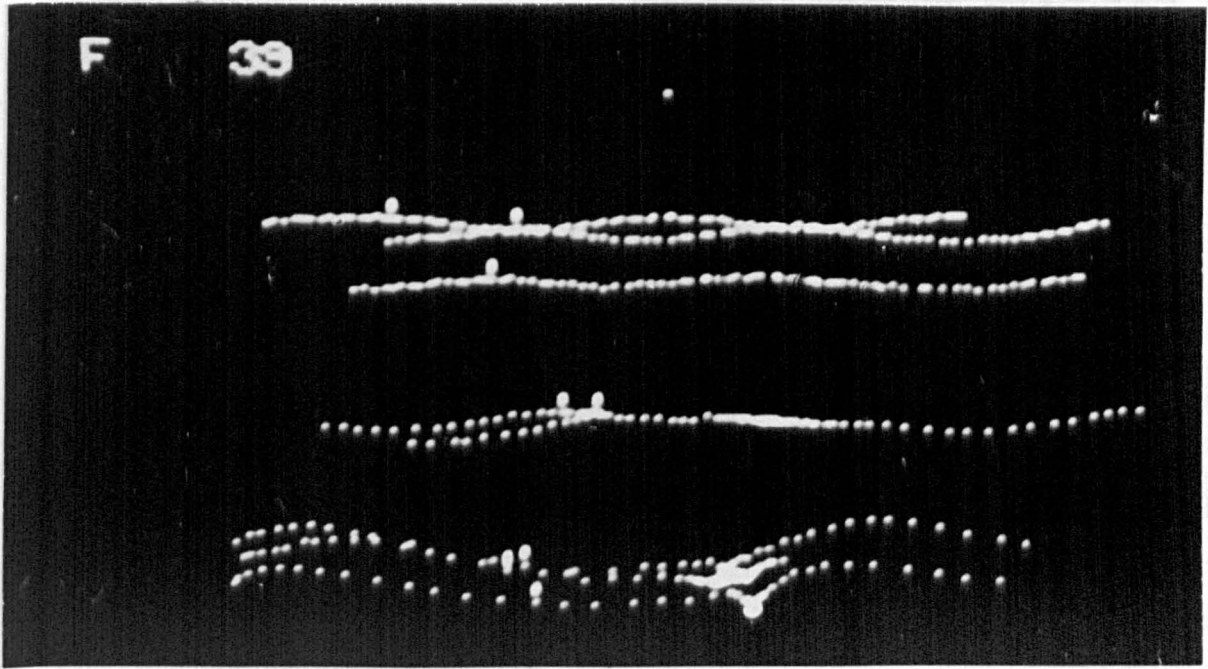


Figure 6.19. Side view data generated for a subject with front and side view markers. The cursors show that data has been generated by the side view camera for the front view markers during part of the trajectories.

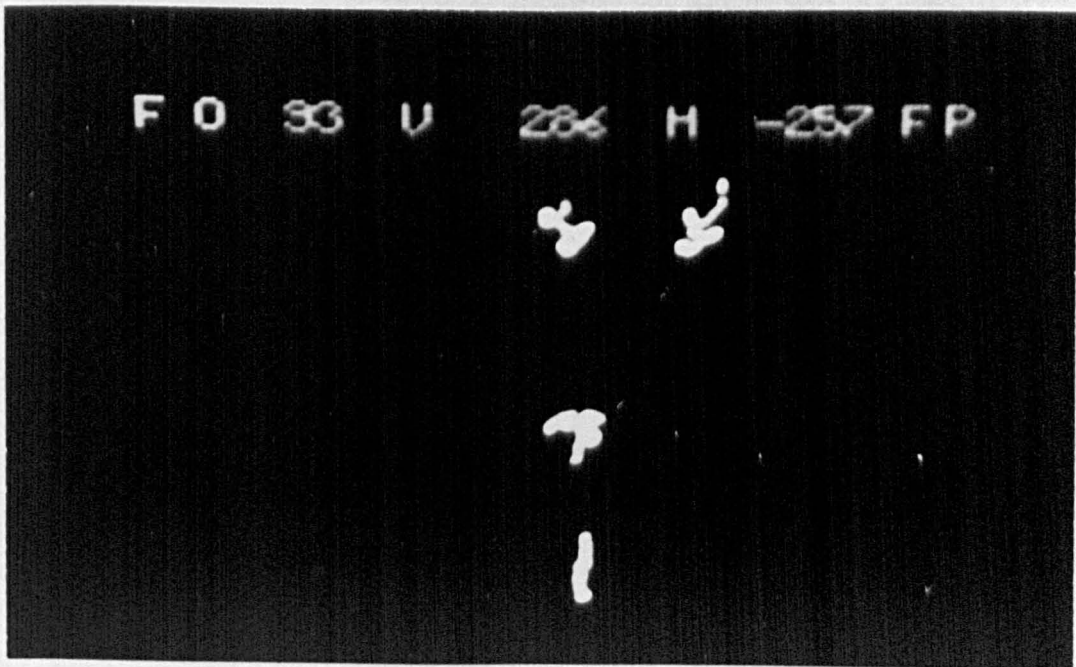


Figure 6.20. Front view data generated simultaneously with the data shown above. The subject had markers on the pelvis, knee and ankle. The side view ankle marker has also been picked up by the front view camera.

6.6 Simultaneous Data Processing and Data Rates

The ability of the system to run a program to acquire, simultaneously, data from other instrumentation while generating co-ordinate data has been shown above. The interface has been designed in such a way that the computer time required for the generation of co-ordinate data is minimised. This has made available a useful period of central processing time for running concurrent programs. The duration of this period is closely related to the data rate generated by the interface, which depends on the number of markers used, the number of lines on which markers are detected and the television field rate (which can be reduced if required by the interface). It is difficult to predict, exactly, data rates because the length of marker trajectories during each field are dependent on their average velocity during this period, however estimating that markers would be picked up on 5 lines then the data rate for 10 markers at the normal field frequency of 50 Hz would be 7500 words/second (3 words for each point - vertical and horizontal co-ordinates plus the code word).

The program DYDIS (Dynamic Display) has been used to assess simultaneous processing and data rates. The simulator was used to provide known inputs and the number of markers generated in each field was increased until display flicker was just noticeable, the data rate was then slightly reduced until there was no flicker. This was achieved when 3 markers were generated on each of 16 lines (5 words/line) at the normal field frequency of 50 Hz. This gave a data rate of 4000 words/sec; or, expressed in terms of markers, 2400 co-ordinate pairs/sec. The processing that is carried out by this program has been discussed in section 5.5.1.

For locomotion studies it will not usually be necessary to process data during a test run to any great extent. Also when a large number of markers is required the possibility of simultaneous processing is precluded because of the very high data rates involved. Even with 16K of core storage available, very little of which is required for the data acquisition program, the duration of a test would be severely limited. The use of the interface Address Register Double Buffer mode to extend the test duration was discussed in section 3.3.4.

The double buffer consists of two 4K areas in computer memory and 4K of data can be written onto one track of the PDP 12's RK05 magnetic disk. One track of data can be written onto disk in 40 mS so that, allowing time for data checking, re-writing, track access time, and possible recalibration of the disk address, data rates of 20 K words/sec should be quite feasible. The time required for data transfers from the television interface would be "invisible" to the disk data transfer program as will be discussed in the next section. The RK05 disk and its controller are described in Appendix A2.

6.7 Effect of Interface on Overall Computer Systems

The television interface uses the direct memory access facility of the PDP 12 (Single Cycle Data Break) for data transfers. Other peripherals, such as the disk, use the same method. The sharing of this facility is accomplished by a multiplexer (DM 12). This device examines data break requests on each cycle and allocates the facility to the peripheral with the highest priority, which can be allocated at installation time. Highest priority should be given to the device with the shortest latency time, where latency is defined by the maximum time which a device can wait for access to the computer memory before the data is lost. In the case of the television interface this time is the duration of the line blank time ($12\ \mu\text{S}$) although it is preferable for the request to be granted almost immediately so that all data transfers can be completed within the line blank time. In the case of the disk there is a maximum latency time of $22.5\ \mu\text{S}$, because the disk has a 4 word data silo which can be emptied before data is lost. This silo is normally kept full during write operations. The television interface has therefore been given the highest priority. This interface is limited to transferring a maximum of 7 words during any one line blank period, there will then follow a period of at least $40\ \mu\text{S}$ before there is likely to be another data break request from the interface. The time required to transfer the seven words would be $11.2\ \mu\text{S}$ for a $1.6\ \mu\text{S}$ cycle time; the disk data silo could therefore be half empty before a data break request by the interface, and still data would not be lost even if the interface transferred its maximum number of words.

Most of the PDP 12 instruction set can be used without any special consideration for the interface operation. However certain instructions should be avoided because of the short latency time of the interface. These instructions are those which use an extended cycle time such as the sample instruction SAM N when used in the normal mode (instruction time of $18.2 \mu\text{S}$); this particular instruction can, however, be operated in a fast mode when it will only take $1.6 \mu\text{S}$. Other instructions which should be avoided because of their long execution time are the Linc Tape Instructions (unless the "No Pause" condition is enabled), and the relay buffer to accumulator instruction RTA. All other instructions, including all Input/Output instructions, can be used quite satisfactorily.

6.8 Recommended Modifications and Additions

One modification that should be made to the interface concerns the Address Register. Unless this is operating in the Double Buffer mode the register will address locations from the Initial Data Address (IDA) up to 32K (15 bits), and locations from zero upwards will be addressed as the register overflows. This only occurs under extreme or fault conditions (when excessively high data rates occur) when software protection may fail. Protection against this event occurring can be provided by hardware in one of two ways. Either the Co-ordinate Generator should be inhibited once the address limit of the computer is reached (16K at present) or it should be arranged to reset the Address Register to the contents of the IDA register on overflow.

The second modification concerns a re-designed calibration control to avoid the limitations discussed in section 6.4. The logic for this control is shown in Figure 6.21 and a timing diagram is in Figure 6.22. This logic simply enables the Co-ordinate Generator for 64 horizontal units at a time until the entire television field has been covered. Only five markers can be registered during any 64 unit period but only fault conditions are likely to cause this number of markers to occur during such a short period. The existing instruction set can still be used with this control, although a right hand margin cannot be set by software.

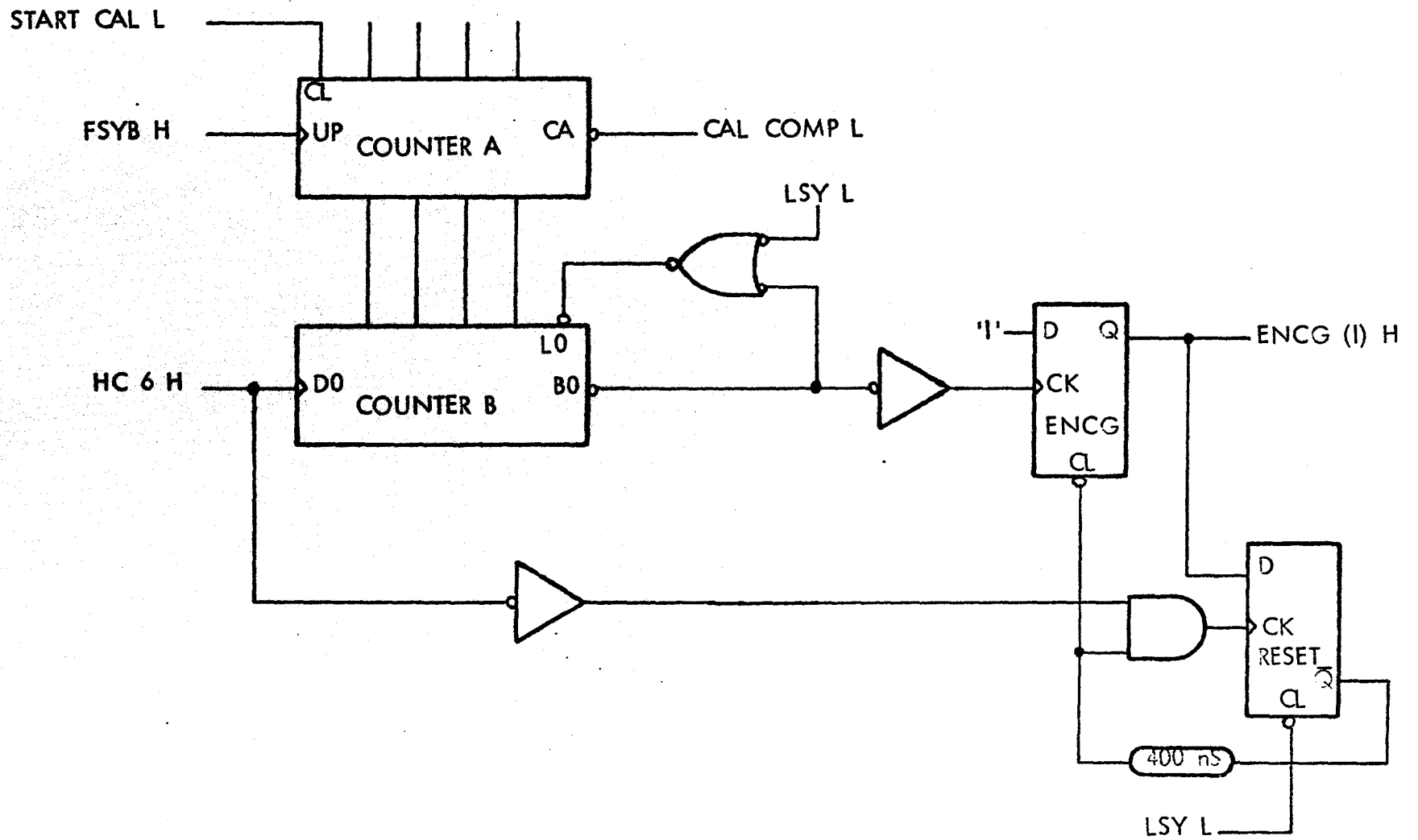


Figure 6.21. Calibration control logic.

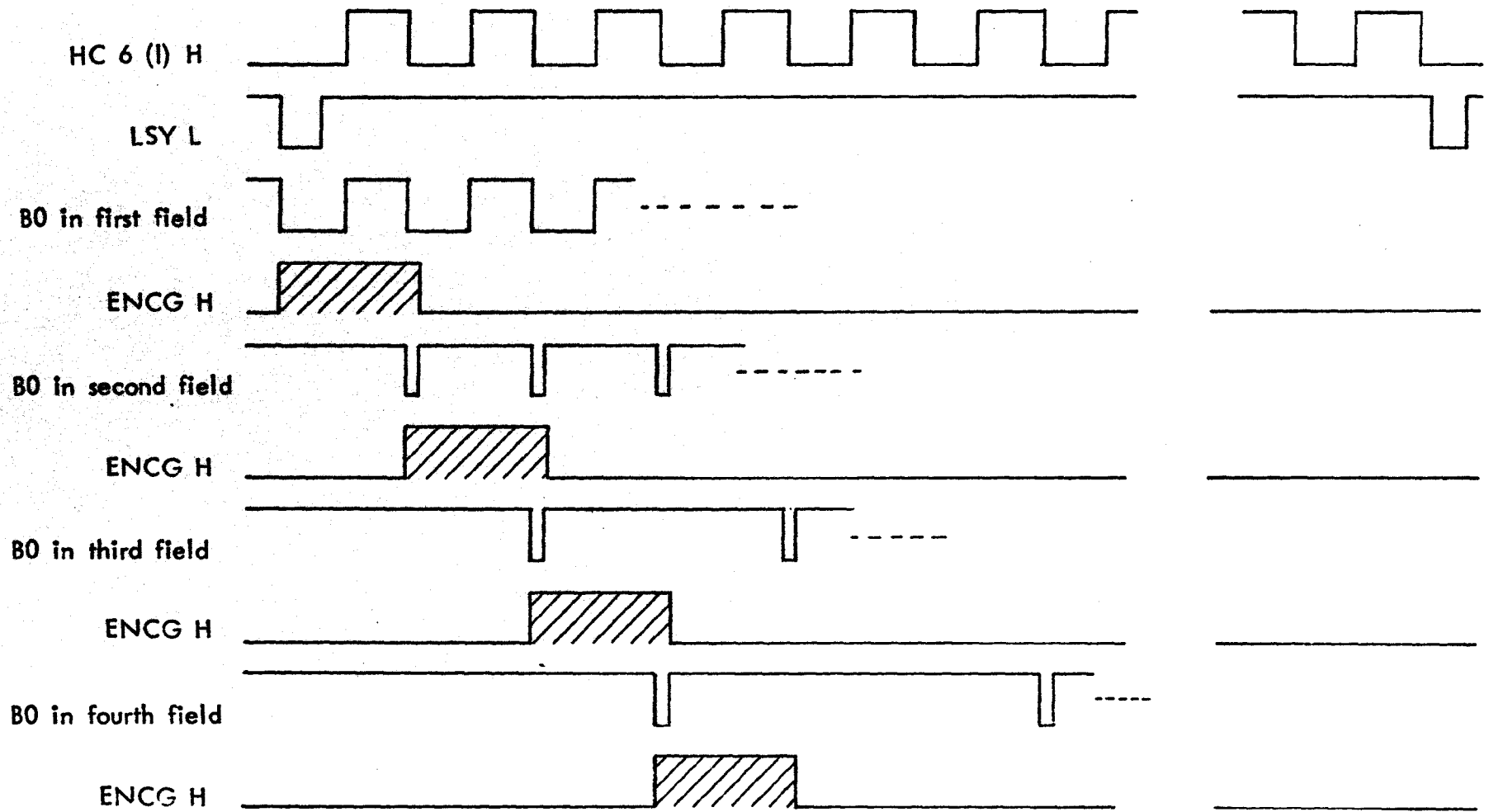


Figure 6.22. Timing diagram for calibration control of previous figure.

A recommended addition to the system is to use light activated switches (LAS's) to control data acquisition. These would be positioned at the start and end of the walk path (the area monitored by cameras), and just before the force plate to initiate data collection from this device. Switch inputs have been provided for this purpose in the interface and a noise immune switch such as that shown in Figure 6.23 could interface the LAS's to these inputs. Only a pulse of duration longer than 5 mS would give an output (SYNC H). The LAS marking the end of the walk path (which would inhibit the Co-ordinate Generator when activated) could also be used to generate an interrupt to the computer to signal the end of a test.

If this interface is to be implemented on other computers, or even on a PDP 12, it would be cheaper to use status and control registers for the hardware generation of software instructions. This method is discussed in Appendix A2.

6.9 Conclusions

The system that has been described here offers a means of automatically acquiring displacement/time measurements of human locomotion. Repeatable measurements can be obtained and it has been shown that co-ordinates can be generated for markers moving with relatively high velocities and accelerations, comparable with those reached during fast walking. The system is flexible in that up to six cameras can be used simultaneously in almost any position. Thus it is possible to obtain three dimensional measurements and cover several strides of the gait by appropriate positioning of the cameras. The only limitation in camera positioning is that the light source of a camera must not be in the field of view of any other camera. This is a limitation of the chosen marker system; if active light sources were used as markers then this limitation would not exist. Passive markers have been used throughout the testing procedures and found to perform adequately, this means that the test subject is not encumbered by connecting leads, power packs, or "strap on" markers. Normal room lighting can be left on during data acquisition. Markers can be positioned quite close to each other, although care must be taken where expected marker velocities and accelerations are high. The interface will generate synchronising pulses at a program selectable rate to control the

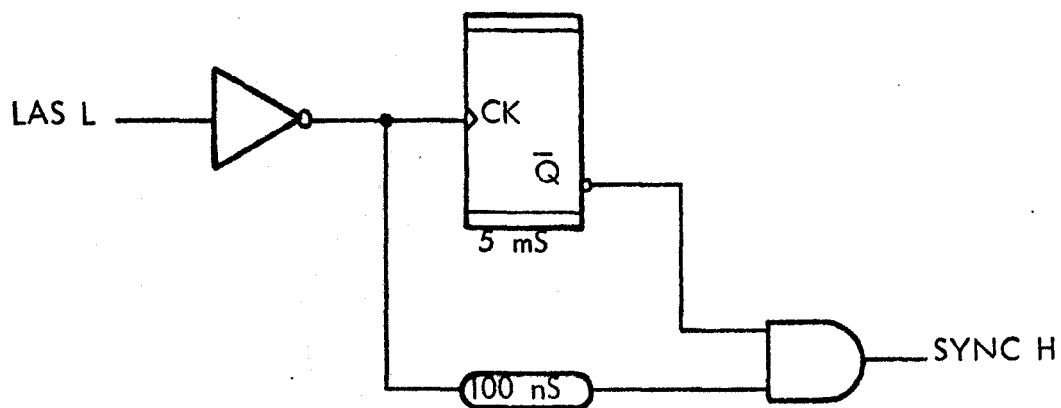


Figure 6.23. Logic for noise immune switch.

acquisition of data from other instrumentation, such as a force plate. A means of generating the co-ordinates of a calibration grid has been built in to the system. This will allow non-linearities to be corrected if the highest degree of accuracy is required. Measurement errors have been discussed and seen to be relatively small, the implications of these errors in biomechanical analyses are further discussed in Andrews (1976). The computation of other parameters from the displacement/time measurements, such as derivatives, is briefly discussed in Jarrett (1976) and more extensively in Andrews (1976).

The data rates generated by the system depend on the number of markers used and can be very high. This can be accommodated by double buffering the data onto disk storage; the facility to do this has been provided in the interface. Alternatively the data rate can be reduced by placing a rotating shutter in front of the camera lens to reduce the sampling period. This shutter also has the advantage of defining a consistent sampling instant, but requires the use of more expensive television cameras. The system can operate at lower sampling frequencies (down to 0.2 Hz), if this is acceptable for the motions being measured, which will reduce data rates considerably. The interface has been very reliable in use, no faults have arisen to date, but a built in marker pulse simulator will facilitate fault tracing. This simulator also allows a rapid check of system function and is useful in software development. It is clear that software must be designed on an interactive basis, preferably with extensive use of data display routines. Providing data rates are not too high it is possible to process incoming data in real time, the program DYDIS does this and produces a display of decoded data. Basic processing of the raw data can be adequately performed on a mini-computer such as a PDP 12. The identification of marker trajectories is made possible by the use of a simple linear extrapolation routine, manual intervention being required only to provide the first two data points.

All of the alternative systems have been extensively reviewed in Chapter I where their principal advantages and disadvantages were examined. Methods involving manual transcription of data, such as the use of cine film, are time

consuming and prone to human error. Automated versions of these methods are costly, still require a lot of manual intervention and still suffer from the basic disadvantages of photographic media - such as slow turnaround from test to results. The systems of locomotion analysis which involve contact with the subject, such as goniometers, are generally undesirable because they may influence the gait, and their measurement ability is often limited. The other opto-electronic techniques such as SELSPOT (Selcom (1975)) and CODA (Mitchelson (1974 and 1975)) are powerful measurement systems. They can sample at higher frequencies than a television based system and, when fully developed, may offer higher resolving power. They can provide outputs in analogue form which is easy to store, or can be interfaced to a computer. Such interfacing would be similar in many respects to that required for the television system. However, these systems both require active light sources to indicate the anatomical landmarks and these sources must be switched on and off involving either trailing connecting wires or a telemetry link. Also the power of the presently available light sources which must be used (Light emitting diodes) is limited which puts constraints on the range of operation of the system.

The other television systems that have been developed are all limited in their performance capabilities in comparison with the system described here. At present none of them can use more than one camera simultaneously and their basic designs will make this extension difficult. None of the systems have been designed with the intention of collecting data from other measurement devices and consequently the ease with which this can be done is limited. The acquisition of the co-ordinates of a grid of calibration points has not been considered in any of the systems, except in the Dutch system (Ingen Schenau (1973)), but even this system was not purpose designed to take in the grid co-ordinates. The results of tests for stability and response to moving markers have been reported here but no similar tests have been reported for the other systems with which to make comparison.

The system will have applications both in the research laboratory and in the clinic. In the research situation it will be possible to conduct large numbers of tests with comparative ease. This has not been practicable in the

past because of the time consuming manual interpretation that has been required to produce results for anything but the simplest of analyses (Jarrett (1974)). Also the research environment demands a system with some degree of flexibility, and especially the ability to acquire data from other transducers simultaneously. In the clinical situation the system offers a completely automated facility for data collection. The complexity of an analysis is variable - simple plots of change in segment angles may be derived, for instance, or data for a complete kinetic analysis could be collected. One of the most important advantages of this system in the clinical environment would be its non-contacting nature and also the complete silence of the system. Important areas of use in the clinic would be "before and after" studies of treatment regimes, identification of the "key" joint where several joints may be affected to varying degrees by a particular pathology, and in observing the different characteristics induced in the gait by an appliance.

6.10 Future Work

Research is currently in progress on methods of filtering co-ordinate data generated by systems such as the one described here, this research also includes investigations into digital differentiation schemes to derive velocities and accelerations (Andrews (1976)). This work will allow the data generated by this system to be used to provide a kinetic analysis of locomotion. The form of this analysis requires to be developed from existing work (Paul (1967), Morrison (1967), Poulson (1973)) to be compatible with suitable marker configurations for the television system. It will be advisable to structure this analysis in such a way that useful intermediate results can be obtained easily - to produce parallax corrected angle/angle diagrams for example. The incorporation of other measurement devices is seen as an important option to be built in to the analysis. These devices should include load measuring devices such as force plates (one or two), and pylon transducers; accelerometers; and possibly electromyograph recorders.

Presentation of data is an area which requires considerable investigation, the most promising direction would seem to be a graphically oriented one to

make use of the outstanding pattern recognition abilities of the brain. Successful routine clinical use of a system, such as the one described here, will only be achieved when the resultant data can be presented in such a way that abnormalities in the gait can be easily and quickly recognised. If this is achieved then it may be possible to use a very basic model of mini-computer (with consequent economy) to collect and process data.

APPENDIX A.1.BASIC PRINCIPLES OF TELEVISION

- AI.1 Principles
- AI.2 Scanning
- AI.3 Synchronisation

AI.1 Principles

In order to reproduce pictures which are acceptable to human vision television exploits a particular characteristic of the eye. This characteristic is known as "persistence of vision". The effect of this is that the eye perceives an image of a source for a certain period after the source has disappeared; a modulated source is, therefore, averaged by the eye. Experiment shows that it is necessary to project about 50 still shots of a moving scene, every second in order to allow the eye to average the pictures and reduce any flicker, due to changes, to an acceptable amount. It follows that a television system must scan the scene to be transmitted at least 50 times every second if flicker is to be acceptably low.

To represent a still picture in electrical form it is necessary that each level of brightness of the picture is specified by a unique and discrete electrical signal. This would require the picture to be broken up into an infinite number of elements each of which would have to be sampled, and allocated a value of electrical signal corresponding to its brightness. The whole array, for a moving scene, would have to be sampled 50 times every second to reproduce a picture acceptable to the eye. As usual a compromise is made and the picture is broken up into a finite number of elements which will produce a reasonably sharp picture at a bandwidth suitable for transmission. A modern, high definition, transmission system divides the picture into 300,000 to 400,000 picture elements and requires a bandwidth of several MegaHertz.

The signal levels corresponding to each element have to be transmitted sequentially. Practical television systems transmit the electrical signals corresponding to the picture elements starting at the top left hand corner of the picture. The signals corresponding to the top line of picture elements are first transmitted, then those comprising the next line, and so on until the whole picture has been transmitted; this process of transmitting a picture as a succession of horizontal lines is known as scanning. In conventional television systems scanning always starts from the left hand side of the scene, as viewed by the camera.

To convert the light signals into electrical signals a photosensitive surface is used. This surface, or target, varies in conductivity according to the intensity of light falling on it. The target is scanned by an electron beam which causes a current to flow in a signal plate, varying in magnitude according to the conductivity of the target. The current is passed through a signal resistor which develops a potential difference proportional to the current, this pd then being amplified. This system which applies to the vidicon and plumbicon pick up tubes is illustrated in Fig. A1.1. Other pick up systems exist, but they all work on similar principles.

A1.2 Scanning

The scanning of the electron beam is controlled by synchronising pulses. Deflection of the beam is accomplished by vertical and horizontal deflection coils which apply a magnetic field of varying strength across the path of the electron beam. As the electron beam passes through the magnetic field it is deflected at right angles to its direction of travel and to the direction of the field. The path followed by the electron beam in scanning the charge pattern on a camera target is shown in Figure A1.2. Starting at A the electron beam is made to travel horizontally at constant speed across the target to the right hand side; it then returns at a much greater speed and commences the next horizontal or line scan. The return traverse is known as the line flyback. Simultaneous to the line scan and flyback the electron beam is moved comparatively slowly and at constant speed downwards, the field scan. When the required number of lines has been scanned the electron beam is moved vertically back by the field flyback. Field synchronising pulses initiate field flyback and line synchronising pulses initiate line flyback.

To reduce the bandwidth required still further a system known as interlaced scanning is used. In this system the television camera transmits information in the same way as before, except that the complete picture is only scanned 25 times in every second. However to maintain the flicker below an obtrusive level each picture is scanned twice, the second scan taking place between the lines of the first scan. This technique is illustrated in Figure A1.3. Each complete scan is known as a field and there are thus

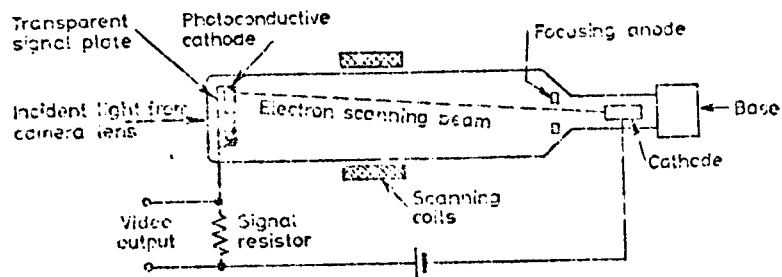


Figure A1.1. Camera pick up tube. (From Wharton (1967)).

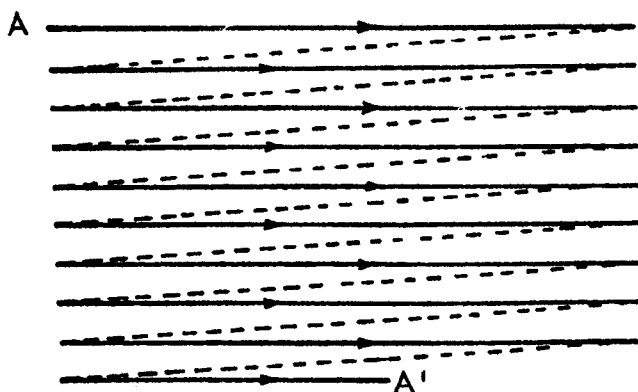


Figure A1.2. Scanning path

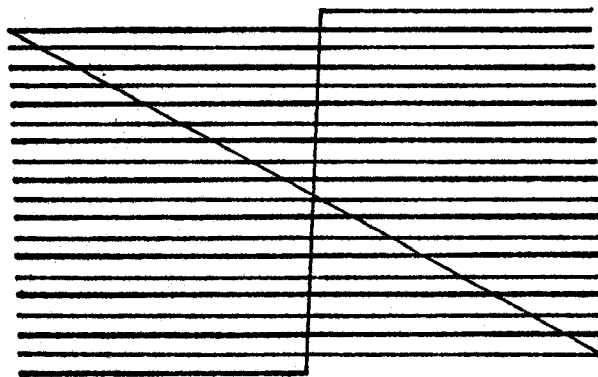


Figure A1.3. Interlaced scanning.

two types of field, "odd" and "even", interlaced to form a complete picture. The advantage of this system is that flicker, due to the succession of fields, is reduced by comparison with an equivalent sequentially scanned picture without an increase in bandwidth.

In the British system 625 lines are scanned in two fields. The number of active lines in each field is $292\frac{1}{2}$, after accounting for those blanked during field flyback.

Al.3. Synchronisation

Field and line synchronising pulses are generated by a central sync pulse generator. They control the time characteristics of the camera scan, and the relationship between field and line syncs determines the interlace of odd and even fields. During line and field flyback the video signal is suppressed, an interval known as the blanking period. This means that a number of lines do not transmit video information between fields and that a certain portion of each line does not carry video information. The lines and proportion of each line which carry picture information are said to be "active".

The video output from the camera may also incorporate the synchronising pulses in which case it is referred to as "composite video". The composite waveform, showing details of the line sync pulses and line blanking is shown in Figure Al.4. The composite waveform showing details of the field syncs and blanking is shown in Figure Al.5. The equalizing pulses shown in the field sync waveform are not necessarily present in closed circuit systems. They are a refinement added to broadcast systems to reduce the effect of the half line which occurs at the end of alternate fields, just before the field sync pulse. This half line can impair the interlace of the receiver, by disturbing the field synchronisation. Also in closed circuit systems the field sync may be one pulse of about $160\ \mu\text{S}$ duration, instead of five broad pulses.

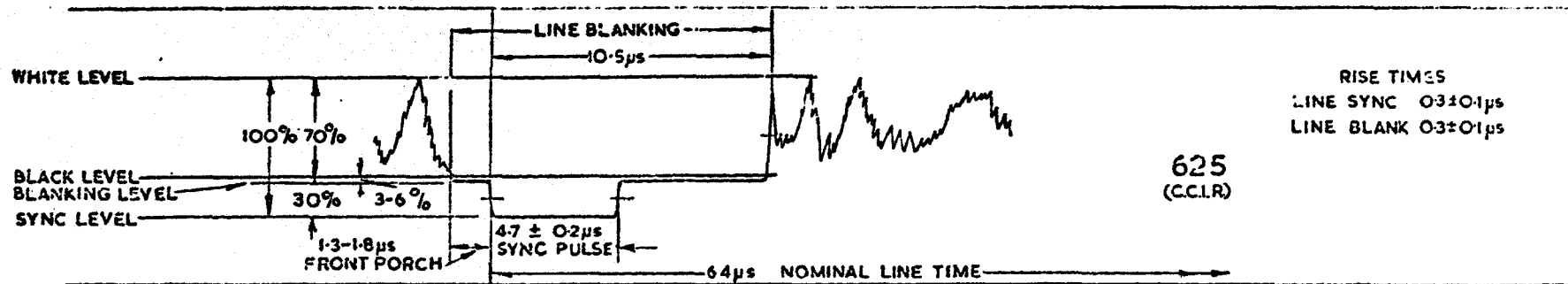


Figure A1.4. Line synchronisation waveform and blanking for the British 625 line television system (From Wharton (1967)).

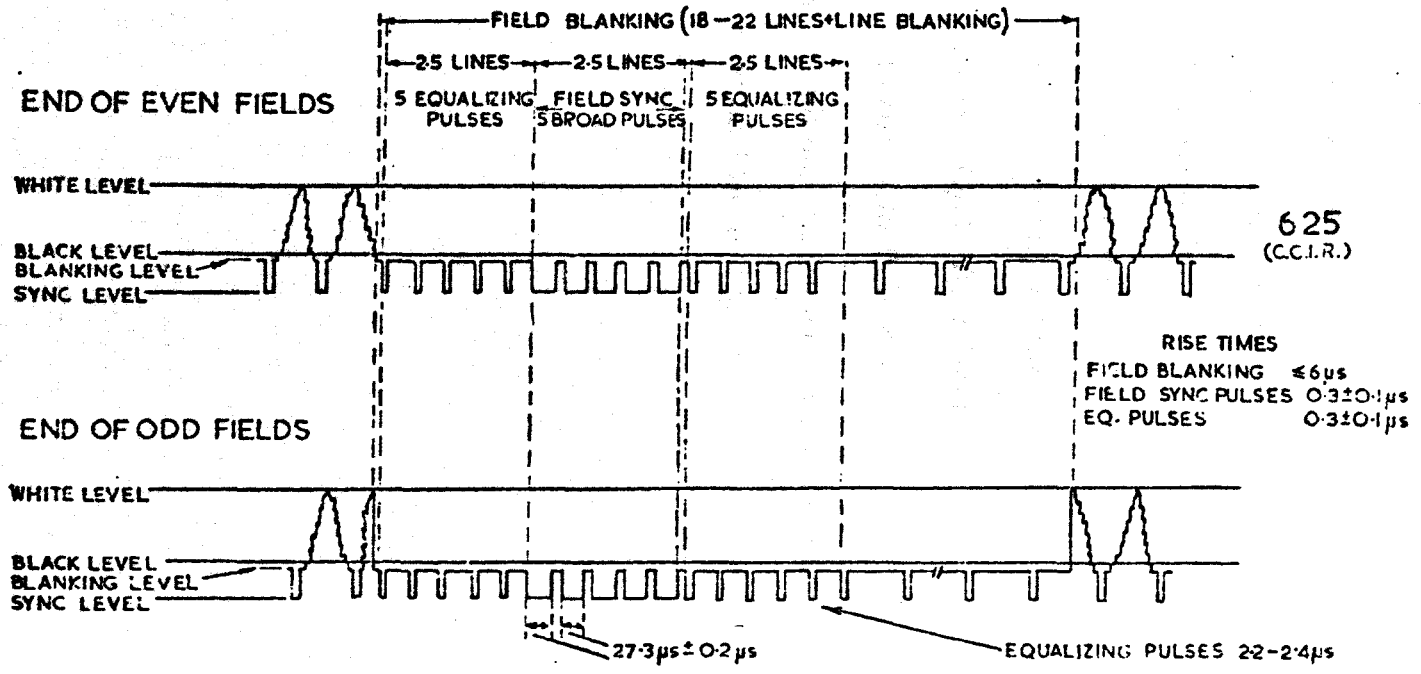


Figure A1.5. Field synchronisation pulses and blanking for the British 625 line television system (From Wharton (1967)).

APPENDIX A.2.
THE PDP 12 COMPUTER

- A2.1 Introduction
- A2.2 Input/Output Transfer Instructions
 - A2.2.1 Programmed Data Transfer
 - A2.2.2 Control and Status Registers
- A2.3 Direct Memory Access
 - A2.3.1 Single Cycle Data Break
 - A2.3.2 Three Cycle Data Break
 - A2.3.3 Multiple Use of the Data Break Facility
- A2.4 RK05 Disk and Control

A2.1 Introduction

The PDP 12 is a mini-computer designed for use in a laboratory environment. It is a 12 bit word length machine and can therefore basically address 4096 (4K) memory locations. An additional three address bits, the extended address, are provided to allow addressing of up to 32K of memory. The basic cycle time of the computer (the time taken to access and interpret one word from memory) is $1.6 \mu S$, although certain instructions will extend this time. There is a 10 bit analogue to digital converter to which up to 32 channels can be multiplexed, and which is controlled by a programmable clock; eight of the channels are prewired to precision potentiometers. Magnetic tapes and disk provide the means of mass storage for data and programs. Six program controlled relays are also provided for control of external equipment. Communication with the computer is achieved via a teletype on the computer console switches. Data may be output on punched tape, on a graph plotter or displayed on a point plot visual display unit (grid dimensions 512 x 512 points).

Full details of the computer will be found in the reference and maintenance manuals (Digital (1970 and 1971)), however those aspects which directly concern the television interface will be briefly described here.

A2.2 Input/Output Transfer Instructions

The PDP 12 has two distinct modes of operation. LINC mode and PDP-8 mode, each of which has its own instruction set. The LINC mode instruction set includes instructions to control some peripheral devices such as LINC tape systems, the visual display unit, and the analogue to digital converter. Most peripherals and certainly any interface will be controlled by the Input/Output Transfer (IOT) class of instructions in the PDP-8 mode instruction set. A special LINC mode instruction, IOB, allows this class of instruction to be used in LINC mode programs without changing modes.

A2.2.1 Programmed Data Transfer

When an IOT class of instruction (~~6000~~) is decoded in the Central

Processor the computer enters a $4.25\ \mu\text{S}$ expanded cycle, and enables the Input/Output Pulse (IOP) generator to produce time sequenced IOP pulses as determined by the three least significant bits of the instruction. A timing diagram for the IOP pulses is shown in Figure A2.1. These pulses are transmitted to all peripheral devices together with the contents of the Memory Buffer (MB) which holds the instruction. Figure A2.2 shows the function of each part of the IOT instruction. Bits 3-8 of the MB contents are transmitted to the peripheral devices in both (1) H and (0) H versions to simplify the logic required for decoding. This logic is shown in Figure A2.3 and gates the IOP pulses to the device when the appropriate code appears on the MB lines. The device code is assigned by wiring the appropriate MB lines to the inputs of the AND gate. In the example shown the device code is 31_8 ($11\ 01\ 00_2$). Any combination of the three IOP pulses can be generated depending on bits 9-11 of the instruction, therefore there can be up to 7 discrete instructions for any one device code. From one to three IOP pulses will be generated in each instruction; any device operation which requires a sequence of pulses can take advantage of this, and accomplish the sequence during one instruction. This type of operation can be used to advantage, for example, when loading a register with data may cause a flag to be set elsewhere in the interface; a second IOP pulse generated by the same instruction can be used to clear the flag without the need to resort to a second instruction.

Three other control lines are available for programmed data transfers. These are the Clear Accumulator, Skip and Interrupt Request lines (EXT AC CLEAR BUS L; EXT SKIP BUS L; and EXT INT RQST BUS L). The Clear Accumulator line is used when making data transfers from an interface to the accumulator. During each IOP data from the interface is strobed into the accumulator. This is done by making an inclusive "OR" of data already in the accumulator and data on the accumulator input lines. If there is no data from the interface or if the interface data is zero then the contents of the accumulator will remain unchanged. If it is desired to read the contents of an interface register then the accumulator must first be cleared. This

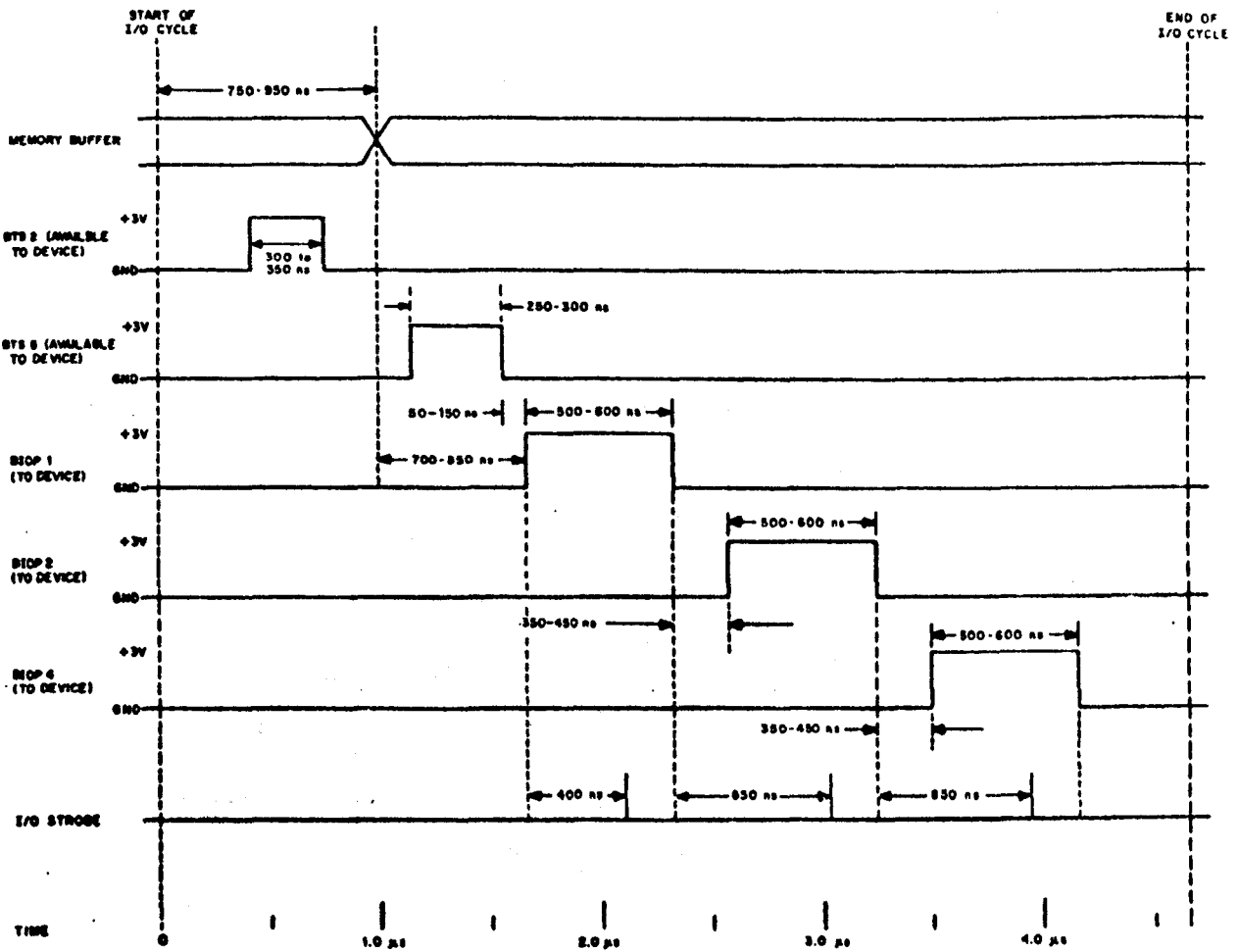


Figure A2.1. Programmed data transfer timing (from Digital (1970))

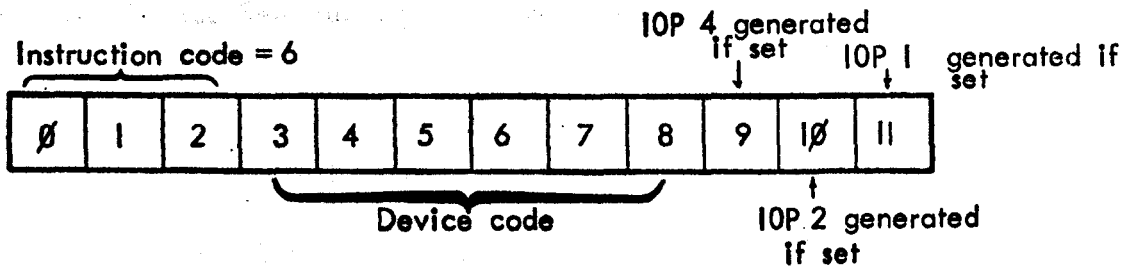


Figure A2.2. Bit functions for input/output transfer instructions.

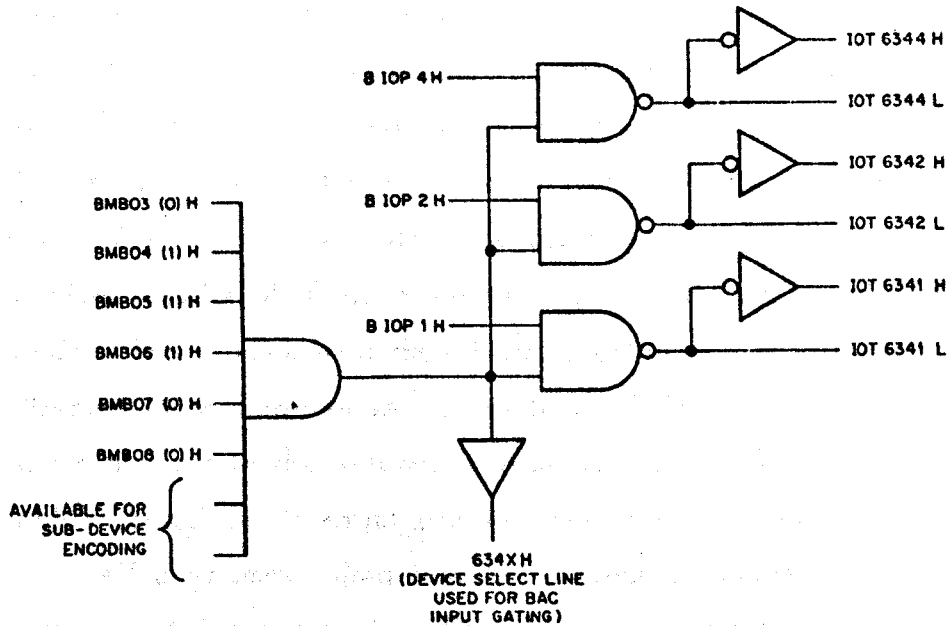


Figure A2.3. Device Selector logic. (From Digital (1971)).

can be done by the program, but it can also be done by using the clear accumulator line, which is preferable to avoid the possibility of programming errors. The Skip line allows the program to test the status of a flag in an interface. An IOP generated by an instruction will gate the status of the flag onto the Skip line, causing the next instruction in the program sequence to be skipped if the flag is set, (increments Program Counter). The Interrupt Request line will cause the current program to be interrupted if the interrupt facility of the computer is turned on (I0N) and the interface pulls the line low. The computer will store the contents of the program counter in absolute memory location \emptyset if the processor is in PDP-8 mode or 4 \emptyset if it is in LINC mode. The program will then jump to location 1 or 41, where it will be directed to an interrupt service routine. This routine will then test the status of flags in the external devices (using the Skip facility) to find out which caused the interrupt and will then take appropriate action.

All programmed data transfers between a device and the computer take place via the accumulator. Data in the accumulator is available on the [100] BAC \emptyset -11 lines; and data from a device is available to the accumulator on the [EXT] IO BUS \emptyset -11 lines. The initialize lines [100] BA and BB INITIALIZE H will be asserted whenever the computer power is switched on, the I/O Preset console switch is depressed, or when the I/O Preset Pulse is generated by program (Special Functions bit 6 set). The signal is used to clear all device flags and registers etcetera.

A2.2.2 Control and Status Registers

The methods of hardware instruction generation described above have been used in the television interface (see Chapter 4, Interface Logic - Instruction Generator 1 and Figure 4.22 for example). Usually each hardware operation is generated by a separate instruction; a great saving in components and the number of instructions used can be made by utilizing status and control registers. Only two instructions are basically required; one to load the register and one to read it. The various status bits are set by the interface (such as "change buffer $\frac{1}{2}$ ") and the control bits are set by data transfer from the computer (e.g. "Double Buffer mode"). Figure A2.4 shows how

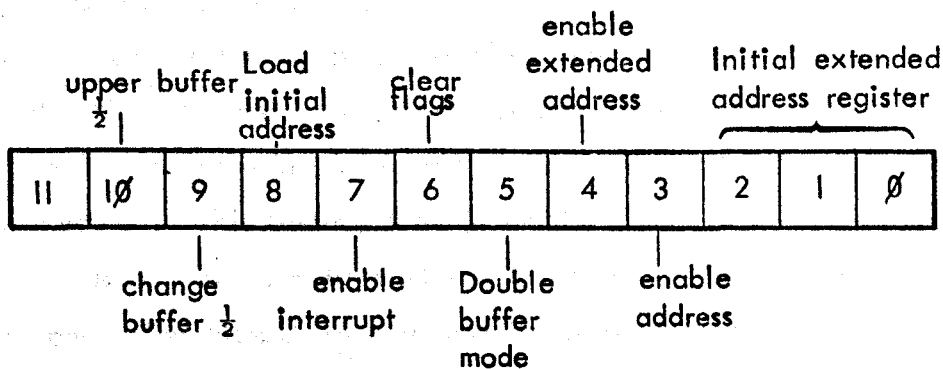


Figure A2.4. Status and control register for the address register instructions.

such a register might be used for some of the Address Register Instructions.

A2.3 Direct Memory Access

Direct memory access is provided in the PDP 12 on a cycle stealing basis by the Data Break Facility. When a break request is generated from an external device the computer completes the current instruction and then enters the "Break" state. The time required to enter this state is referred to as the latency of a break request and depends on the instruction currently being executed. This latency time is usually from $1.6 \mu\text{S}$ to $4.8 \mu\text{S}$ for three cycle instructions but can be anything up to $18.2 \mu\text{S}$ for some extended cycle instructions. The transfer of data can be either from or to the computer. To effect an input data break transfer the interface must specify the appropriate address in memory, provide the data word, indicate that transfer is to the computer, indicate whether 3 cycle or 1 cycle data break is required, and request a data break.

A2.3.1 Single Cycle Data Break

The timing diagram for input data transfers is shown in Figure A2.5. Data transfers to memory take place via the Memory Buffer to the location specified by the external data address lines. This address is provided by a 15 bit register in the Interface and must be present on the address lines in anticipation of the data break cycle. When the address has been strobed into a central processor register the address accept pulse is generated and transmitted to the interface. This pulse is usually used to increment the interface address register in readiness for the next data transfer, and to clear the device break request flag if all data transfers will have been completed at the end of the current break cycle. If the break request flag is cleared before TP2 time of the cycle then the computer will return to normal programmed operation on completion of the data transfer. The B-BREAK signal is generated by the computer and remains low for the duration of data break transfers. The data to be transferred must be available on the external data lines by TP2 of the cycle and must not be removed until after TP3. The B-BREAK signal

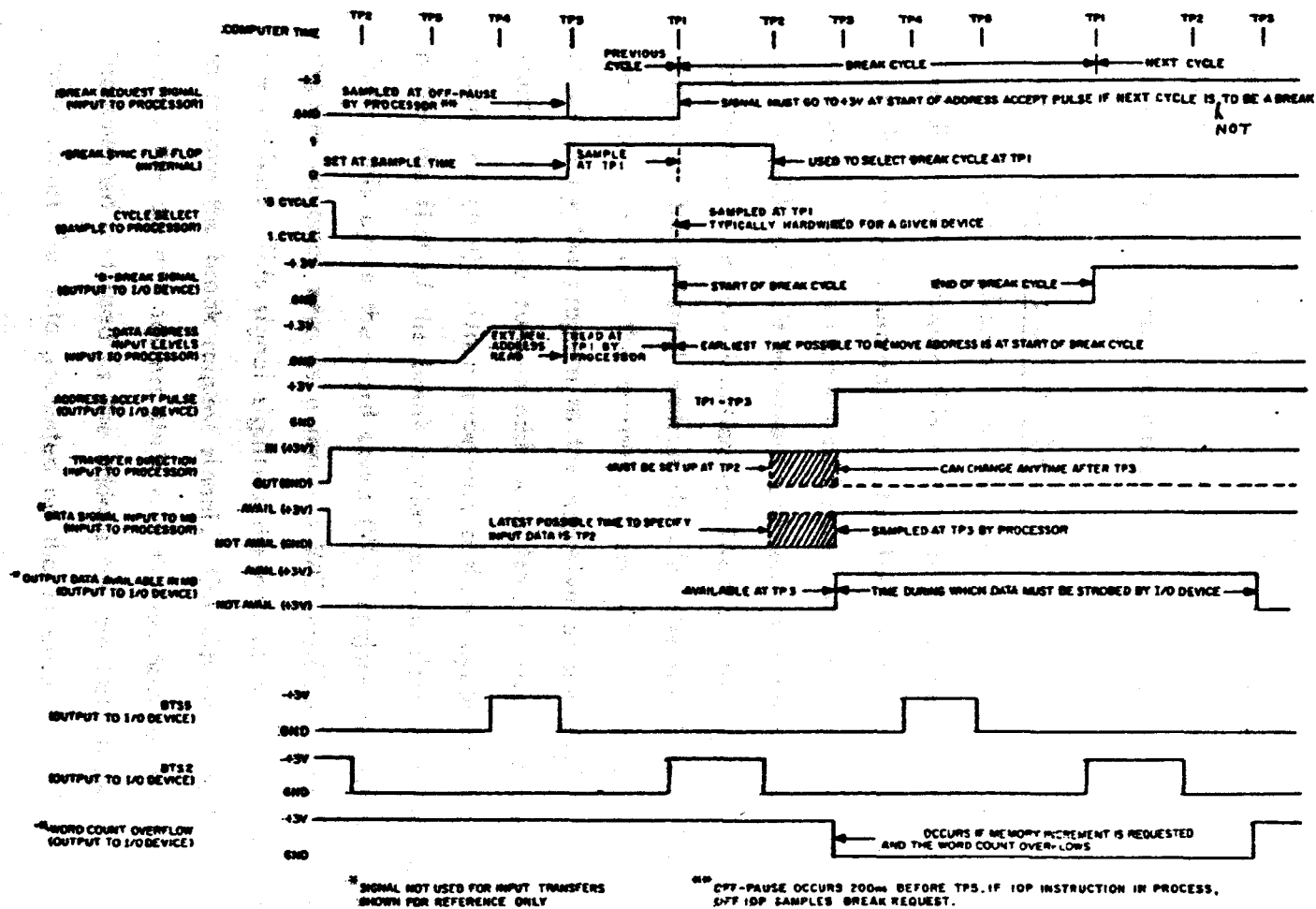


Figure A2.5. Single cycle data break input transfer timing diagram. (From Digital (1971)).

is useful as a data gating signal. BTS 5 and BTS 2 are "time state" signals generated by the computer, they are present on every cycle and must be gated with a data break control signal (e.g. B-BREAK) if they are to be used by the interface during data break cycles.

The initial conditions in the interface for single cycle data break transfers must be set up under program control. In the television interface the only conditions are the initial address for data transfers and the address register mode of operation. Usually an interface may also include a word count register which defines the number of data words to be transferred and is set up by program. In the television interface this register is set up automatically and depends on the number of markers detected.

A2.3.2 Three Cycle Data Break

The timing diagram for input or output transfers is shown in Figure A2.6. The difference between this method of data transfer and the previous one is that the computer provides registers for the current address and word count functions. If the extended addressing facility is to be used then the interface must provide a 3 bit extended address. The register used for the word count function is a memory location specified by the address (usually hard wired) placed on the external address lines by the interface. The next sequential memory location is the current address (CA) register. The word count register is preset by the program to the negative of the number of words to be transferred. The current address register is preset to the initial address minus one (A-1). Upon receiving a three cycle break request the computer enters the first of the break cycles on completion of the current instruction. During this cycle the word count (WC) register is incremented and if the register becomes zero as a result a WC overflow pulse is transmitted to the device. This signal signifies the end of a block transfer and is used to remove the break request signal. On the next break cycle the computer enters the current address state and the contents of the CA register are incremented and used as the address for the data transfer. The last break cycle stores the data presented on the external data lines by the interface

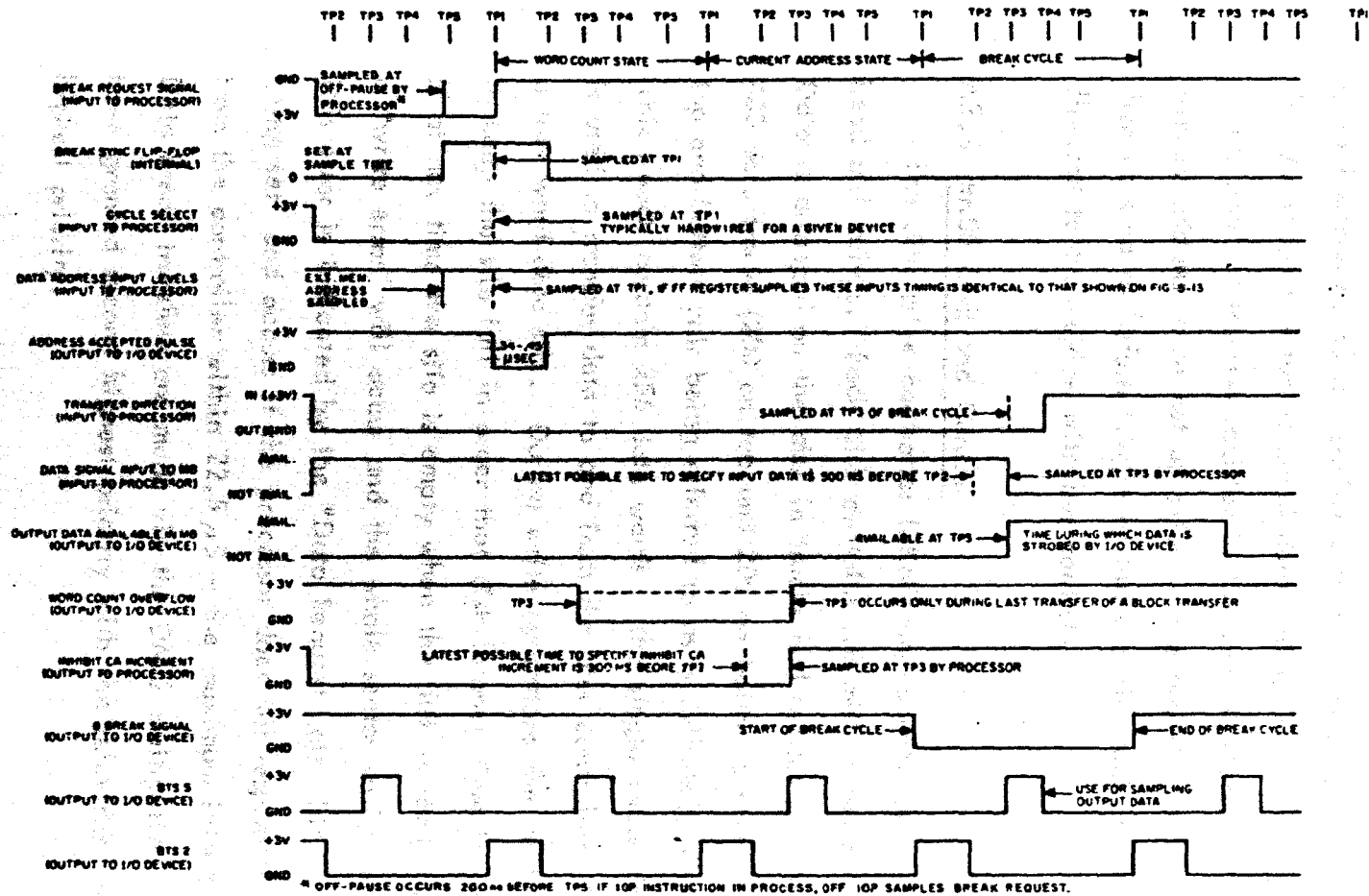


Figure A2.6. Three cycle data break timing diagram. (From Digital (1971)).

in the memory location specified by the CA register.

A2.3.3 Multiple use of the Data Break Facility

If more than one device requires the use of the data break facility then a multiplexer, the DMI2, must be used. Separate cables are taken from the DMI2 to each device for the data break control signals, the external data address, and the external data. Each device is allocated a priority and the break request lines are sampled, on every cycle, by the DMI2 which gives bus control to the device with the highest priority requesting a break. On giving control the external data address lines of the device are gated to the Memory Address register in the central processor. This allows the memory address to be set up prior to the break cycle. The break cycle is then entered as before (single or three cycle) and all control signals are directed to the device with bus control.

A2.4 RK05 Disk and Control

The disk drive controller (RK8F) can control up to four RK05 moving head disk drives. Each disk contains 1.6 million words of data storage. Data is arranged on the disk in tracks and sectors of a track. There are 400 data tracks (200 on top and 200 on bottom surfaces of the disk, each top and bottom pair being referred to as a cylinder), divided into 16 sectors which each contain 256 data words. The single cycle data break facility is used to transfer data to and from the disk controller, in which there is a four word data silo. During write operations the controller endeavours to keep the silo full in order that disk latency time is kept at a maximum. If at any time the silo is empty when the disk is ready to accept data then a "write error" will occur and the whole sector will have to be re-written. This error is indicated by the "Data Request Late" bit in the status register being set, and occurs if the processor does not respond to a break request from the controller within $22.5 \mu\text{S}$ - the maximum latency time of the disk. Data break transfers are made via the DMI2 multiplexer and the disk has been granted second highest priority with the television interface having the highest priority.

The transfer rate to or from disk is $8.32 \mu\text{S}$ per word, or 40 mS for a complete track of data (4096 words). Each sector of the disk contains, in addition to the data words, control sections and a header which provides a sector and cylinder address. Errors can arise in positioning the disk heads and this can be checked by comparing the disk cylinder address of a sector header with the cylinder address previously sent to the disk drive. If an address error is indicated then the disk heads must be returned to the home position (cylinder 0) which is referred to as re-calibration, and the operation to find the required cylinder repeated.

In combining data transfers from the television interface and to the disk (double buffering) in order to obtain the maximum data rates it will be necessary to first of all position the disk heads on the required cylinder and then write 4096 data words (one track). On completion of this operation the disk heads will be moved to the next track (by the program) and a check of cylinder address carried out with re-calibration if required in readiness for the next 4096 word block transfer. Checking of data will be limited if the highest possible data rates are required.

The details of the RK05 disk and its controller given above are only meant to provide a pointer to combined operation with the television interface. Full details will be found for disk operation in the appropriate DEC manual.

APPENDIX A.3.

COMPUTER PROGRAMS.

Interface Mnemonics

Real Time Display

Data Process

Calibration Data Acquisition

Calibration

Display Calibrated Data - DISC-CAL

DISF-CAL

Display Interface Data - DISC

DISF

Display Grid Data - DISG

0000
0001
0002
0003
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0060
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0064
0065
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0067
0068
0069
0070
0071
0072
0073
0074
0075

*20
/MNEMONICS FOR TV INTERFACE INSTRUCTIONS
/
/SOURCE VERSION IS CALLED "TVI-MN" IF SYMBOL
/TABLE IS LOST IT MAY BE REPLACED BY ADDING
/PROGRAM "TVI-MN" AND THEN ASSEMBLING SYMBOL
/TABLE CONTAINING TVI MNEMONICS WILL THEN
/BE SAVED IN SAVSYM SYMBOL AREA ON TAPE ON
/UNIT 1.
/A TAPE CONTAINING THIS SAVED SYMBOL TABLE MUST
/BE USED IN CONJUNCTION WITH THE LQDSYM
/COMMAND WHEN WRITING PROGRAMS CONTAINING
/TVI MNEMONICS.

/WHEN WRITING PROGRAMS USING THESE
/MNEMONICS A LQDSYM PSEUDO-OPERATOR
/COMMAND IS USED AT THE BEGINNING OF
/THE PROGRAM TO PHING THEM INTO THE
/SYMBOL TABLE.

/CAL INSTRUCTIONS
LQMON=6301 /SET MONITOR O/P FF
LTCAM=6302 /LD RR MARGIN
CAGU=6303 /CAL GJ, CL FLAG, SET
MONITOR O/P FF; SET
CAL GOLF FF.
CLCAL=6314 /CLEAR ALL CAL FF.
CAGUFR=6305 /CAL GJ FREE RUN, SET
MONITOR O/P FF, SET
FREE RUN FF, CLR FLAG
CCAFI=6306 /CLR CAL COMPLETE FLAG
SCAC=6307 /SKIP IF CAL COMPLETE
FLAG SET, CLR FLAG.

/SGS INSTRUCTIONS
LDFC=6311 /LD FIELD COUNT
LLLC=6312 /LD LINE COUNT
ENFC=6313 /ENABLE FIELD CNT
ENSA=6314 /ENABLE SIMULATOR
CFC=6315 /CLEAR FIELD CNT
CSA=6316 /CLEAR SIMULATOR

/CG INSTRUCTIONS
ENCG=6321 /ENABLE CG
DISCG=6322 /DISABLE CG

/ADDR INSTRUCTIONS
ENAL=6331 /ENABLE MEMORY ADDR
ENFAD=6332 /ENABLE FIELD ADDR
LECA=6333 /LD COMPOSITE ADDR
CLAL=6334 /DISABLE ADDR. FILL
RCA=6335 /READ CURRENT MEMORY
ADDRESS.
RCAA=6336 /READ CURRENT FIELD
ADDRESS.

0076
0077
0100
0101
0102
0103
0104
0105
0106
0107
0110
0111
0112
0113
0114
0115
0116
0117
0120
0121
0122
0123
0124
0125
0126
0127
0130

NO ERRORS, NO BINARY OUTPUT

CAGU 6303
CAGUFR 6305
CCAFI 6306
CLRF 6307
CIFI 6308
CDEM 6309
CFC 6310
CLAD 6311
CLCAL 6312
CSA 6313
IPX 6314
LISCG 6315
ENAD 6316
ENCG 6317
ENBRI 6318
ENFAD 6319
ENFC 6320
ENMON 6321
ENSA 6322
LTCAM 6323
LDFC 6324
LLLC 6325
RCA 6326
RCAA 6327
SCAC 6328
SDPH 6329

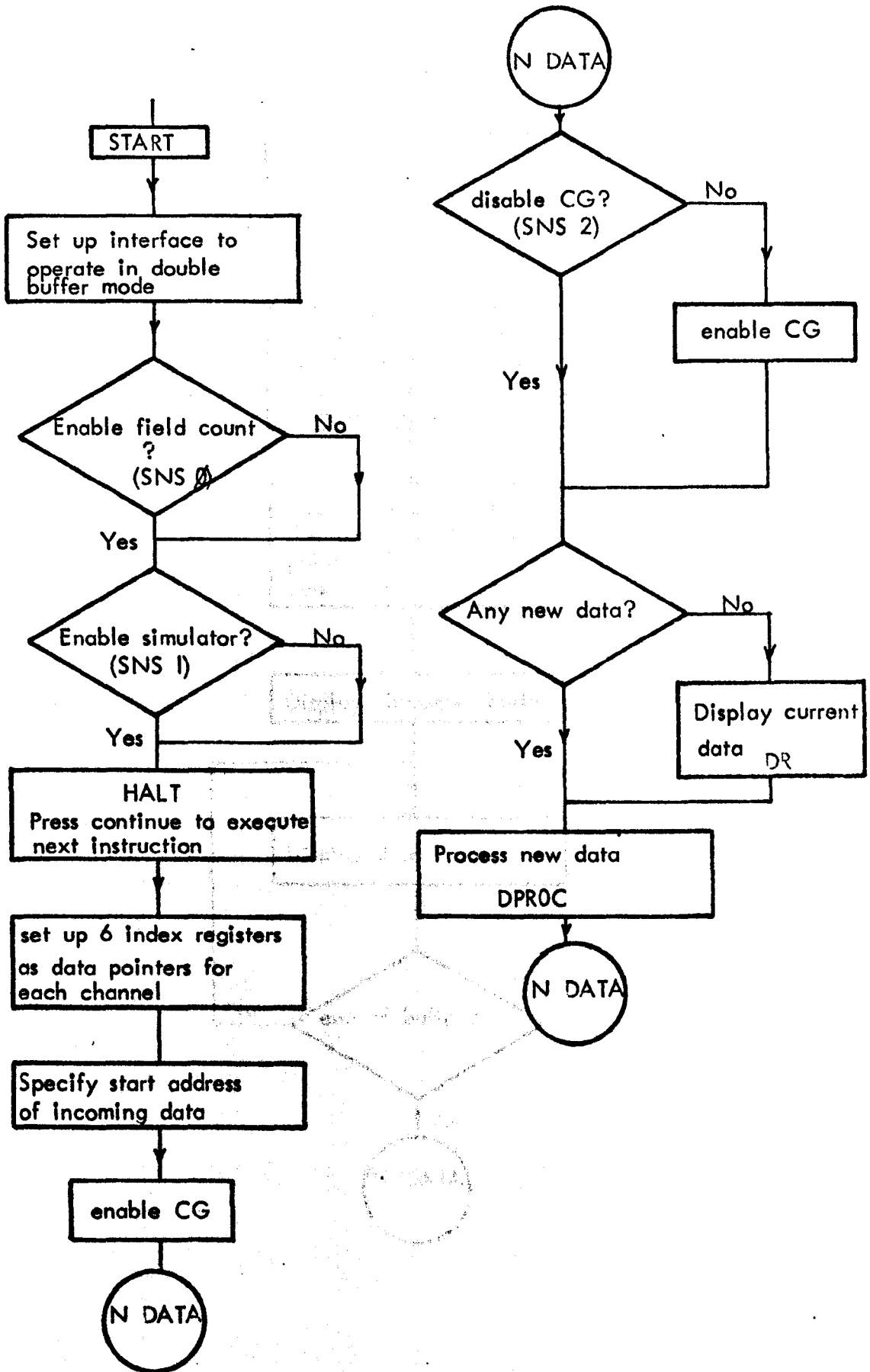
/ADDR INSTRUCTIONS (CONT)
LRA=6341 /LDUPLE BUFFER ADDR.
CLR4=6342 /CLEAR LRF4
ENLBI=6343 /ENABLE LRF4 INT
CLPI=6344 /DISABLE LRF4 INT
SCBI=6345 /SKIP IF CHANGE BUFFER
HALF FLAG IS SET.
SUDH=6346 /SKIP IF UPPER BUFFER
HALF IS BEING FILLED
CDBF=6347 /CLEAR UPPER HALF FLAG

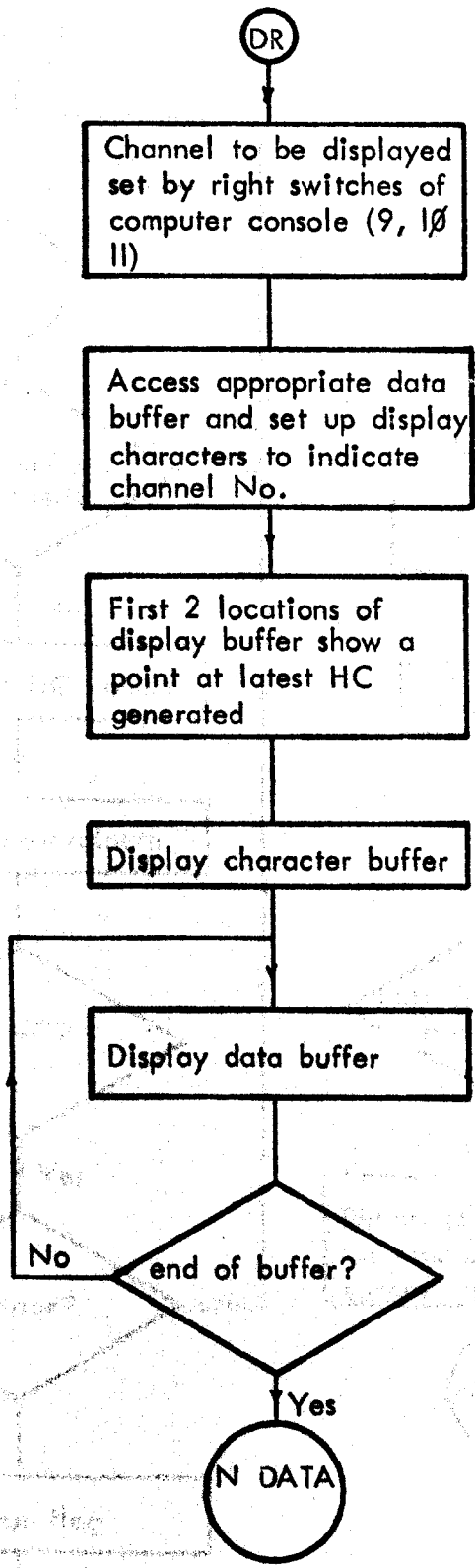
/IO INIT CLEARS ALL FLAGS AND MODE FF, ALL
/INTERRUPTS ARE DISABLED

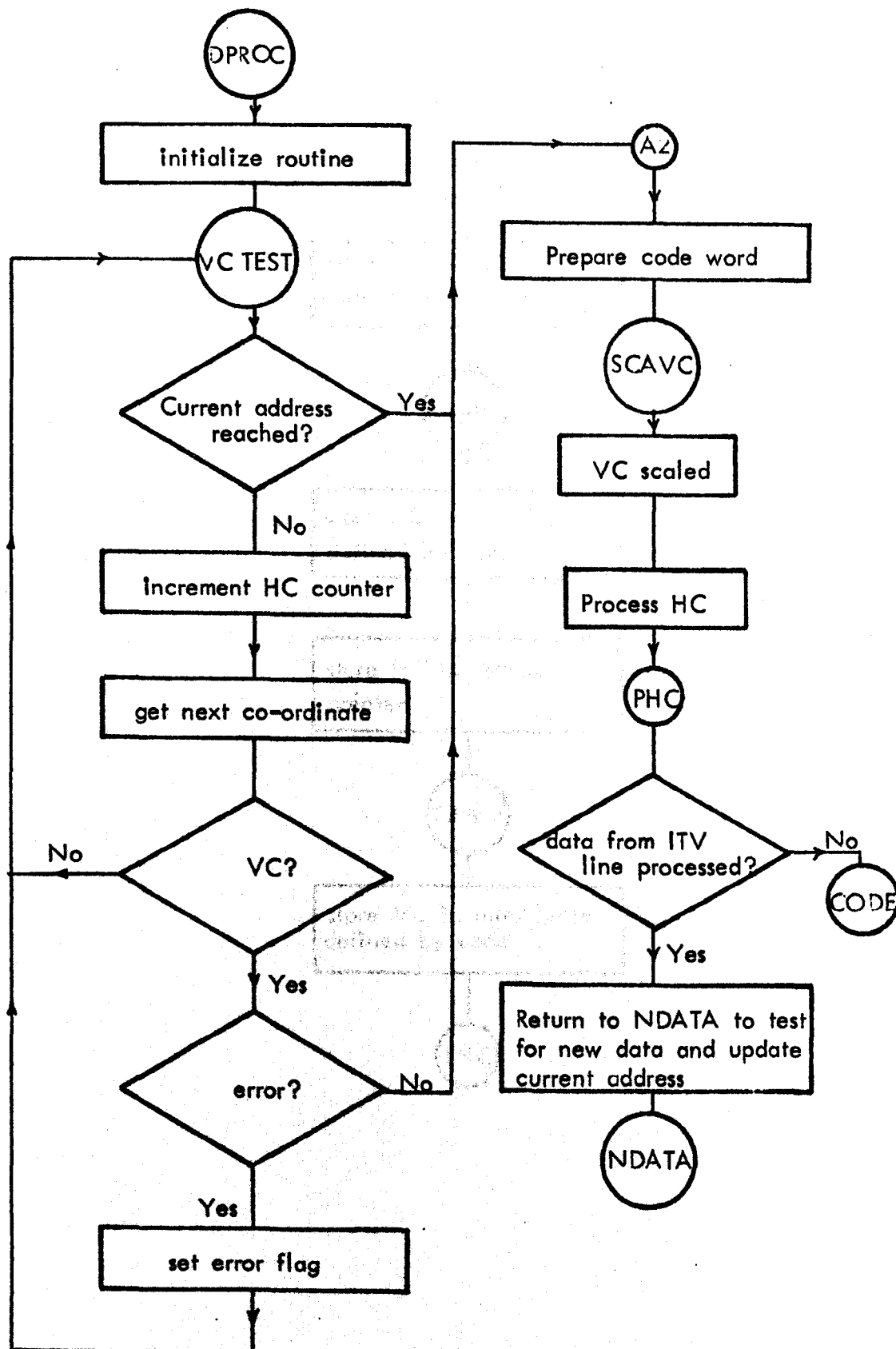
/4.8 US REQUIRED FOR EACH INSTRUCTION IN
/PMODE.

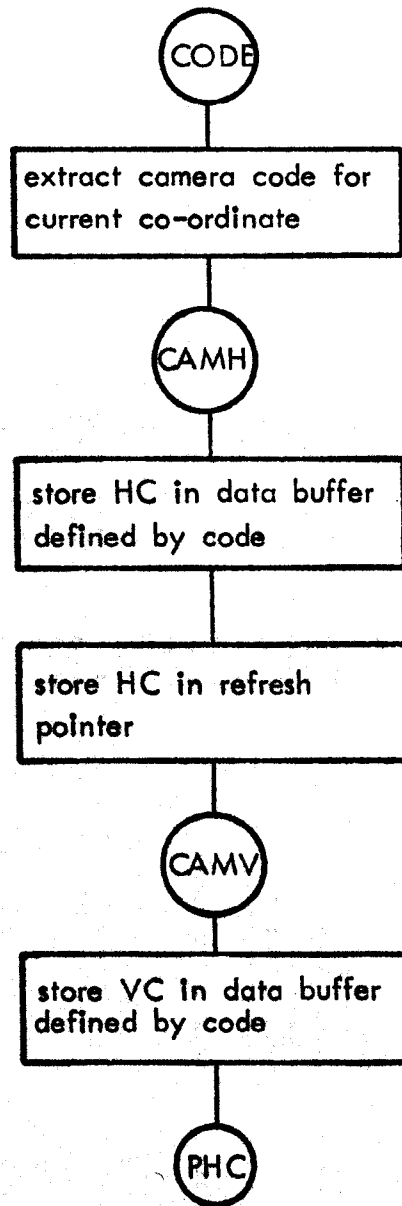
SAVSYM 1

Flow chart for real time display program (DYDIS)









0173	0531	0535	WORD1		0270	0614	0376	SET I 16	
0174	0531	1120	ALA I		0271	0615	0390	0	
0175	0532	0010	19		0272	0616	1003	LDA RSTAD	
0176	0533	4540	STC WORD2		0273	0617	4101	STC VC	/VC STORED
0177	0534	1000	LDA		0274	0620	1000	LDA	
0270	0535	0000	WORD1, 0000		0275	0621	0003	RSTAD	
0271	0536	4212	STC DW1		0276	0622	4002	STC R2	/START ADDR TO B2
0272	0537	1000	LIA		0277	0623	1000	LDA	
0273	0540	0000	WORD2, 0000		0278	0624	0002	R2	
0274	0541	4213	STC LW2		0279	0625	1440	SA2	/CURRENT ADDR?
0275	0542	0644	DFD, LDF 4	/LDF N	0280	0626	0105	CAD	
0276	0543	1020	LDA I		0281	0627	0456	SKP	/NO
0277	0544	0223	23		0282	0630	6655	JMP A2	/YES
0278	0545	1120	ALA I		0283	0631	0236	ASK I 16	
0279	0546	0000	MARKD, 0000		0284	0632	1022	LDA I B2	/GET NEXT COORD
0280	0547	1940	STA		0285	0633	1560	RCL I	
0281	0551	0552	POINT		0286	0634	1777	1777	
0282	0551	1000	LDA		0287	0635	1460	SAF I	
0283	0552	0000	POINT, 0000		0288	0636	6300	6000	/VC?
0284	0553	1040	S14	/CURRENT HC TO FIRST	0289	0637	6623	J4P VCTEST	/NO
0285	0554	2000	LJC		0290	0640	1000	LDA	/YES
0286	0555	0111	2000		0291	0641	0002	R2	
0287	0556	1120	CLR		0292	0642	1120	ADA I	
0288	0557	7400	ADA I		0293	0643	7776	-1	
0289	0560	1340	-377		0294	0644	1620	PSE I	
0290	0560	1340	STA	/FIXED VC TO SECOND L	0295	0645	6000	6000	
0291	0561	2001	2001		0296	0646	1440	SAE	
0292	0562	1020	LDA I		0297	0647	0003	RSTAD	/ERROR?
0293	0563	3777	3777		0298	0650	6655	JMP A2	
0294	0564	4336	STC LC		0299	0651	1020	LDA I	
0295	0565	0377	SET I 17		0300	0652	7300	7000	
0296	0566	0177	177		0301	0653	4103	STC FLAG	/SET ERROR FLAG
0297	0567	0161	SET I 1		0302	0654	6623	JMP VCTEST	
0298	0574	0323	0		0303	0655	1000	LDA	
0299	0571	0376	SET I 16		0304	0656	0002	R2	
0300	0572	7763	-14		0305	0657	1120	ALA I	
0301	0573	1020	LIA I		0306	0660	7776	-1	
0302	0574	7400	7400		0307	0661	1620	PSE I	
0303	0575	1777	DISC, DSC I 17		0308	0662	6000	6000	
0304	0576	0236	ASK I 16		0309	0663	4004	STC RSR	/SR WORD ADDR
0305	0577	6575	JMP *-2		0310	0664	2016	ADD 16	
0306	0610	1026	DISP, LIA I LC		0311	0665	0017	CJ4	
0307	0601	0341	SCR 1		0312	0666	1120	ATA I	
0308	0602	1560	RCL I		0313	0667	0256	0256	/ROL N FORMED
0309	0603	7000	7000		0314	0670	4672	STC *+2	
0310	0604	4007	STC D		0315	0671	1004	LDA RSR	
0311	0605	0226	ASK I DC	/END OF BUFFER ?	0316	0672	0000	0000	/ROL N
0312	0606	0456	SKP	/NO	0317	0673	1044	STA RSR	
0313	0607	6470	JMP NDATA	/YES	0318	0674	1000	LDA	
0314	0610	1036	LDA DC	/LOADS VC	0319	0675	0304	PSR	
0315	0611	0147	DIS D		0320	0676	4005	STC BHC	/HC ADDR+1
0316	0612	6600	JMP DISP		0321	0677	1000	LDA	
0317	/	/	/		0322	0700	0101	CC	
0318	/	/	/		0323	0701	1560	RCL I	
0319	/	/	/		0324	0702	6000	6000	
0320	/	/	/		0325	0703	1120	ALA I	
0321	/	/	/		0326	0704	7307	-470	
0322	/	/	/		0327	0705	0471	APJ I	/OLL OR EVEN FIELD?
0323	/	/	/		0328	0706	6711	JMP *+3	
0324	/	/	/		0329	0707	1120	ALA I	
0325	/	/	/		0330	0710	0470	470	
0326	/	/	/		0331	0711	1120	ALA I	
0327	0613	0653	DPROC, LDF 13	/DATA FIELD=13	0332	0712	7577	-200	

0367	0713	0917	COM	/INVERT	0466					
0370	0714	1560	RCL I		0467					
0371	0715	7000	7000		0470					
0372	0716	1040	STA	/REPLACE SCALED VC	0471					
0373	0717	0131	VC		0472					*200
0374	0720	1303	PHA	/PROCESS HC	0473	0200	4135			4136
0375	0721	0305	RHC		0474	0201	2241			2241 /C
0376	0722	1127	ADA I		0475	0202	1077			1077
0377	0723	7776	-1		0476	0203	7710			7710 /H
0400	0724	1620	RSE I		0477	0204	4477			4477
0401	0725	6000	6000		0500	0205	7744			7744 /A
0402	0726	1040	STA		0501	0206	3077			3077
0403	0727	0335	RHC	/HC ADDR	0502	0207	7706			7706 /N
0404	0730	1443	SAP		0503	0210	0000			0000
0405	0731	0303	BSTAD	/BHC=BSTAD?	0504	0211	0000			0000
0406	0732	6737	JMP CJDE	/NO	0505	0212	0000	DW1,		0000
0407	0733	1000	LDA	/YES	0506	0213	0000	DW2,		0000 /0-7
0410	0734	0302	R2		0507					
0411	0735	4003	STC BSTAD	/UPDATE START ADDR	0510					
0412	0736	6470	JMP NDATA		0511					
0413	0737	1004	LDA RSR	/SR WORD TO AC	0512					*220
0414	0740	0261	EOL I 1		0513	0220	4136			4136 /0
0415	0741	1044	STA RSR	/REPLACE SR WORD	0514	0221	2101			2101 /1
0416	0742	1005	LDA RHC		0515	0222	4523			4523 /2
0417	0743	1043	STA		0516	0223	4122			4122 /3
0420	0744	0102	HC		0517	0224	2414			2414 /4
0421	0745	0312	RJR 12		0520	0225	5172			5172 /5
0422	0746	0261	RCL I 1		0521	0226	1506			1506 /6
0423	0747	1560	RCL I	/CAMERA CODE	0522	0227	4443			4443 /7
0424	0750	7770	7770		0523					
0425	0751	1043	STA		0524					
0426	0752	0762	CAMCO		0525					
0427	0753	1043	STA		0526					*230
0430	0754	0776	MARK		0527	0230	3641			3641 /0
0431	0755	1120	ADA I		0530	0231	0177			0177 /1
0432	0756	0643	0643	/CREATE LDF N	0531	0232	2151			2151 /2
0433	0757	4760	STC LDF		0532	0233	2651			2651 /3
0434	0760	0647	LDF 7	/LDF N	0533	0234	0477			0477 /4
0435	0761	1120	ADA I		0534	0235	0651			0651 /5
0436	0762	0000	0000		0535	0236	4225			4225 /6
0437	0763	1120	ADA I		0536	0237	6050			6050 /7
0440	0764	1070	1070	/CREATES STA I (USES	0537					
0441	0765	1043	STA	/REGISTERS 10 TO 15)	0540					
0442	0766	0772	CAMH		0541					
0443	0767	5005	STC CAMV		0542					
0444	0770	1000	LDA		0543					*100
0445	0771	0102	HC		0544	0100	0000	COUNT,		0
0446	0772	0000	0000	/STORES HC IN CORRECT	0545	0101	0000	VC,		0
0447	0773	1000	LLA I	/FIELD)	0546	0102	0000	HC,		0
0450	0774	4023	4023		0547	0103	0000	FLAG,		0
0451	0775	1120	ADA I		0551	0104	6000	CALLR,		6000
0452	0776	0000	0000	/CREATES STC	0551	0105	6000	CAL,		6000
0453	0777	5002	STC INDI		0552					
0454	1000	1000	LLA		0553					
0455	1001	0102	HC		0554					
0456	1002	0000	0000	/SETS UP MARKER TO	0555					*20
0457	1003	1002	LLA	/SHOW POSITION OF	0556	0000	0001	PCOUNT,		1
0460	1004	0101	VC	/REFRESH FOR EACH CH	0557	0001	0177	LCOUNT,		0177
0461	1005	0000	0000	/STORES VC IN CORRECT	0560	0002	6000	MDI,		6000
0462	1006	0653	LDF 13	/FIELD	0561			/REFRESH POINTERS		
0463	1007	6720	JMP PHC		0562	0003	0000			0000
0464					0563	0004	0000			0000
0465					0564	0005	0000			0000

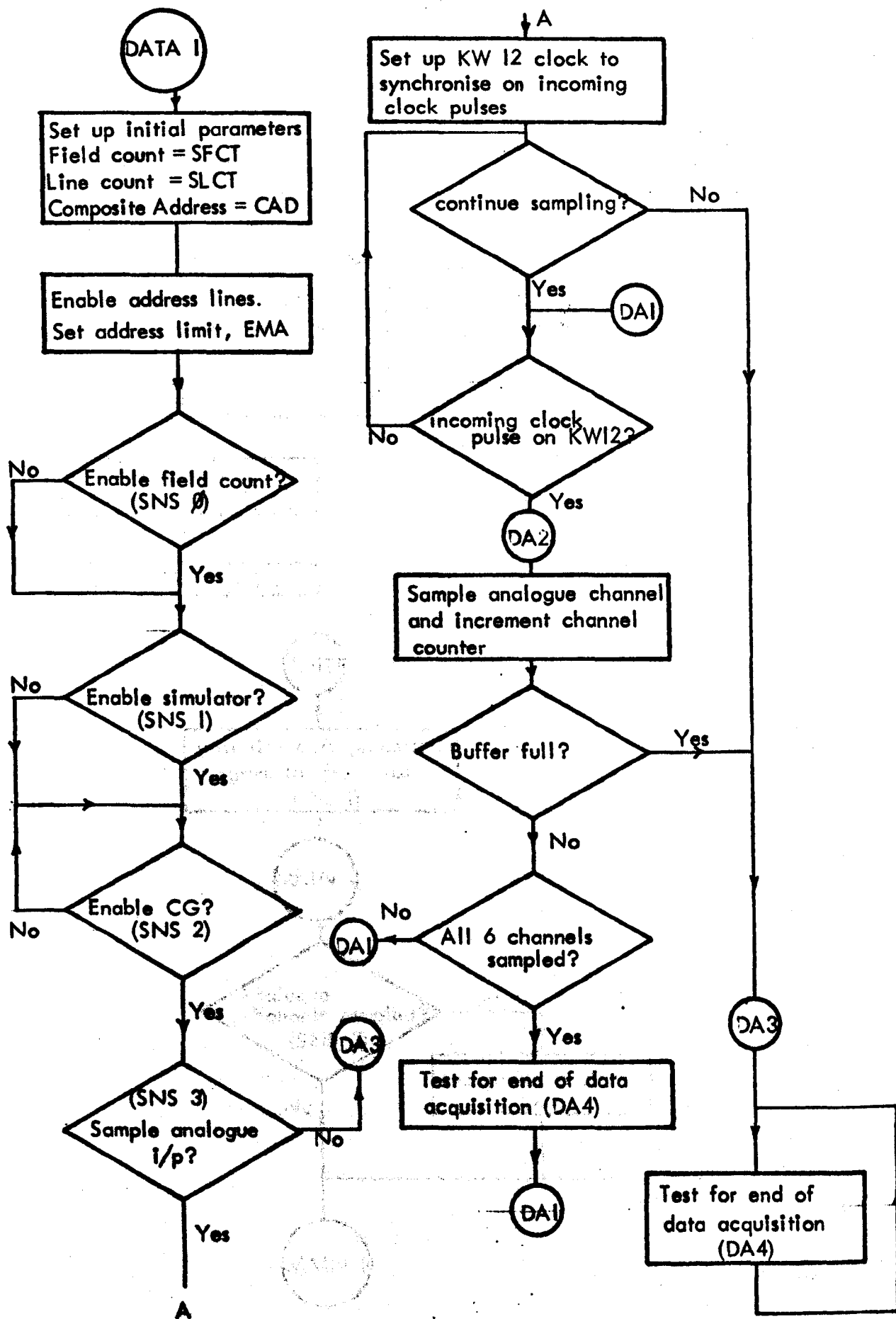
0565	0026	0000		0000
0566	0027	0000		0000
0567	0030	0000		0000
0570	0031	0000		0000
0571			/	
0572			/	
0573			/	
0574				*10
0575	0010	3777		3777
0576	0011	3777		3777
0577	0012	3777		3777
0600	0013	0000	AUTO.	0000
0601	0014	3777		3777
0602	0015	3777		3777
0603			/	
0604			/	
0605			/	
0606			/	
0607				*2
0610	0002	0000	R2.	0000
0611	0003	0000	RSTAL.	0000
0612	0004	0000	RSK.	0000
0613	0005	0000	RHC.	0000
0614	0006	0000	LC.	0000
0615	0007	0000	L.	0000

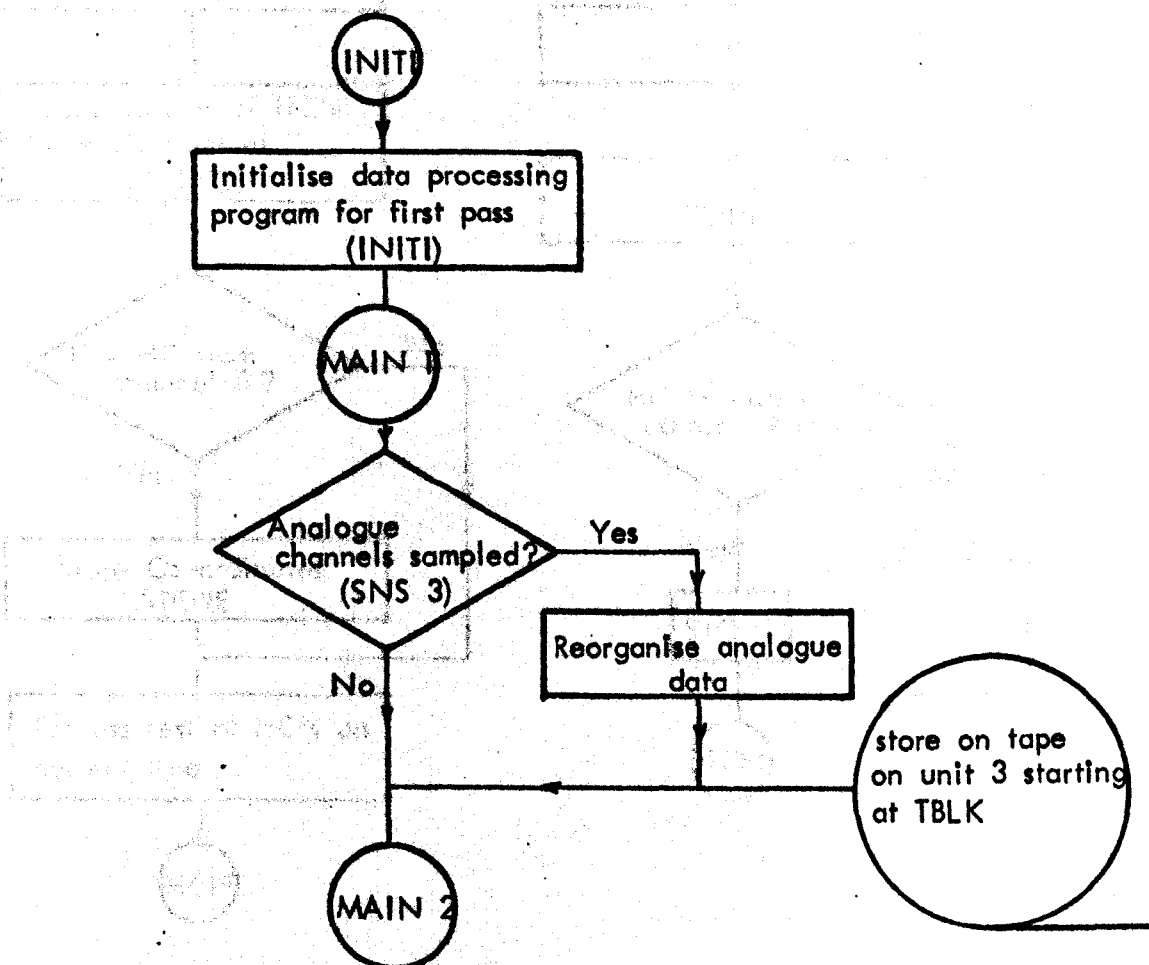
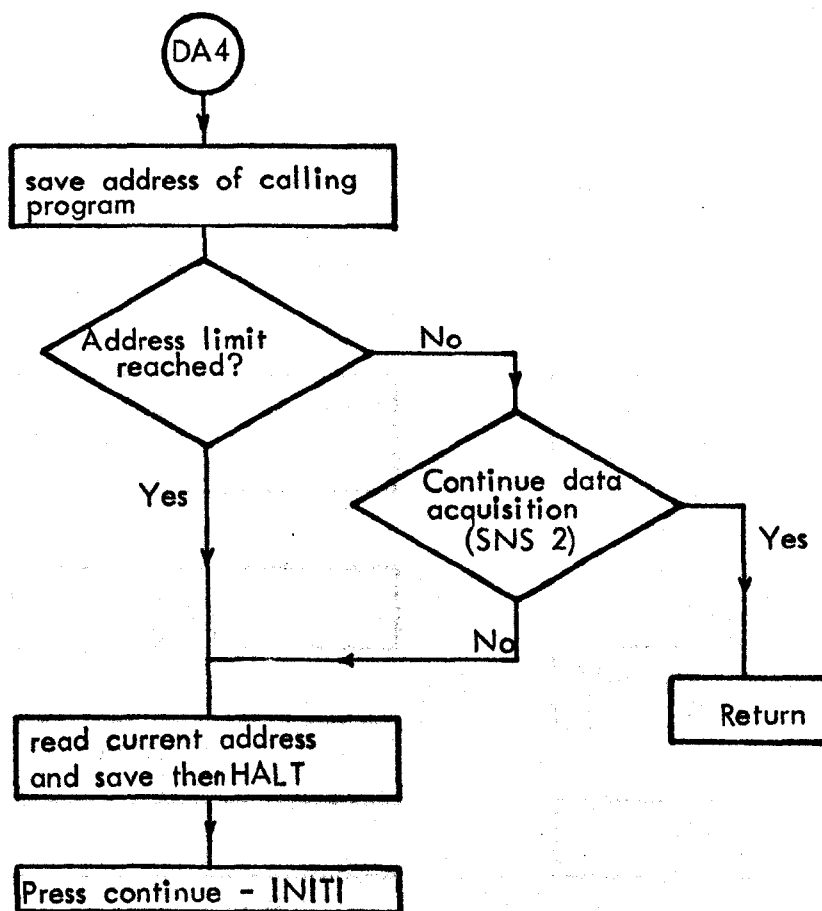
VJ PERKS

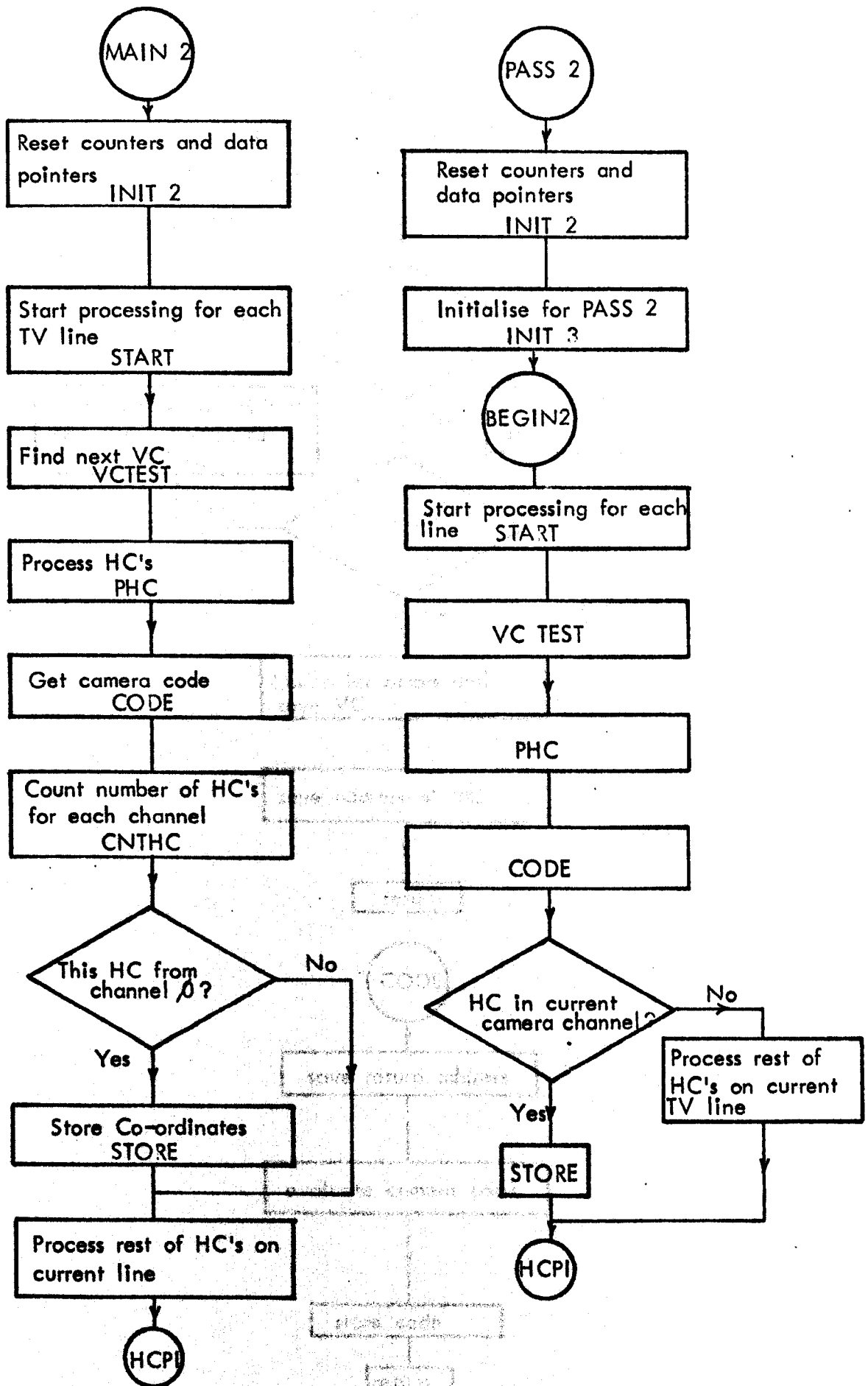
AUTO	0013
AP	0655
ARC	0035
ASR	0004
RSTAL	0003
R2	0002
CAF	0105
CALDR	0104
CAGJ	6303
CAGJFR	6305
CAGCJ	0762
CAMT	0772
CANV	1115
CANPL	6305
CITP	6347
CDPI	6344
CDP4	6342
CHIS	0575
CFC	6315
CLAD	6334
CLCAL	6304
CJDF	0737
CJUNT	0103
CSS	6316
D	0117
DPM	6341
LC	0006
Fr	0760
ENI	0540
FISCG	6322
FISP	0610
FPOJC	0610
IE	0512
LE1	0012
LA2	0013

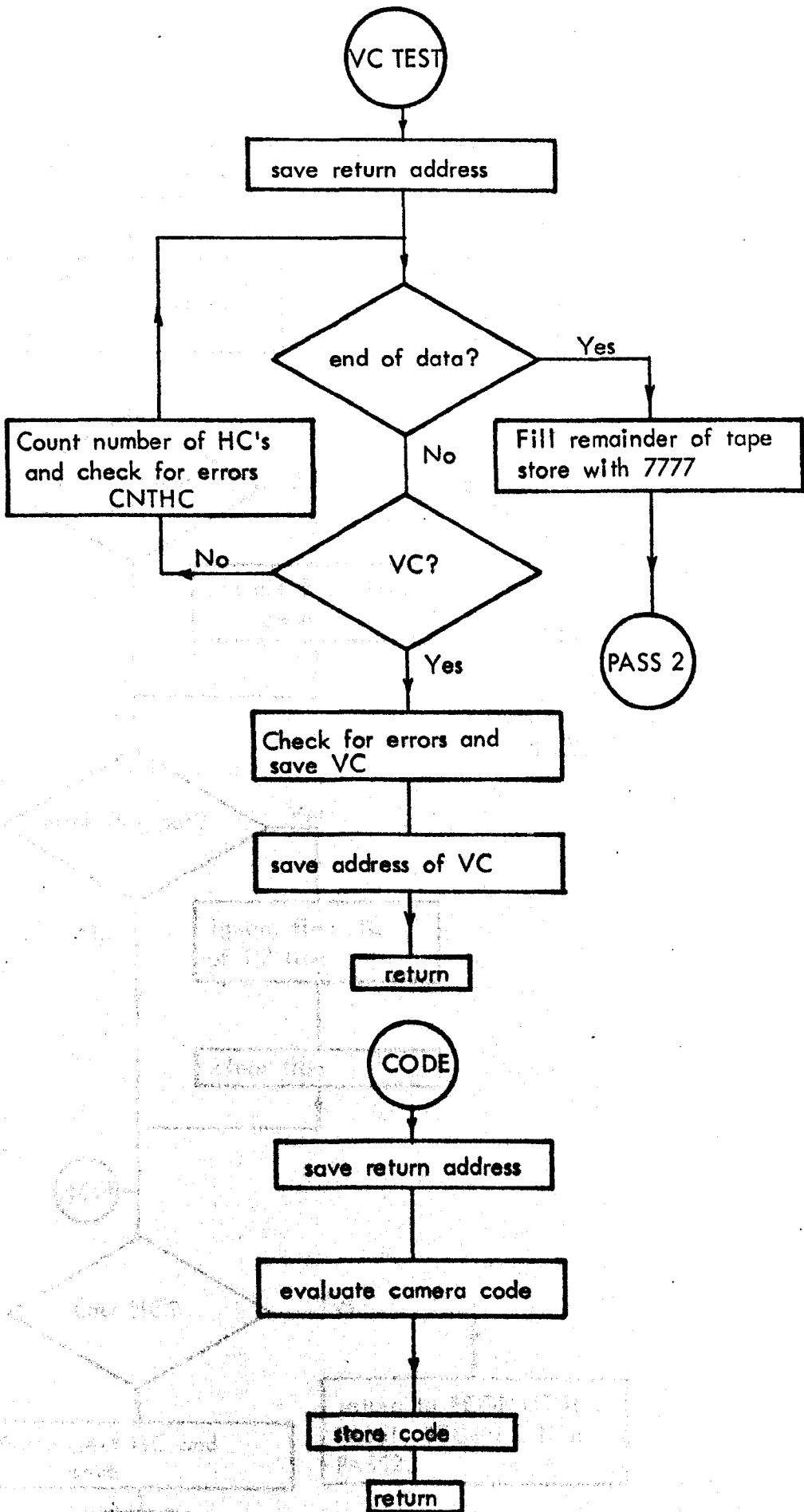
EVAD	6331
FVCG	6321
FVPTI	6343
EVFAD	6332
EVFC	6313
FVXON	6331
ENSM	6314
FCOUNT	0000
FLAG	0103
HC	0102
INLI.	1002
LCOUNT	0021
LECA	6333
LLCAM	6302
LLFC	6311
LLLC	6312
MARK	0776
MARKD	0546
MULI	0022
VDATA	0470
PHC	0720
PJINT	0552
RCA	6335
RCEA	6336
SCAC	6307
SCAVC	0674
SCPH	6345
SUPH	6346
VC	0101
VCTEST	0623
WJRD1	0535
WJRD2	0540

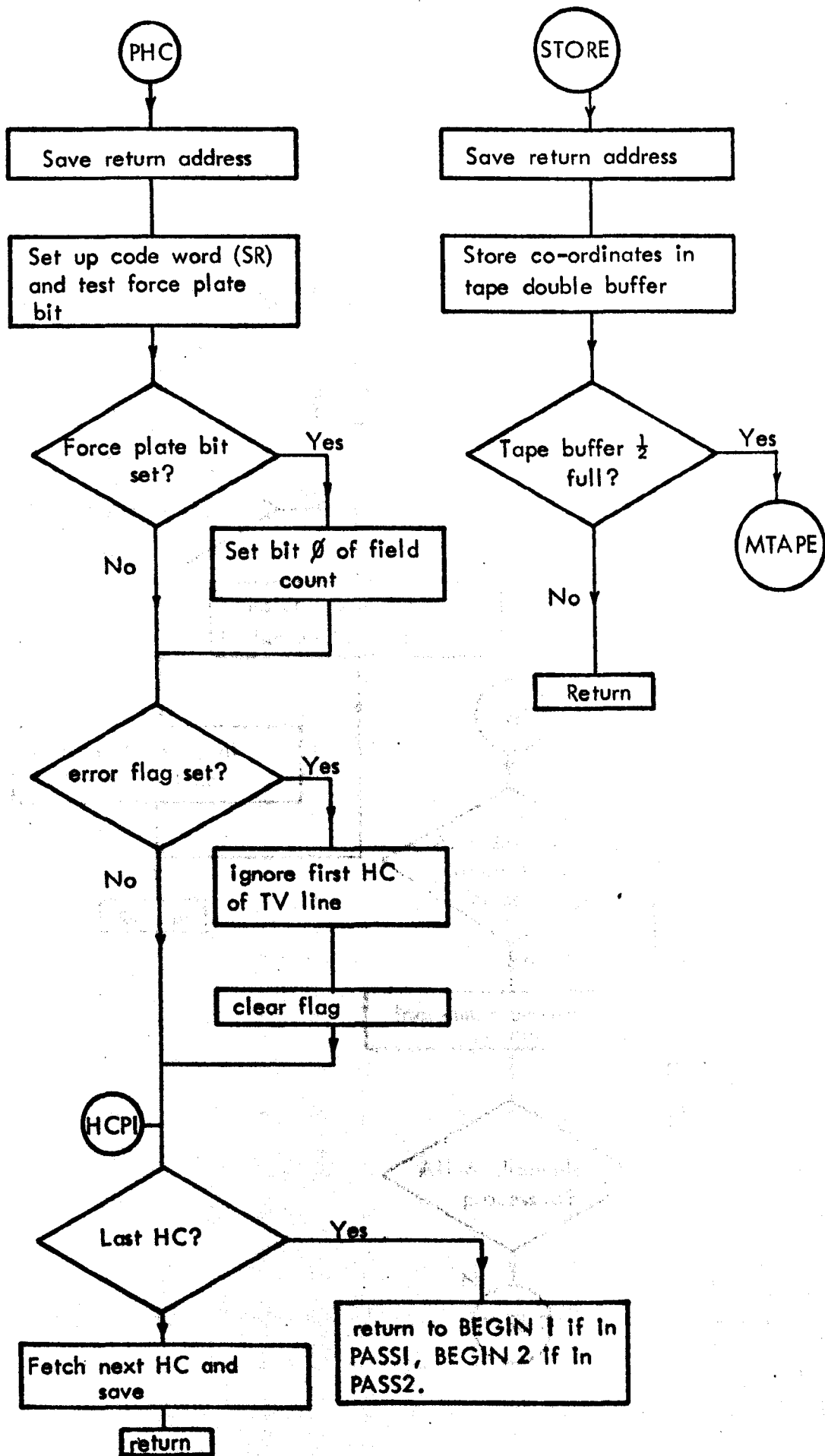
Flow Chart for data process program (DP).

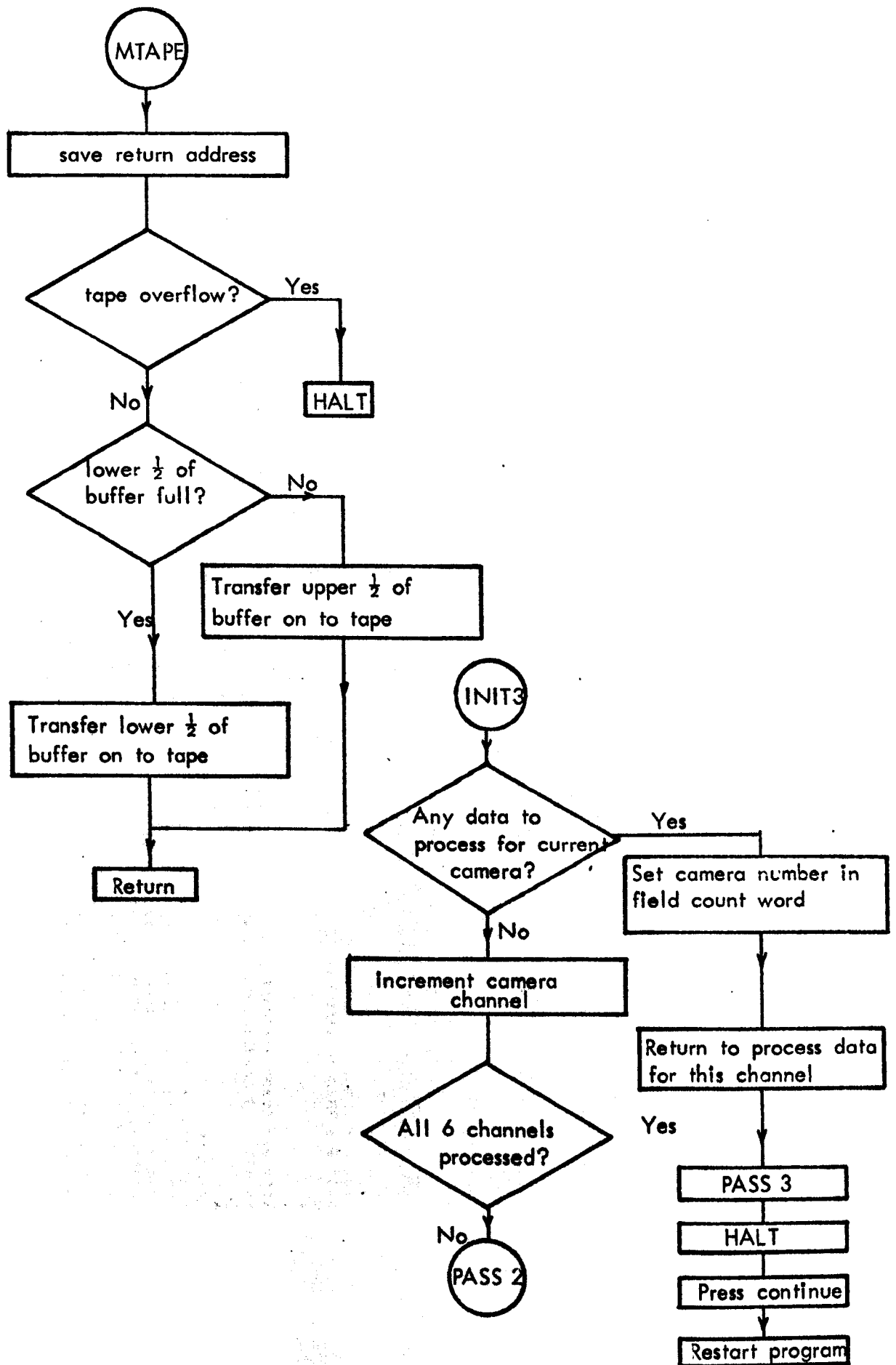













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0070 *20
0071 /DP - DATA PROCESS
0072 /
0073 /17-6-75
0074 /
0075 /
0076 /M.O.JARRETT.
0077 /
0078 /
0079 /PROGRAM CONTROLS ACQUISITION OF DATA
0080 /FROM INTERFACE, AND STORES IT ON TAPE
0081 /CONIT 3) IN THE FORMAT - TV FIELD NUMBER,
0082 /VERTICAL COORDINATE, HORIZONTAL
0083 /COORDINATE WITH THE CAMERA NUMBER
0084 /CONTAINED IN BITS 1, 2, 3 OF THE TV FIELD
0085 /NUMBER. BIT 0 OF THE TV FIELD NO IF SET
0086 /INDICATES THAT THE FORCE PLATE IS BEING
0087 /SAMPLED
0088 /
0089 /
0090 /B MODE I/O PRESET START 20 AFTER SETTING
0091 /DESIRED SENSE SWITCHES ETC.
0092 /
0093 /
0094 /
0095 /SVS 0 - ENABLES FIELD COUNT
0096 /SVS 1 - ENABLES SIMULATOR
0097 /SVS 2 - ENABLES COORDINATE GENERATOR
0098 /IF RESET AFTER ENABLING CG THEN CG IS
0099 /DISABLED AND THE DATA COLLECTED IS PROCESSED
0100 /SVS 3 - IF SET THEN ANALOGUE CHANNELS
0101 /WILL BE SAMPLED, IF RESET ONCE PROGRAMME
0102 /HAS STARTED THEN SAMPLING WILL BE
0103 /DISCONTINUED
0104 /
0105 /
0106 /PROGRAM HALTS AFTER DATA HAS BEEN COLLECTED
0107 /PRESS CONTINUE TO PROCESS AND STORE DATA.
0108 /
0109 /
0110 /SFCT - LOCATION 101 CONTAINS FIELD COUNT
0111 /SLCT - LOCATION 102 CONTAINS LINE COUNT
0112 /CAD - LOCATION 103 CONTAINS COMPOSITE
0113 /ADDRESS
0114 /EFA - LOCATION 104 CONTAINS END FIELD
0115 /ADDRESS
0116 /EMA - LOCATION 105 CONTAINS END MEMORY
0117 /ADDRESS
0118 /VCERR - LOCATION 106 CONTAINS COUNT OF
0119 /VERTICAL COORDINATE ERRORS
0120 /HCERR - LOCATION 101 CONTAINS COUNT OF
0121 /HORIZONTAL COORDINATE ERRORS
0122 /
0123 /
0124 /TFLK - LOCATION 104 CONTAINS INITIAL
0125 /BLOCK NUMBER FOR TAPE TRANSFER AT START
0126 /OF PROGRAM. EACH TIME A BLOCK IS USED
0127 /TFLK IS INCREMENTED. SET TO 100 WHEN
0128 /PROGRAM IS FIRST LOADED.
0129 /
0130 /

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0076 /
0077 /
0078 /INTERFACE STORES DATA STARTING AT LOCATION
0079 /0 OF MEMORY FIELD 1. ALL LOCATIONS UP
0080 /TO 7777 OF MEMORY FIELD 3 ARE USED. A
0081 /LIMIT MAY BE PLACED BY SETTING EFA AND
0082 /EMA, THESE ARE INITIALLY SET TO 2
0083 /AND 4000 RESPECTIVELY.
0084 /
0085 /
0086 /THE SAMPLED ANALOGUE DATA IS IN DE 0
0087 /ABSOLUTE LOCATIONS 6000-7777. IT IS
0088 /STORED IN THE FIRST SIX BLOCKS ON
0089 /TAPE IF SVS 3 IS SET. ONE TAPE BLOCK
0090 /FOR EACH ANALOGUE CHANNEL.
0091 /
0092 /
0093 /
0094 /
0095 /
0096 /
0097 /
0098 /
0099 /
0100 /
0101 /
0102 /
0103 /
0104 /
0105 /
0106 /
0107 /
0108 /
0109 /
0110 /
0111 /
0112 /
0113 /
0114 /
0115 /
0116 /
0117 /
0118 /
0119 /
0120 /
0121 /
0122 /
0123 /
0124 /
0125 /
0126 /
0127 /
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0164 /
0165 /
0166 /
0167 /
0168 /
0169 /
0170 /
0171 /
0172 /
0173 /
0174 /
0175 /
0200 5500
0201 6002
0202 7300
0203 1101
0204 6311
0205 7300
0206 1132
0207 6312
0210 7200
0211 1103
0212 6333
0213 6331
0214 6332
0215 6141
0216 1000
0217 0105
0220 1560
0221 0037
0222 4105
0223 0440
0224 6027
0225 0500
0226 6313
0227 0441
0230 6033
0231 0500
0232 6314
0233 0442
0234 6233
0235 0500
0236 6321
0237 0443
0241 6304
0241 0077
0242 3777
0243 0643
0244 3302
0245 7300
LOLSYM
PMODE
*20
JMP I DATA
*200
/ DATA ACQUISITION
DATA1, IOF
DISCG
CLA CLL
TAD SFCT /LD SIM FIELD CT
LDFC
CLA CLL
TAD SLCT
LILC /LD SIM LINE CT
CLA
TAD CAD
LLCA /LD COMPOSITE ADDR
ENAD /EN MEM ADDR
ENFAD /EN FIELD ADDR
LINC
LJODE
LEA
EMA
PCL I
0037
STC EMA
SVS 0
JMP ++3
IOF
ENFC /EN TV FIELD CT
SVS 1
JAP ++3
IOF
ENEM /EN SIMULATOR
SVS 2
JMP --1
IOF
ENUG /EN COORD GEN
SVS 3 /SAMPLE FORCE PLATE?
JAP DAB /NO
SET I 17 /YES
3777
LDF 3
PDP
PMODE
CLA CLL /CLR CLR CTRL REG

```

0175	0246	6132		CLLR	/RATE=STOP,MODE=0	0274	0336	0500		IOP	
0176	0247	1350		TAD K0260	/EN INT ON EVI	0275	0337	0335		RCA	
0177	0250	6134		CLEN	/I/P 1	0276	0340	4137		STC CA	/SAVE CURRENT MEM AD
0200	0251	6141		LINC		0277	0341	1020		LDA I	
0201				LMODE		0300	0342	0020		0020	
0202	0252	0443		SNS 3	/CONTINUE SAMPLING?	0301	0343	0004		ESF	
0203	0253	6304		JMP DA3	/NO	0302	0344	0000		HLT	
0204	0254	0002		PDP		0303	0345	6352		JMP INITI	
0205				PAJDE		0304	0346	6000	DA5,	JMP 0	/RETURN
0206	0255	6131	DA1,	CLSK	/SAM?	0305			/		
0207	0256	5251		JMP --5	/NO	0306				PMODE	
0210	0257	6141		LINC	/YES	0307	0347	7772	KN6,	-6	
0211				LMODE		0310	0350	0060	K0060,	0060	
0212	0260	0110	DA2,	SAM 10		0311	0351	7772	SCTR,	-6	
0213	0261	1077		STA I 17		0312			/		
0214	0262	0217		KSK 17		0313			/		
0215	0263	0456		SKP		0314			/		
0216	0264	6304		JMP DA3	/BUFFER FULL	0315			/		
0217	0265	0002		PLP		0316				LMODE	
0220				PMODE		0317				/FIRST INITIALISATION	
0221	0266	6135		CLSA		0320	0352	0062	INITI,	SET I 2	
0222	0267	2260		ISZ DA2		0321	0353	0000		0	
0223	0270	2351		ISZ SCTR		0322	0354	1020		LDA I	
0224	0271	5255		JMP LA1		0323	0355	0540		REGINI	
0225	0272	7200		CLA		0324	0356	4141		STC BEGIN	
0226	0273	1347		TAD KN6		0325	0357	1020		LDA I	
0227	0274	3351		DCA SCTR		0326	0360	0052		CCI	
0230	0275	1260		TAL DA2		0327	0361	1040		STA	
0231	0276	1347		TAD KN6		0330	0362	1615		I3	
0232	0277	3260		DCA DA2	/RESET SAM	0331	0363	1040		STA	
0233	0300	6141		LINC		0332	0364	1620		I4	
0234				LMODE		0333	0365	1040		STA	
0235	0301	6306		JMP DA4	/TEST FOR END DATA	0334	0366	1631		I5	
0236	0302	0002		PDP		0335	0367	1040		STA	
0237				PAJDE		0336	0370	1674		I6	
0240	0303	5255		JMP LA1		0337	0371	1220		LDA I	
0241				LMODE		0340	0372	0053		CCI	
0242	0304	6306	DA3,	JMP DA4	/TEST FOR END DATA	0341	0373	1040		STA	
0243	0305	6304		JMP --1		0342	0374	1606		I1	
0244	0306	1000	DA4,	LDA		0343	0375	1040		STA	
0245	0307	0000		0		0344	0376	1613		I2	
0246	0310	4346		STC DA5		0345	0377	1020		LLA I	
0247	0311	0500		IOP		0346	0400	0001		I	
0250	0312	6306		RCFA	/READ CURRENT FLD AD	0347	0401	4140		STC CC	
0251	0313	1440		SAE	/CFA=END FIELD ADDR ?	0350	0402	1000		LDA	/COMP ADDR TO AC
0252	0314	0104		EFA		0351	0403	0100		CAD	
0253	0315	6317		JMP ++2	/NO	0352	0404	1500		PCL I	
0254	0316	6322		JMP ++4	/YES	0353	0405	7770		7770	
0255	0317	0462		SNS I 2		0354	0406	0002		PEP	
0256	0320	6346		JMP DA5	/CONTINUE	0355				PMODE	
0257	0321	6331		JMP ++10		0356	0407	7041		CIA	/TWO'S COMPLEMENT
0260	0322	0500		IOP		0357	0410	1106		TAD CFA	
0261	0323	6335		RCA	/READ CURRENT ADDR	0360	0411	3213		LCA ++2	
0262	0324	1560		PCL I		0361	0412	5214		JMP ++2	
0263	0325	0007		0007		0362	0413	0000		0000	
0264	0326	1440		SAE		0363	0414	7006		RTL	/X4
0265	0327	0105		EFA		0364	0415	6141		LINC	
0266	0330	6346		JMP DA5	/NO	0365				LMODE	
0267	0331	0500		IOP	/YES,	0366	0416	4110		STC NLF	
0270	0332	6322		DISCG	/DISABLE CG	0367	0417	1000		LDA	
0271	0333	0500		IOP		0370	0420	0107		CA	
0272	0334	6306		RCFA		0371	0421	1120		ADA I	/DIV BY 1777
0273	0335	4136		STC CFA	/SAVE CURRENT FLD AD	0372	0422	6000		-1777	

0373	0423	0451	APU	/REMAINDER +VE	0472	0513	0077	SET I 17
0374	0424	0427	JMP ++3	/NO	0473	0514	3777	3777
0375	0425	0222	XSK I 2	/YES, INC QUOTIENT	0474	0515	6614	JMP AD1
0376	0426	6421	JMP --5	/REPEAT	0475	0516	1020	LDA I
0377	0427	1120	ADA I		0476	0517	2003	2003
0430	0430	1777	1777		0477	0520	4623	STC AD3
0431	0431	4111	STC REM	/SAVE REMAINDER	0500	0521	6614	JMP AD1
0402	0432	2032	ADD 2		0501	0522	6643	JMP TAI
0403	0433	2110	ADD NLF		0502	0523	1020	LDA I
0404	0434	4110	STC NLF	/STORE NO OF LINC	0503	0524	2004	2004
0405	0435	2103	ADD CAD	/FIELDS	0504	0525	4623	STC AD3
0436	0436	1560	BCL I		0505	0526	0077	SET I 17
0437	0437	7770	7770		0506	0527	3777	3777
0410	0440	4442	STC ++2		0507	0530	6614	JMP AD1
0411	0441	6443	JMP ++2		0510	0531	1020	LDA I
0412	0442	0000	0000	/INITIAL FIELD ADDR	0511	0532	2005	2005
0413	0443	0242	RUL 2	/K4	0512	0533	4623	STC AD3
0414	0444	1120	ADA I		0513	0534	6614	JMP AD1
0415	0445	0640	0640	/LDF N	0514	0535	6643	JMP TAI
0416	0446	4112	STC DFD1	/SAVE INIT LINC FIELD	0515	0536	0002	MAIN2, PDP
0417	0447	0011	CLH		0516			PDP
0421	0450	4050	STC 50		0517	0537	4514	JMS I INIT2
0421	0451	4051	STC 51		0520	0540	4535	BEGIN1, JMS I START
0422	0452	4052	STC 52		0521	0541	4500	JMS I VCTEST
0423	0453	4053	STC 53		0522	0542	4521	JMS I PHC
0424	0454	4054	STC 54		0523	0543	4522	JMS I CODE
0425	0455	4055	STC 55		0524	0544	7200	CLA
0426	0456	4056	STC 56		0525	0545	1123	TAD CAMCO
0427	0457	4057	STC 57		0526	0546	7100	CLL
0430	0460	4060	STC 60		0527	0547	7000	RAL
0431	0461	4061	STC 61		0530	0550	1124	TAD K2050
0432	0462	4062	STC 62		0531	0551	3355	DCA ++4
0433	0463	4063	STC 63		0532	0552	1355	TAD ++3
0434	0464	4064	STC 64		0533	0553	1125	TAD K1
0435	0465	4065	STC 65		0534	0554	3357	DCA ++3
0436	0466	4066	STC 66		0535	0555	0000	0000
0437	0467	4067	STC 67		0536			/ISZ, COUNTS HC FOR EACH CAMERA
0440	0470	0002	PDP		0537	0556	5361	JMP ++3
0441			PROBE		0540	0557	0000	0000
0442	0471	5513	JMP I MAIN		0540	0560	7200	CLA
0443	/	/	/		0542	0561	1123	TAD CAMCO
0444	/	/	/		0543	0562	7440	SZA
0445	/	/	/		0544	0563	5365	JMP ++2
0446	/	/	/		0545	0564	4527	JMS I STORE
0447			/MAIN PROGRAM		0546	0565	6141	LINC
0450	0472	6141	MAIN1, LINC		0547			LMODE
0451			LMODE		0550	0566	7247	JMP HCPI
0452	0473	0443	SNS 3		0551	0567	0000	HLT
0453	0474	6536	JMP MAIN2	/NO FP DATA TO STORE	0552	0570	0002	PASS2, PDP
0454	0475	1320	LDA I		0553			PROBE
0455	0476	2000	2000		0554	0571	4514	JMS I INIT2
0456	0477	4623	STC AD3		0555	0572	4537	JMS I INIT3
0457	0500	0077	SET I 17		0556	0573	4535	BEGIN2, JMS I START
0460	0501	3777	3777		0557	0574	4520	JMS I VCTEST
0461	0502	6614	JMP AD1		0560	0575	4521	JMS I PHC
0462	0503	1020	LDA I		0561	0576	4522	JMS I CODE
0463	0504	2001	2001		0562	0577	6141	LINC
0464	0505	4623	STC AD3		0563			LMODE
0465	0506	6614	JMP AD1		0564	0600	1000	LDA
0466	0507	6643	JMP TAI	/XFER 2 BLKS	0565	0601	0123	CAMCO
0467	0510	1320	LDA I		0566	0602	1440	SAP
0470	0511	2002	2002		0567	0603	0140	CC
0471	0512	4623	STC AD3		0570	0604	6610	JMP ++4

0767	0751	0471	AP0 I		1066	1347	7056	JMP END	
0773	0752	5417	STC M1		1067	1350	0011	CLR	
0771	0753	1023	LDA I		1070	1051	3005	ADD DFD	
0772	0754	7757	-2J		1071	1052	1120	ADA I	
0773	0755	5440	STC M2		1072	1053	0001	I	
0774	0756	1000	LDA		1073	1054	5005	STC DFD	/INC DATA FIELD CNT
0775	0757	2030	2030		1074	1055	7005	JMP DFD	
0776	0760	4116	STC TVC	/INITIAL VC	1075	1056	0016	NOP	
0777	0761	0202	PDP		1076	1057	0011	CLR	
1000			PMODE		1077	1060	2111	ADD RE4	/REMAINDER
1001	0762	5675	JMP I SINIT2		1100	1061	0017	COM	
1002					1101	1062	4005	STC COUNT	
1003					1102	1063	1020	LDA I	
1004					1103	1064	7776	-1	
1005					1104	1065	4006	STC FCOUNT	
1006					1105	1066	3005	ADD DFD	
1007					1106	1067	1120	ADA I	
1008					1107	1070	0001	I	
1009					1110	1071	5005	STC DFD	
1010					1111	1072	1020	LDA I	
1011	1000	0000	CUTEST, 0000	/RETURN JMP	1112	1073	7076	JMP ++3	
1012	1031	6141	LINC		1113	1074	5056	STC END	
1013			LMODE		1114	1075	7005	JMP DFD	/RETURN TO PROCESS
1014	1002	0225	XSK I COUNT		1115	1076	1020	LDA I	/REMAINDER
1015	1003	7005	JMP ++2		1116	1077	7101	JMP ++2	
1016	1004	7345	JMP FCNT	/END OF LINC FIELD	1117	1100	5056	STC END	
1017	1005	0644	DFD, 0644	/LDF CURRENT DATA FIELD	1120	1101	0066	SET I FCOUNT	/PROCESS LAST
1020					1121	1102	7776	-1	/LINE OF DATA
1021	1006	1021	LDA I STAD	/NEXT WORD	1122	1103	0065	SET I COUNT	
1022	1007	1560	DCL I		1123	1104	7776	-1	
1023	1010	1777	1777		1124	1105	1020	LDA I	
1024	1011	1460	SAF I	/VC ?	1125	1106	7111	JMP ++3	
1025	1012	6000	6000		1126	1107	5056	STC END	
1026	1013	7150	JMP CNTHC	/NO	1127	1110	7024	JMP IADDR+1	
1027	1014	1030	LDA	/YES	1130	1111	1020	LDA I	
1028	1015	0001	STAD		1131	1112	7777	7777	
1031	1016	1120	ADA I		1132	1113	4004	STC TVFCNT	
1032	1017	7776	-1		1133	1114	0017	COM	
1033	1020	1620	FSE I		1134	1115	4117	STC VC	
1034	1021	2000	2000		1135	1116	0017	COM	
1035	1022	1460	SAF I	/ERROR	1136	1117	4133	STC HC	
1036	1023	0000	IADDR, 0000		1137	1120	2134	ADD TELK	
1037	1024	7026	JMP ++2	/NO	1140	1121	1120	ADA I	
1040	1025	7035	JMP ++10	/YES	1141	1122	0022	2	
1041	1026	1001	LDA STAD		1142	1123	5132	STC ++7	
1042	1027	4116	STC TVC		1143	1124	0002	PLP	
1043	1030	1000	LDA		1144			PMODE	
1044	1031	0001	STAD		1145	1125	4527	JMS I STORE	
1045	1032	5323	STC IADDR		1146	1126	7000	CLA	
1046	1033	0002	PDP		1147	1127	1134	TAD TELK	
1047			PMODE		1150	1130	6141	LINC	
1050	1034	5630	JMP I CUTEST	/RETURN	1151			LMODE	
1051			LMODE		1152	1131	1460	SAF I	/TAPE STORE FULL ?
1052	1035	0002	PDP		1153	1132	0000	0000	/ITLK+2
1053			PMODE		1154	1133	7124	JMP --7	/NO
1054	1036	2130	ISZ VCERR	/INC VC ERROR COUNT	1155	1134	6570	JMP PASS2	/YES
1055	1037	5241	JMP ++2						
1056	1040	7402	HLT	/TOO MANY ERRORS					
1057	1041	7240	STA	/SET AC=7777					
1060	1042	3132	DCA RFLAG1	/SET ERROR FLAG					
1061	1043	6141	LINC						
1062			LMODE						
1063	1044	7150	JMP CNTHC						
1064	1045	0226	XSK I FCOUNT						
1065	1046	7050	JMP ++2						

*1150
/COUNT HORIZONTAL COORDINATES & SH. WORD

1165			LMODE		1264	1244	5256	STC LHC	/LAST HC
1166	1150	0002	CNTHC,	PDP	1265	1245	0011	CLR	
1167			PMODE		1266	1246	4132	STC EFLAG1	/CLEAR FLAG
1170	1151	2115	ISZ HCNT	/ERROR?	1267	1247	0011	HCPI,	CLR
1171	1152	5357	JMP ++5	/NO	1270	1250	3217	ADD AD	
1172	1153	2131	ISZ HCERR	/YES,COUNT ERRORS	1271	1251	1120	ADA I	
1173	1154	5356	JMP ++2		1272	1252	7776	-1	
1174	1155	7432	HLT	/TOO MANY ERRORS	1273	1253	1000	STA	
1175	1156	5541	JMP I BEGIN	/RETURN TO START	1274	1254	1217	AD	
1176	1157	6141	LINC		1275	1255	1460	SAE I	/LAST HC ?
1177			LMODE		1276	1256	0000	LHC,	0000
1200	1160	1001	LDA STAD	/STORE CURRENT HC	1277	1257	7262	JMP ++3	/NO
1201				IN APROPRIATE LOC	1300	1260	0002	PDP	/YES
1202	1161	4030	TEMSTO, 4030	/STC 30,31,32 ETC	1301			PMODE	
1203	1162	3161	ADD TEMSTO		1302	1261	5541	JMP I BEGIN	
1204	1163	1120	ADA I		1303			LMODE	
1205	1164	0001	I		1304	1262	5263	STC ++1	
1206	1165	5161	STC TEMSTO		1305	1263	0000	0000	/ADD LATPST HC
1207	1166	7002	JMP CVTEST+2		1306	1264	4133	STC HC	
1210					1307	1265	0002	PDP	
1211					1310			PMODE	
1212					1311	1266	5600	JMP I SPHC	
1213					1312				
1214			*1200		1313	1267	0000	PH2,	0
1215			/SUBROUTINE TO PROCESS HORIZONTAL COORDINATE		1314				
1216			PMODE		1315				
1217	1207	0000	SPHC,	/RETURN JMP	1316				
1220	1201	6141	LINC		1317			/SUBROUTINE TO EVALUATE CAMERA CODE	
1221			LMODE		1320	1270	0000	SCODE,	0000
1222	1202	1030	LDA		1321	1271	6141		/RETURN JMP
1223	1203	0115	HCNT		1322			LMODE	
1224	1204	1120	ADA I		1323	1272	0011	CLR	
1225	1205	0005	S		1324	1273	2136	ADD SR	
1226	1206	1100	ADA I		1325	1274	3261	RCL I 1	
1227	1207	2030	0030	/ADD I-7	1326	1275	4136	STC SR	/REPLACE SHIFTL SR
1230	1210	5217	STC AD		1327	1276	2130	ADD HC	
1231	1211	2115	ADD HCNT	/FORM RCL N TO	1330	1277	0312	RCL I 2	
1232	1212	1660	RCL I	/CORRECTLY ORIENTATE	1331	1300	0261	RCL I 1	
1233	1213	0007	0007	/SR WORD-	1332	1301	1560	RCL I	
1234	1214	1120	ADA I		1333	1302	7770	7770	
1235	1215	0053	0053	/RCL N FORMED	1334	1303	4120	STC CAMCO	/CAMERA CODE
1236	1216	5231	STC PH1		1335	1304	0002	PDP	
1237	1217	0000	AD,	/ADD HCN	1336			PMODE	
1240	1220	0263	RCL I 3		1337	1305	5670	JMP I SCODE	/RETURN
1241	1221	0472	LZE I	/F P?	1340				
1242	1222	7231	JMP PH1	/NO	1341				
1243	1223	5267	STC PH2	/YES	1342				
1244	1224	2034	ADD TVFCNT		1343				
1245	1225	1620	RSE I		1344			/SUBROUTINE TO STORE COORDINATES	
1246	1226	4030	4030		1345	1336	0000	SSTORE,	0000
1247	1227	4034	STC TVFCNT		1346	1307	6141	LINC	/RETURN JMP
1250	1230	3267	ADD PH2		1347			LMODE	
1251	1231	0000	PH1,	/RCL N	1350	1310	0641	LLF I	
1252	1232	4136	STC SR	/SAVE SR WORD	1351	1311	1000	LDA	
1253	1233	2132	ADD EFLAG1		1352	1312	0004	TVFCNT	/TV FILL COUNT
1254	1234	0471	APD I	/FLAG SET ?	1353	1313	1003	STA I TRUE	
1255	1235	7242	JMP ++5	/NO	1354	1314	0000	ASK I TCNT	/TAPE BUFFER FULL?
1256	1236	1020	LDA I	/YES	1355	1315	7317	JMP ++2	/NO
1257	1237	0030	0030		1356	1316	7450	JMP STAPE	/YES,STORE ON TAPE
1260	1240	5256	STC LHC	/LAST HC	1357	1317	1000	LLA	
1261	1241	7245	JMP ++4		1360	1320	0117	VC	/VERTICAL COORDINATE
1262	1242	1020	LDA I		1361	1321	1063	STA I TRUE	
1263	1243	2027	2027		1362	1322	0200	ASK I TCNT	

1363	1323	7325	JMP +2	1462	/
1364	1324	7453	JMP MTAPE	1463	/
1365	1325	1300	LDA	1464	/
1366	1326	0133	HC	1465	/
1367	1327	1563	FCL I	1466	/
137J	133J	6000	6000	1467	/
1371	1331	1963	STA I TRUF	1470	/SUBROUTINE TO STORE DATA ON TAPE
1372	1332	0222	XSK I TCNT	1471	LMODE
1373	1333	7335	JMP +2	1472	MTAPE, LDA
1374	1334	7453	JMP MTAPE	1473	J
1375	1335	0002	PDP	1474	STC RETURN
1376			P.MODE	1475	LDA I
1377	1336	5736	JMP I SSTORE	1476	1
1400			/	1477	AKO
1401			/	1500	LDA I
1402			/	1501	-1000
1403			/	1502	STC TCNT
1404			/	1503	/RESET TCNT
1405			/SUBROUTINE TO START PROCESSING	1504	LDA
1406			*1400	1504	TRLK
1407	1400	0000	STARTI, 0000	1505	ADA I
1410	1401	6141	LINC	1506	1
1411			LMODE	1507	STA
1412	1402	1000	LDA I	1510	TRLK
1413	1403	7771	-6	1511	ADA I
1414	1404	4115	STC HCNT	1512	3000
1415	1405	1000	LLA I	1513	APJ
1416	1406	4000	4000	1514	HLT
1417	1407	5161	STC TEMSTO	1515	/YES
1420	1410	2116	ADD TVC	1515	LDA
1421	1411	1563	FCL I	1516	/NO
1422	1412	6000	6000	1516	TRUF
1423	1413	1000	STA	1517	ADA I
1424	1414	0117	VC	1520	1000
1425	1415	1120	ADA I	1521	APJ I
1426	1416	7307	-470	1522	JMP +6
1427	1417	3471	M1, APO I	1523	LDA
1430	1420	7446	JMP M3	1524	TRLK
1431	1421	0224	XSK I TVFCNT	1524	ADA I
1432	1422	1000	LLA	1525	6000
1433	1423	0004	TVFCNT	1526	JMP +5
1434	1424	1560	FCL I	1527	LDA
1435	1425	7403	7400	1530	TRLK
1436	1426	146J	SAL I	1531	ADA I
1437	1427	0377	0377	1532	1120
1440	1430	7436	JMP M1A	1533	4000
1441	1431	1000	LDA	1534	STC +2
1442	1432	0004	TVFCNT	1535	0714
1443	1433	1560	FCL I	1536	XFER1, 0000
1444	1434	0377	0377	1537	LDA
1445	1435	4004	STC TVFCNT	1540	XFER1
1446	1436	0011	M1A, CLR	1541	ADA I
1447	1437	1120	ADA I	1542	1001
1450	1440	7757	M2, -20	1543	STA
1451	1441	3417	ADD M1	1544	XFER2
1452	1442	5417	STC M1	1545	LLA
1453	1443	3440	ADD M2	1546	XFER1
1454	1444	0917	COM	1547	FCL I
1455	1445	5440	STC M2	1550	7000
1456	1446	2202	M3, PDP	1551	ADA I
1457			P.MODE	1552	1
1460	1447	5600	JMP I STARTI	1553	STA
1461			/RETURN	1554	TRLK
				1555	ADA I
				1556	3000
				1557	APJ
				1560	HLT
					/TAPE OVERFLOW?
					/YES

1561	1537	0714	0714	/NO, WRC 1	1660	1662	1020	LDA I	
1562	1540	0000	XFR2, 0000		1661	1663	0001	I	
1563	1541	0000	RETURN, 0000	/RETURN JMP	1662	1664	1140	ADM	
1564	/	/	/		1663	1665	0140	CC	/INC CHAN COUNTER
1565	/	/	/		1664	1666	1460	SAE I	
1566	/	/	/		1665	1667	0006	6	
1567	/	/	/		1666	1670	6570	JMP PASS2	
1573	/	/	/		1667	1671	6611	JMP PASS3	
1571	/	/	/		1670	1672	0011	RET, CLR	
1572			*1600		1671	1673	1040	STA	
1573			/THIRD INITIALISATION, PASS 2		1672	1674	0052	16, CC1	
1574	1600	0000	SINIT3, 0000	/RETURN ADDR	1673	1675	1000	LDA	
1575			PMODE		1674	1676	0140	CC	
1576	1601	6141	LINC		1675	1677	0250	RDL 10	
1577			LMODE		1676	1700	5703	STC 17	
1600	1602	1020	LDA I		1677	1701	2004	ADD TVFCNT	
1601	1603	0573	PEGIN2		1700	1702	1620	BSE I	
1602	1604	4141	STC BEGIN		1701	1703	0000	17, 0000	
1603	1605	1203	LDA		1702	1704	4004	STC TVFCNT	
1604	1606	0053	11, CC2		1703	1705	0002	PDP	
1605	1607	0470	AZE I		1704			PMODE	
1606	1610	7617	JMP T2		1705	1706	5600	JMP I SINIT3	
1607	1611	0011	CLR		1706			/	
1610	1612	1040	STA		1707			/	
1611	1613	0053	12, CC2	/CLEAR CC2	1710			/	
1612	1614	1040	STA		1711			/	
1613	1615	0052	13, CC1	/CLEAR CC1	1712			/	
1614	1616	7672	JMP RET		1713			/	
1615	1617	1000	12, LDA		1714				*1
1616	1620	0052	14, CC1		1715	0001	0000	STAD, 0	
1617	1621	0451	APD	/>2000 PTS ?	1716	0002	0000	TCNT, 0	
1620	1622	7672	JMP RET	/YES RETURN	1717	0003	0000	TRUF, 0	
1621	1623	1120	ADA I		1720	0004	0000	TVFCNT, 0	
1622	1624	3400	3400		1721	0005	0000	COUNT, 0	
1623	1625	0451	APD	/>256 PTS	1722	0006	0000	FCOUNT, 0	
1624	1626	7672	JMP RET	/YES, RETURN	1723	0007	0000	LFCNT, 0	
1625	1627	0011	CLR	/NO	1724			/	
1626	1630	1040	STA		1725			/	
1627	1631	0052	15, CC1		1726			/	
1630	1632	1020	LDA I		1727				*52
1631	1633	0002	2		1730	0052	0000	CC1, 0	
1632	1634	1140	ADM		1731	0053	0000	CC2, 0	
1633	1635	1606	I1		1732			/	
1634	1636	1020	LDA I		1733			/	
1635	1637	0002	2		1734			/	
1636	1640	1140	ADM		1735			/	
1637	1641	1613	I2		1736			/	
1640	1642	1020	LDA I		1737				*100
1641	1643	0002	2		1740	0100	0000	DATA, 200	
1642	1644	1140	ADM		1741	0101	0000	SFCT, 0	
1643	1645	1615	I3		1742	0102	0036	SLCT, 236	
1644	1646	1020	LDA I		1743	0103	0031	CAD, 0031	
1645	1647	0002	2		1744	0104	0002	EFA, 2	
1646	1650	1140	ADM		1745	0105	4000	EMA, 4000	
1647	1651	1620	I4		1746	0106	0000	CFA, 0	
1650	1652	1020	LDA I		1747	0107	0000	CA, 0	
1651	1653	0002	2		1750	0110	0000	NLF, 0	
1652	1654	1140	ADM		1751	0111	0000	NFG, 0	
1653	1655	1631	I5		1752	0112	0000	DFD1, 0	
1654	1656	1020	LDA I		1753	0113	0472	MAIN, MAIN1	
1655	1657	0002	2		1754	0114	0670	INITE, SINIT2	
1656	1660	1140	ADM		1755	0115	0000	HCNT, 0	
1657	1661	1674	I6		1756	0116	0000	TVC, 0	

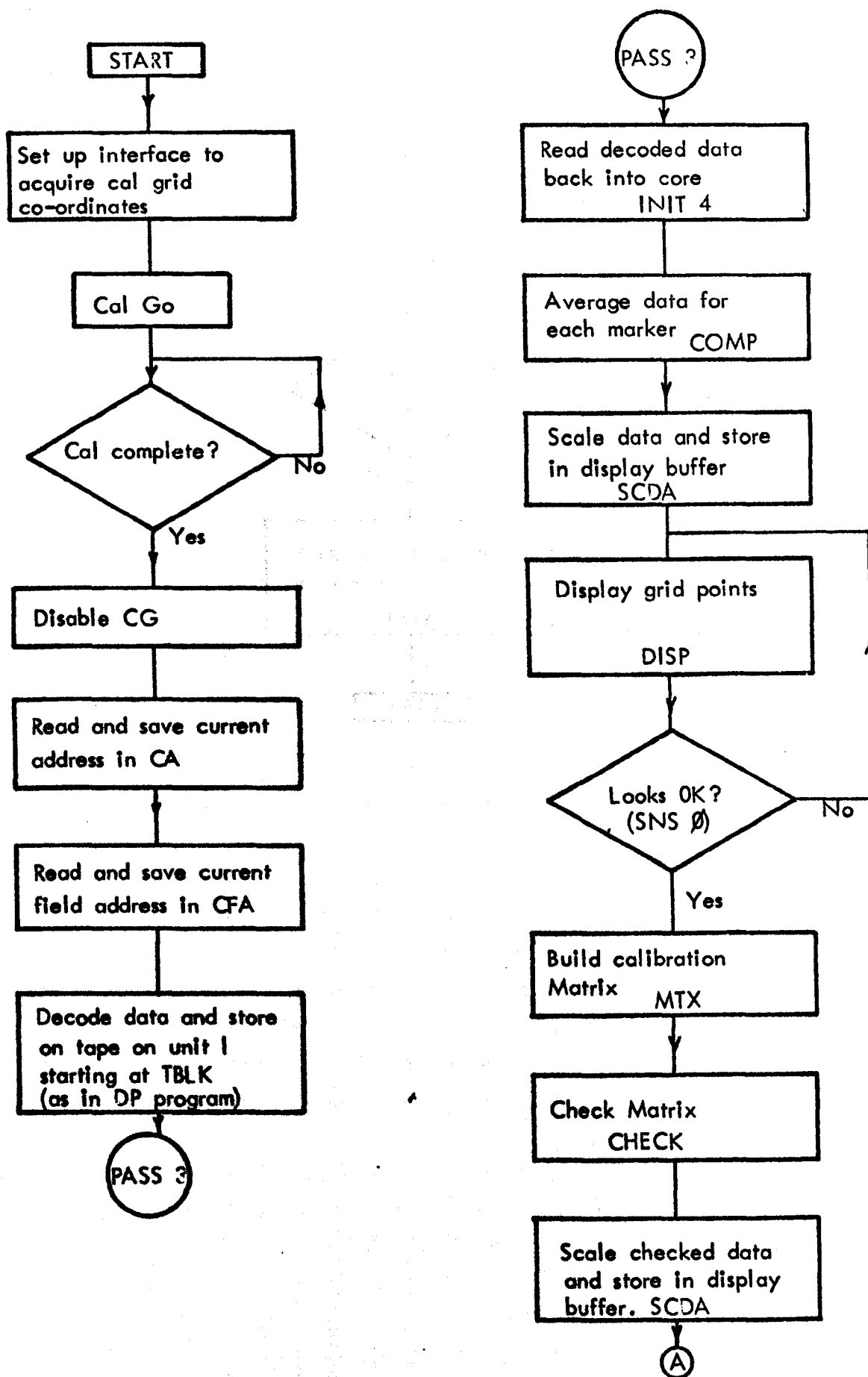
1757	0117	0000	VC,	0
1763	0120	1000	VCTFST,	CVTEST
1761	0121	1200	PHC,	SPHC
1762	0122	1270	CODE,	SCODE
1763	0123	0000	CAMCO,	0
1764	0124	2050	K2050,	2050
1765	0125	0001	K1,	1
1766	0126	1247	HCP,	HCP1
1767	0127	1306	STORE,	SSTORE
1770	0130	0000	VCERR,	0
1771	0131	0000	HCERR,	0
1772	0132	0000	EFLAG1,	0
1773	0133	0000	HC,	0
1774	0134	0077	TBLK,	77
1775	0135	1400	START,	START1
1776	0136	0000	SR,	0
1777	0137	1600	INIT3,	SINIT3
2001	0140	0001	CC,	1
2001	0141	0540	BEGIN,	BEGIN1
2002			/	
2003			/	
2004			/	
2005			/	

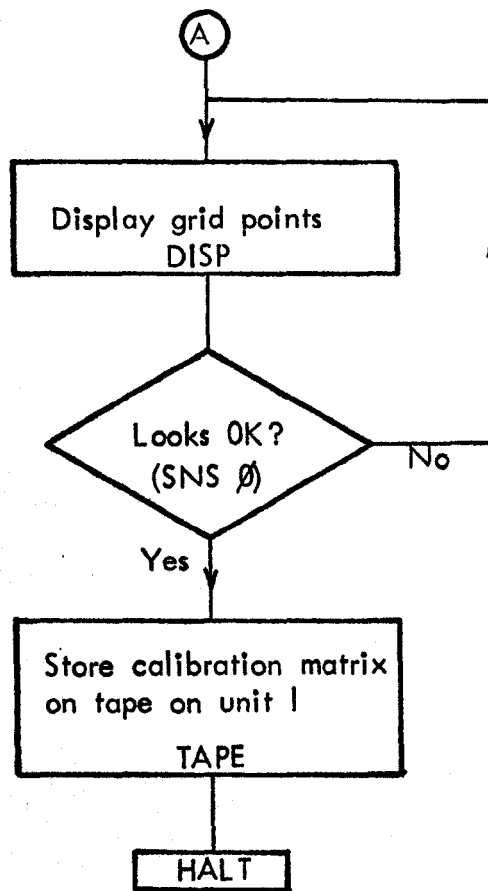
NO ERRORS

AE	1217
AD1	0614
AD2	0621
AD3	0623
AD4	0642
PERIN	0141
PERIN1	0540
PERIN2	0573
CA	0137
CAL	0133
CAG0	6303
CAGJFR	6305
CAMCO	0123
CC	0140
CCAF1	6306
CC1	0352
CC2	0353
CDFF	6347
CDPI	6344
CDP4	6342
CFA	0136
CFG	6315
CLAD	6334
CLCAL	6344
CVTFC	1150
CODE	0122
COUNT	0005
CSM	6316
CVTEST	1000
DATA	0130
DATA1	0200
DA1	1255
DA2	0260
DA3	0374
DA4	0306
DA5	0346
DA6	6341

DFD	1005
DFD1	0112
DISCG	6322
EFA	0104
EFLAG1	0132
EKA	0105
ENAD	6331
ENCG	6321
END	1056
ENDPI	6343
ENDI	1077
ENFAD	6332
ENFC	6313
ENMON	6301
FJSM	6314
FCNT	1045
FCOUNT	0006
HC	0133
HCERR	0131
HCNT	0115
HCP	0126
HCP1	1247
IADDR	1003
INIT1	0352
INIT2	0114
INIT3	0137
I1	1606
I2	1613
I3	1615
I4	1620
I5	1631
I6	1674
I7	1703
K06	0347
K0060	0350
K1	0125
K2050	0124
LDCA	6333
LDCAM	6302
LDPC	6311
LPLC	6312
LFCNT	0007
LHC	1256
MAIN	0113
MAIN1	0472
MAIN2	0536
MTAPE	1450
M1	1417
M1A	1436
M2	1440
M3	1446
VLF	0110
PASS2	0573
PASS3	0611
PHC	1121
PH1	1231
PH2	1267
PCA	6335
TCFA	6336
REF	0111
REF1	1672
RETURN	1541
SCAC	6307
SCPH	6345
SCOLE	1070
SCPH	0351
SECT	0131
SINIT2	0675
SINIT3	1600
SLCT	0102
SPHC	1200
SR	0136
SSTORE	1306
STAD	0001
START	0135
START1	1470
STDEF	0127
SUPH	6346
TA1	0643
TA2	0657
TA3	0666
TA4	0674
TFLK	0134
TRF	0003
TCNT	0002
TRMST0	1161
TVC	0116
TVCNT	0004
T2	1617
VC	0117
VCERR	0130
VCTEST	0120
XFER1	1514
XFER2	1540

Flow chart for calibration grid data acquisition (CALMTX)





```

0000 *20
0001 /CALMIX - CALIBRATION MATRIX ACQUISITION.
0002 /
0003 /16-5-75
0004 /
0005 /M.O.JARRETT.
0006 /
0007 /
0010 /8 MODE I/O PRESET, START 20.
0011 /
0012 /
0013 /PROGRAM OBTAINS THE CO-ORDINATES OF A
0014 /GRID OF POINTS AND STORES IN THE FORMAT
0015 /UC,HC, ON TAPE ON UNIT 1 STARTING AT TAPE
0016 /BLOCK HELD IN TRK (LOCATION 2360) INITIALLY
0017 /SET TO 200. THIS DATA IS THEN REDUCED TO
0020 /PROVIDE AN AVERAGED PAIR OF CO-ORDINATES FOR
0021 /EACH GRID POINT, AND STORED IN A CALIBRATION
0022 /MATRIX - FORMAT:-
0023 /
0024 /UC,UC ... UC,K ...K,HC,HC ...HC,K ...K
0025 /
0026 /.
0027 /.
0028 /.
0029 /
0030 /UC,UC ... UC,K ...K,HC,HC ...HC,K ...K
0031 / K, K ... K,K ...K, K, K ... K,K ...K
0032 /
0033 /.
0034 /.
0035 /.
0036 /.
0037 /.
0038 /
0039 / K, K ... K,K ...K, K, K ... K,K ...K
0040 /
0043 /WHERE K=3777; THE MATRIX OCCUPIES LOCATIONS
0044 /0000 - 2777 OF A MODE DATA FIELD. FOR A
0045 /CALIBRATION GRID OF 25X10 THE ACTIVE ELEMENTS
0046 /OF THE MATRIX WILL BE FOUND IN LOCATIONS
0047 /0000 - 2377.
0048 /
0051 /THE MATRIX IS STORED ON TAPE IN 6 BLOCKS
0052 /STARTING AT THE BLOCK NUMBER HELD IN CTRLK
0053 /(LOCATION 5273) INITIALLY SET TO 20.
0054 /
0055 LOESYM
0056 PNODE
0057 *10
0060 0010 7777 GD, 7777
0061 0011 0000 FVC, 0
0062 /
0063 /
0064 MTKV=GD
0065 MTKH=FVC
0066 /
0067 /
0070 *20
0071 0020 5521 JMP I CAL
0072 /
0073 /
0074 /
0075 /

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0076 /
0077 /
0080 0050 0000 *50
0081 0051 0000 0
0082 0052 0000 CCI, 0 /COORDINATE COUNTS
0083 0053 0000 CC2, 0 /FOR EACH CAMERA
0084 0054 0000 0 /CHANNEL
0085 0055 0000 0
0086 0056 0000 0
0087 0057 0000 0
0088 0058 0000 0
0089 0059 0000 0
0090 0061 0000 0
0091 0062 0000 0
0092 0063 0000 0
0093 /
0094 /
0095 /
0096 /
0097 /
0098 /
0099 /
0100 0100 0000 SFCT, *100
0101 0101 0036 SLCT, 0 /TV FIELD COUNT
0102 0102 0001 CAD, 36 /SIM LINE COUNT
0103 0103 0000 CFA, 0 /COMPOSITE ADDR
0104 0104 0000 CA, 0 /CURRENT FIELD
0105 0105 2100 INITE, SINITE /CURRENT ADDR
0106 0106 2600 VCTEST, CVTEST
0107 0107 2434 PFC, SPFC
0108 0110 2512 CODE, SCODE
0109 0111 0000 CAMC01, 0
0110 0112 2050 K2050, 2050
0111 0113 0001 K1, 1
0112 0114 2542 STORE, SSTORE
0113 0115 2400 START, START1
0114 0116 3067 INIT3, SINIT3
0115 0117 0001 CC, 1
0116 0120 0000 BEGIN, BEGIN1
0117 0121 0000 CAL, SCAL
0118 0122 7400 CAM, 7400 /CAL RM MARGIN
0119 0123 3474 DISP, SDISP
0120 0124 7777 K01, -1
0121 0125 7776 K01, 7776
0122 0126 3400 SCDA, SSCDA
0123 0127 0000 NELS, 0000 /NO DATA WORDS
0124 0130 3000 INIT4, SINIT4
0125 0131 0000 TRAP, 0000
0126 0132 2200 INIT1, SINIT1
0127 0133 4000 COAP, SCOAP
0128 0134 4400 RED, SRED
0129 0135 4531 STOR, SSTOR
0130 0136 3777 K3777, 3777
0131 0137 4600 MTK, S4TK
0132 0140 5063 CHCK, SCHECK
0133 0141 0037 K37, 37
0134 0142 0077 K77, 77
0135 0143 0137 K137, 137
0136 0144 5200 TAPE, STAPE
0137 /
0138 /
0139 /
0140 /
0141 /
0142 /
0143 /
0144 0200 0000 SCAL, *200
0145 0000 0000 0000

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0175	0201	6322	DISCG		0274	0267	4506	JMS I VCTEST
0176	0202	7200	CLA		0275	0270	4507	JMS I PHC
0177	0203	1192	TAD CAD		0276	0271	4510	JMS I CODE
0200	0204	6333	LDCA	/COMPOSITE ADDR	0277	0272	6141	LINC
0201	0205	7200	CLA		0300			LMODE
0202	0206	1122	TAD CAM		0301	0273	1000	LDA
0203	0207	6302	LECAM	/RH MARGIN	0302	0274	0541	CAMCO
0204	0210	6303	CAGO		0303	0275	1440	SAE
0205	0211	6331	EVAD	/EN ADDR	0304	0276	0117	CC
0206	0212	6332	EVFAD	/EN FIELD ADDR	0305	0277	6303	JMP ++4
0207	0213	7200	CLA		0306	0300	0022	PLP
0210	0214	1101	TAD SLCT		0307			PMODE
0211	0215	6312	LILC	/LD SIM LINE CNT	0310	0301	4514	JMS I STORE
0212	0216	6314	ENSM	/EN SIMULATOR	0311	0302	6141	LINC
0213	0217	6321	EVCG		0312			LMODE
0214	0220	6337	SCAC	/CAL COMPLETE?	0313	0303	0601	LIF 1
0215	0221	5220	JMP --1	/NJ	0314	0304	6472	JMP HCPI
0216	0222	6302	DISCG	/YES	0315	0305	0002	PLP
0217	0223	6306	CCAFI	/CLR CAL COMP FLAG	0315			PMODE
0220	0224	6335	HCA		0317	0306	4530	JMS I INIT4
0221	0225	3104	DCA CA	/CURRENT MEM ADDR	0320	0307	4533	JMS I COMP
0222	0226	6336	RCFA		0321	0310	4535	JMS I STOR
0223	0227	3103	DCA CFA	/CURRENT FIELD ADDR	0322	0311	4526	JMS I SCDA
0224					0323	0312	4523	JMS I DISP
0225					0324	0313	4537	JMS I MTX
0226					0325	0314	4540	JMS I CHECK
0227					0326	0315	4526	JMS I SCDA
0230					0327	0316	4523	JMS I DISP
0231					0330	0317	4544	JMS I TAPE
0232					0331	0320	7402	HLT
0233	0230	4532	JMS I INIT1		0332			
0234	0231	4505	MAIN1, JMS I INIT2		0333			
0235	0232	4515	REGIN1, JMS I START		0334			
0236	0233	4506	JMS I VCTEST		0335			
0237	0234	4507	JMS I PHC		0336			
0240	0235	4510	JMS I CODE		0337			
0241	0236	7200	CLA		0340			
0242	0237	1111	TAD CAMCO1	/CAMERA CODE	0341			LMODE
0243	0240	7100	CLL		0342			SEGANT 1
0244	0241	7304	BAL	/X10 (BINARY)	0343			/FIRST INITIALISATION
0245	0242	1112	TAL K2050	/ISZ FORMED	0344			*200
0246	0243	3247	DCA ++4		0345			PMODE
0247	0244	1247	TAL ++3		0346	2200	0000	SINIT1, 0000
0250	0245	1113	TAD K1	/2ND ISZ	0347	2201	6141	LINC
0251	0246	3251	DCA ++3		0350			LMODE
0252	0247	0000	0000	/ISZ, COUNTS HC FOR	0351	0202	0062	SET I 2
0253	0250	5253	JMP ++3	/EACH CAMERA	0352	0203	0030	3
0254	0251	0000	0000	/OVERFLOW	0353	0204	0040	LDF 3
0255	0252	7200	CLA		0354	0205	1020	LDA I
0256	0253	1111	TAD CAMCO1		0355	0206	0232	REGIN1
0257	0254	7440	SZA	/CAM 0?	0356	0207	1040	STA
0260	0255	5257	JMP ++2	/NO	0357	0210	2120	REGIN12000
0261	0256	4514	JMS I STORE	/YES, STORE COORDS	0360	0211	1020	LDA I
0262	0257	6141	LINC		0361	0212	2052	CC112000
0263			LMODE		0362	0213	1040	STA
0264	0260	0601	LIF 1		0363	0214	1106	I3
0265	0261	6472	JMP HCPI		0364	0215	1040	STA
0266	0262	0000	HLT		0365	0216	1112	I4
0267	0263	0002	PMODE		0366	0217	1040	STA
0270					0367	0220	1120	I5
0271	0264	4505	JMS I INIT2		0370	0221	1040	STA
0272	0265	4516	JMS I INIT3		0371	0222	1167	I6
0273	0266	4515	REGIN2, JMS I START		0372	0223	1020	LDA I

0373	0224	2053	CC12000		0472	0321	2050	5012000	/USED TO COUNT
0374	0225	1040	STA		0473	0322	1040	STA	/NO OF COLUMNS
0375	0226	1077	I1		0474	0323	2051	5112000	/FROM EACH
0376	0227	1040	STA		0475	0324	1040	STA	/CAMERA CHAN
0377	0230	1104	I2		0476	0325	2052	5212000	
0400	0231	1020	LDA I	/CAMERA CHAN	0477	0326	1040	STA	
0401	0232	0001	I	/SET TO 1 FOR	0500	0327	2053	5312000	
0402	0233	1040	STA	/FIRST PASS OF	0501	0330	1040	STA	
0403	0234	2117	CC12000	/PASS 2	0502	0331	2054	5412000	
0404	0235	1000	LDA	/COMP ADDR TO AC	0503	0332	1040	STA	
0405	0236	2102	CAD12000		0504	0333	2055	5512000	
0406	0237	1560	NCL I		0505	0334	1040	STA	
0407	0240	7770	7770		0506	0335	2056	5612000	
0410	0241	0002	PDP		0507	0336	1040	STA	
0411			PMODE		0510	0337	2057	5712000	
0412	0242	7041	CIA	/CALCULATE NO OF	0511	0340	1040	STA	
0413	0243	1103	TAD CFA	/LINC FIELDS IN	0512	0341	2060	6012000	
0414	0244	3046	DCA ++2	/COMPLETE PMODE	0513	0342	1040	STA	
0415	0245	5047	JMP ++2	/FIELDS	0514	0343	2061	6112000	
0416	0246	0000	0000		0515	0344	1040	STA	
0417	0247	1246	TAD --1		0516	0345	2062	6212000	
0420	0250	1246	TAD --2		0517	0346	1040	STA	
0421	0251	1246	TAD --3		0520	0347	2063	6312000	
0422	0252	1246	TAD --4	/K4	0521	0350	1000	LDA	/INITIAL TAPE
0423	0253	6141	LINC		0522	0351	0360	ITLK	/BLOCK NO
0424			LOAD		0523	0352	4361	STC ITLK	
0425	0254	4355	STC NLF		0524	0353	0002	PDP	
0426	0255	1000	LDA		0525			PMODE	
0427	0256	2124	CA12000		0526	0354	5600	JMP I SINIT1	
0430	0257	0471	AP0 I	/<2 LINC FIELDS ?	0527			/	
0431	0260	6065	JMP ++5	/YES	0530			/	
0432	0261	1560	ECL I	/NO	0531			LOAD	
0433	0262	4000	4000		0532			/	
0434	0263	0002	SET I 2		0533	0355	0000	NLF, 0	
0435	0264	0002	2		0534	0356	0000	REL, 0	
0436	0265	1100	ALA I	/DIV BY 2000	0535	0357	0000	DFD1, 0	
0437	0266	5777	-2000		0536	0360	0177	TLK, 177	
0440	0267	4451	AP0	/REMAINDER +VE?	0537	0361	0177	ITLK, 177	
0441	0270	6273	JMP ++3	/NO	0540			/	
0442	0271	0222	XSR I 2	/YES, INC QUOTIENT	0541			/	
0443	0272	6265	JMP --5	/REPEAT	0542			/	
0444	0273	1100	ALA I		0543			/	
0445	0274	2000	2000		0544			/SUBROUTINE FOR SECOND INITIALISATION	
0446	0275	4356	STC REL	/SAVE REMAINDER	0545			*100	
0447	0276	2002	ADD 2	/CALCULATE AND	0546			PMODE	
0450	0277	2355	ADD NLF	/STORE TOTAL NO	0547	2100	0000	SINIT2, 0000	/RETURN JMP
0451	0300	4355	STC NLF	/OF LINC FIELDS	0550	2101	6141	LINC	
0452	0301	1000	LIA		0551			LOAD	
0453	0302	2102	CA12000		0552	0102	1000	LDA	
0454	0303	1560	ECL I		0553	0103	0355	NLF	
0455	0304	7770	7770		0554	0104	0017	COM	
0456	0305	4307	STC ++2		0555	0105	0070	ARE I	/NLF=0 ?
0457	0306	6310	JMP ++2		0556	0106	6116	JMP ++10	/YES
0460	0307	0000	0000	/INITIAL PMODE	0557	0107	4006	STC PCOUNT	/SET UP LINC FIELD
0461	0310	2307	ADD --1	/FIELD ADDR	0560	0110	1000	LDA I	/COUNTER
0462	0311	2307	ADD --2		0561	0111	0016	NOP	/SET END TO NOP
0463	0312	2307	ADD --3		0562	0112	4655	STC END	
0464	0313	2307	ADD --4	/K4	0563	0113	0005	SET I COUNT	
0465	0314	1100	ALA I		0564	0114	1777	1777	
0466	0315	0040	0640	/LDF N	0565	0115	6127	JMP ++12	
0467	0316	4357	STC DFD1	/SAVE INIT LINC FIELD	0566	0116	1000	LIA	/<1 LINC FIELD
0470	0317	0011	CLR		0567	0117	0000	REM	/OF DATA SET
0471	0320	1040	STA	/THESE LOCATIONS	0570	0120	0017	004	/COUNT TO REM

0571	0121	4335	STC COUNT	/AND LINC FIELD	0670	0414	0537	VC	
0572	0122	0366	SET I FCOUNT	/COUNT TO -1	0671	0415	1120	ADA I	
0573	0123	7776	-1		0672	0416	7307	-473	
0574	0124	1000	LDA	/SET END TO JMP	0673	0417	4471	APJ I	/NEW TV FIELD?
0575	0125	0677	ENDI	/INST AT ENDI	0674	0423	6432	JMP ++12	
0576	0126	4656	STC END		0675	0421	0224	XSK I TVFCNT	/INC TV FIELD COUNTER
0577	0127	0062	SET I TCNT	/2 BLOCKS OF DATA	0676	0422	0011	CLR	
0580	0130	6777	-1000		0677	0423	1120	ADA I	
0601	0131	0063	SET I TRUF	/TAPE BUFFER SET	0700	0424	7757	-20	
0602	0132	3777	3777		0701	0425	2417	ADD --6	/ADD APO INST
0603	0133	0061	SET I STAD	/START ADDR FOR	0702	0426	4417	STC --7	/REPLACE MODIFIED INS
0604	0134	2000	2000	/DATA	0703	0427	2424	ADD --3	
0605	0135	0064	SET I TVFCNT	/TV FIELD COUNT	0704	0430	0017	COM	
0606	0136	0000	0000		0705	0431	4424	STC --5	/APO I INS CHANGED
0607	0137	1000	LDA		0706	0432	0002	PDP	
0610	0140	0357	DFDI		0707			PAJDE	
0611	0141	1043	STA		0710	2433	5600	JMP I START1	/RETURN
0612	0142	0144	++2		0711			/	
0613	0143	4605	STC DFD		0712			/	
0614	0144	0000	0000	/LDF N	0713			/	
0615	0145	1000	LDA I		0714			/	
0616	0146	2000	2000		0715			/SUBROUTINE TO PROCESS HORIZONTAL COORDINATE	
0617	0147	4600	STC IADDR	/INITIAL ADDR	0716	2434	0000	SPHC, 0000	/RETURN JMP
0620	0150	4771	STC VCERR	/VCERR SET TO 0	0717	2435	6141	LINC	
0621	0151	4770	STC HCERR	/HCERR SET TO 0	0720			LADDE	
0622	0152	4772	STC EFLAG1	/EFLAG1 SET TO 0	0721	0436	1000	LDA	
0623	0153	1023	LDA I		0722	0437	0773	HCNT	
0624	0154	0471	APJ I	/TV FIELD COUNT	0723	0440	1120	ADA I	
0625	0155	4417	STC M1	/ROUTINE SET TO	0724	0441	0005	5	
0626	0156	1020	LDA I	/INITIAL CONDITIONS	0725	0442	1120	ADA I	
0627	0157	7757	-20		0726	0443	2030	2030	/ADD 1-7
0630	0159	4424	STC M2		0727	0444	4453	STC AD	
0631	0161	1000	LDA		0730	0445	2773	ALL HCNT	/FORM POL N TO
0632	0162	2000	2000		0731	0446	1660	BCO I	/CORRECTLY ORIENTATE
0633	0163	4774	STC TVC	/INITIAL VC	0732	0447	0037	0037	/SR WORD.
0634	0164	0002	PLP		0733	0450	1120	ADA I	
0635			PRODR		0734	0451	0256	0256	/ROL N, FORMED
0636	2165	5700	JMP I SINIT2		0735	0452	4454	STC ++2	
0637			/		0736	0453	0000	AD, 0000	/ADD HCN
0640			/		0737	0454	0000	0000	/ROL N
0641			/		0740	0455	4540	STC SR	/SAVE SR WORD
0642			/		0741	0456	2772	ADD EFLAG1	
0643			/		0742	0457	0471	APJ I	/FLAG SET ?
0644			/		0743	0460	6465	JMP ++5	/NO
0645			/		0744	0461	1020	LDA I	/YES
0646			/		0745	0462	2030	2030	
0647			/		0746	0463	4501	STC LHC	/LAST HC
0650			LNODE		0747	0464	6470	JMP ++4	
0651			/SUBROUTINE TO START PROCESSING		0750	0465	1020	LDA I	
0652			*400		0751	0466	2027	2027	
0653	2400	0000	START1, 0000		0752	0467	4501	STC LHC	/LAST HC
0654	2401	6141	LINC		0753	0470	0011	CLR	
0655			LADDE		0754	0471	4772	STC EFLAG1	/CLEAR FLAG
0656	0402	1024	LEA I		0755	0472	0011	HCPI, CLR	
0657	0403	7771	-6		0756	0473	2453	ADD AD	
0660	0404	4773	STC HCNT	/SET UP HC COUNTER	0757	0474	1120	ADA I	
0661	0405	1020	LDA I		0760	0475	7776	-1	
0662	0406	4000	4000		0761	0476	1000	STA	
0663	0407	4762	STC TEMSTO	/RESET TEMSTO	0762	0477	0453	AD	
0664	0410	2774	ADD TVC		0763	0500	1460	SAR I	/LAST HC ?
0665	0411	1560	RCL I		0764	0501	0000	LHC, 0000	/2027 OR 2030
0666	0412	6000	6000		0765	0502	6502	JMP ++3	/NO
0667	0413	1040	STA	/NEXT VC STORED	0766	0503	0002	PDP	/YES, RETURN TO

0767			P.MODE	/PROCESS NEXT	1066	0561	0002	PEP		
0770	2504	5520	JMP I RFGIN	/TV LINE OF DATA	1067			PMODE		
0771			L.MODE		1070	2562	5742	JMP I SSTORE		
0772	0505	4506	STC .+1		1071			/		
0773	0506	0030	0000	/ADD LATEST HC	1072			/		
0774	0507	4536	STC HC		1073			/		
0775	0510	0302	PDP		1074					
0776			P.MODE		1075			L.MODE		
0777	2511	5634	JMP I SPHC		1076			/SUBROUTINE TO TEST FOR VERTICAL COORDINATE		
1000					1077			*600		
1001					1100	2600	0000	PMODE		
1002					1101	2601	6141	CUTEST,	/RETURN JMP	
1003					1102			LINC		
1004			/SUBROUTINE TO EVALUATE CAMERA CODE		1103	0602	0225	L.MODE		
1005	2512	0030	SCODE,	0330	/RETURN JMP	1104	0633	6605	XSK I COUNT	
1006	2513	6141	LINC		1105	0604	6645	JMP .+2		
1007			L.MODE		1106	0605	0644	JMP FCNT	/END OF LINC FIELD	
1010	0514	0011	CLR		1107	0606	1021	DFD,	0644	
1011	0515	2540	ADD SR		1110	0607	1560	LDA I STAD	/FIELD	
1012	0516	0261	ROL I 1		1111	0610	1777	RCL I	/NEXT WORD	
1013	0517	4540	STC SR	/REPLACE SHIFTED SR	1112	0611	1460	SAE I	/VC ?	
1014	0520	2536	ALD HC		1113	0612	6000	0000		
1015	0521	0312	RJR 12		1114	0613	6751	JMP CNTHC	/NO	
1016	0522	0261	ROL I 1		1115	0614	1000	LDA	/YES	
1017	0523	1560	RCL I		1116	0615	0001	STAD		
1020	0524	7770	7770		1117	0616	1120	ADA I		
1021	0525	4541	STC CAMCO	/CAMERA CODE	1120	0617	7776	-1		
1022	0526	2536	ALD HC		1121	0620	1620	PEP I		
1023	0527	1560	RCL I		1122	0621	2303	0000		
1024	0530	6400	6000		1123	0622	1460	SAE I	/ERROR	
1025	0531	4536	STC HC		1124	0623	0000	IADDR,	0000	
1026	0532	0002	PEP		1125	0624	6626	JMP .+2	/NO	
1027			P.MODE		1126	0625	6635	JMP .+10	/YES	
1030	2533	1341	TAD CAMCO		1127	0626	1001	LDA STAD	/VC FOR NEXT TV	
1031	2534	3111	DCA CAMCO1		1130	0627	4774	STC TVC	/LINE STORED	
1032	2535	5712	JMP I SCODE	/RETURN	1131	0630	1000	LDA		
1033					1132	0631	0001	STAD	/INITIAL ADDR	
1034			L.MODE		1133	0632	4623	STC IADDR	/RESET	
1035					1134	0633	0002	PEP		
1036	0536	0000	HC,	0	1135			PMODE		
1037	0537	0000	VC,	0	1136	2634	5600	JMP I CUTEST	/RETURN	
1040	0540	0000	SR,	0	1137			L.MODE		
1041	0541	0000	CAMCO,	0	1140	0635	0002	PDP		
1042					1141			P.MODE		
1043			P.MODE		1142	2636	2371	ISZ VCERR	/INC VC ERROR COUNT	
1044					1143	2637	5241	JMP .+2		
1045			/SUBROUTINE TO STORE COORDINATES		1144	2640	7432	HLT	/TOO MANY ERRORS	
1046	2542	0000	SSTORE,	0000	/RETURN JMP	1145	2641	7240	STA	/SET AC=7777
1047	2543	6141	LINC		1146	2642	3372	DCA EFLAG1	/SET ERROR FLAG	
1050			L.MODE		1147	2643	6141	LINC		
1051	0544	0650	LDF 10		1150			L.MODE		
1052	0545	1000	LDA		1151	0644	6751	JMP CNTHC		
1053	0546	0537	VC	/VERTICAL COORDINATE	1152	0645	0226	FCNT,	XSK I FCOUNT	
1054	0547	1063	STA I TBUF		1153	0646	6650	JMP .+2	/INC LINC FIELD	
1055	0550	0222	XSK I TCNT	/TAPE BUFFER FULL?	1154	0647	6656	JMP END	/END OF L FIELDS	
1056	0551	6553	JMP .+2	/NO	1155	0650	0011	CLR		
1057	0552	7000	JMP MTAPE	/YES, STORE ON TAPE	1156	0651	2605	ADD DFD	/INCREMENT CURRENT	
1060	0553	1000	LDA		1157	0652	1120	ADA I	/LINC FIELD	
1061	0554	0536	HC	/HORIZONTAL COORD	1160	0653	0001	I		
1062	0555	1063	STA I TBUF		1161	0654	4605	STC DFD		
1063	0556	0222	XSK I TCNT		1162	0655	6605	JMP DFD		
1064	0557	6561	JMP .+2		1163	0656	0016	END,	VOP	
1065	0560	7000	JMP MTAPE		1164	0657	0011	CLR		

1165	0663	2356	ADD REM	/SET UP COUNTER	1264					
1166	0661	0317	COM	/FOR REMAINDER	1265					
1167	0662	4005	STC COUNT		1266					
1170	0663	1020	LDA I		1267	0751	0032	CNTHC,	PPD	
1171	0664	7776	-1		1270				PMODE	
1172	0665	4006	STC FCOUNT		1271	2752	2373		ISZ HCNT	/ERROR?
1173	0666	2635	ADD DFD	/INC LINC FIELD	1272	2753	5360		JMP ++5	/NO
1174	0667	1120	ADA I		1273	2754	2370		ISZ HCERR	/YES, COUNT ERRORS
1175	0670	2091	I		1274	2755	5357		JMP ++2	
1176	0671	4635	STC DFD		1275	2756	7402		HLT	/TOO MANY ERRORS
1177	0672	1020	LDA I		1276	2757	5520		JMP I BEGIN	/RETURN TO START
1207	0673	6676	JMP ++3		1277	2760	6141		LINC	
1208	0674	4656	STC END		1300				LMODE	
1209	0675	6605	JMP D+D	/RETURN TO PROCESS	1301	0761	1001		LDA STAD	/STORE CURRENT HC
1209	0676	1020	LDA I	/REMAINDER	1302					IN APPROPRIATE LOC
1209	0677	6701	JMP ++2		1303	0762	4030	TEMSTO,	4030	/STC 30, 31, 32 ETC
1209	0700	4656	STC END		1304	0763	2762		ALL TEMSTO	
1206	0701	0066	SET I FCOUNT	/PROCESS LAST	1305	0764	1120		ADA I	
1207	0702	7776	-1	/TV LINE OF	1306	0765	3001		I	
1210	0703	3065	SET I COUNT	/DATA	1307	0766	4762		STC TEMSTO	
1211	0704	7776	-1		1310	0767	6602		JMP CVTEST+2	
1210	0705	1020	LDA I		1311					
1213	0706	6711	JMP ++3		1312					
1214	0707	4656	STC END	/RESET END	1313					
1215	0710	6624	JMP IADDR+1		1314	0770	0030	HCERR,	0	
1216	0711	1320	LDA I	/FILL REMAINING	1315	0771	0000	VCERR,	0	
1217	0712	7777	7777	/TAPE LOCATIONS	1316	0772	0000	EFLAG1,	0	
1220	0713	4537	STC VC	/WITH 7777	1317	0773	7771	HCNT,	-6	
1221	0714	0017	COM		1320	0774	0000	TVC,	0	
1222	0715	4536	STC HC		1321					
1223	0716	0017	COM		1322					
1224	0717	4004	STC TVFCNT		1323					
1225	0720	2033	ADD TRUF		1324					
1226	0721	1120	ADA I		1325					
1227	0722	4777	-3000		1326					
1230	0723	0471	APD I		1327					
1231	0724	6727	JMP ++3		1330	1000	1000	MTAPE,	+1000	
1232	0725	1120	ADA I		1331	1031	0000		LDA	
1233	0726	1000	1000		1332	1002	5066		STC RETURN	/SAVE RETURN ADDRESS
1234	0727	0640	LDF 0		1333	1003	1020		LDA I	
1235	0730	1040	STA		1334	1004	6777		-1000	
1236	0731	2127	JWLS:2000		1335	1005	4000		STC TCNT	/RESET TCNT
1237	0732	0011	CLR		1336	1006	1000		LDA	
1240	0733	2360	ALL TELK		1337	1007	0000		TELK	
1241	0734	1120	ADA I		1340	1010	1120		ADA I	
1242	0735	0002	2		1341	1011	0001		I	
1243	0736	4745	STC ++7		1342	1012	1040		STA	
1244	0737	0032	PLP		1343	1013	0000		TELK	
1245			PAJDE		1344	1014	1120		ADA I	
1246	2740	4514	JMS I STORE		1345	1015	0000		3000	
1247	2741	6141	LINC		1346	1016	0451		APD	/TAPE OVERFLOW?
1250			LMODE		1347	1017	0000		HLT	/YES
1251	0742	1000	LDA		1350	1020	1000		LDA	/NO
1252	0743	0360	TELK		1351	1021	0000		TRUF	
1253	0744	1460	SAP I	/TAPE STORE FULL ?	1352	1022	1120		ADA I	
1254	0745	0000	0000	/TELK+2	1353	1023	1000		1000	
1255	0746	6737	JMP --7	/NO	1354	1024	0471		APD I	/LOWER HALF FULL?
1256	0747	0600	LIF 0		1355	1025	7033		JMP ++6	/YES
1257	0750	6263	JMP PASS2	/YES	1356	1026	1000		LDA	/NO, UPPER HALF FULL
1260					1357	1027	0000		TELK	
1261					1360	1030	1120		ADA I	
1262					1361	1031	6000		6000	
1263					1362	1032	7037		JMP ++5	

1363	1333	1000	LDA		1462	1120	7165	JMP RET	/YES, RETURN
1364	1034	0360	TLK		1463	1121	0311	CLR	/NO
1365	1035	1120	ADA I		1464	1122	1040	STA	
1366	1336	4300	4300		1465	1123	2052	CC1!2000	
1367	1037	5341	STC +2		1466	1124	1020	LDA I	/INCREMENT CURRENT
1370	1040	0714	0714	/WRC 1	1467	1125	0002	2	/CAMERA CHANNEL
1371	1041	0000	0000		1470	1126	1140	ADM	/COORDINATE COUNTERS
1372	1042	1000	LDA		1471	1127	1377	11	/BY 2 IN THESE
1373	1043	1041	XFER1		1472	1130	1020	LDA I	/LOCATIONS
1374	1044	1120	ADA I		1473	1131	0002	2	
1375	1045	1001	1001		1474	1132	1140	ADM	
1376	1046	1040	STA		1475	1133	1104	12	
1377	1047	1065	XFER2		1476	1134	1020	LDA I	
1433	1353	1030	LDA		1477	1135	0002	2	
1431	1051	1041	XFER1		1500	1136	1140	ADM	
1432	1052	1560	ECL I		1501	1137	1106	13	
1433	1353	7040	7000		1502	1140	1020	LDA I	
1434	1054	1120	ADA I		1503	1141	0032	2	
1435	1055	0311	1		1504	1142	1140	ADM	
1436	1056	1040	STA		1505	1143	1112	14	
1437	1057	0360	TLK	/RESET TLK	1506	1144	1020	LDA I	
1410	1060	1120	ADA I		1507	1145	0002	2	
1411	1061	3000	3000		1510	1146	1140	ADM	
1412	1062	0451	APJ	/TAPE OVERFLOW?	1511	1147	1123	15	
1413	1063	0000	HLT	/YES	1512	1150	1020	LDA I	
1414	1064	0714	0714	/NO, WRC 1	1513	1151	0002	2	
1415	1065	0000	0000		1514	1152	1140	ADM	
1416	1066	0000	0000	/RETURN JMP	1515	1153	1167	16	
1417	/	/	/		1516	1154	1000	LIA I	
1420	/	/	/		1517	1155	0001	1	
1421	/	/	/		1520	1156	1140	ADM	
1422	/	/	/		1521	1157	2117	CC12030	/INC CHAN COUNTER
1423	/	/	/		1522	1160	0600	LIF 0	
1424	/	/	/		1523	1161	1460	SAE I	/ALL CHANS PROC?
1425	1067	0000	SINIT3, 0000	/RETURN ADDR	1524	1162	0006	6	
1426			PRODE		1525	1163	6263	JMP PASS2	/NO, REPEAT PASS 2
1427	3073	6141	LINC		1526	1164	6305	JMP PASS3	/YES
1430			LMODE		1527	1165	0011	RET,	
1431	1071	0640	LDF 0		1530	1166	1040	STA	
1432	1072	1020	LDA I	/SETS UP RETURN	1531	1167	2052	16,	
1433	1073	0266	PFGIN2	/LOCATION FOR	1532	1170	0302	PDP	
1434	1074	1040	STA	/COORD PROCESSING	1533			PRODE	
1435	1075	2120	BEGIN!2030	/IN PASS 2	1534	3171	5667	JMP I SINIT3	
1436	1076	1000	LDA		1535			/	
1437	1077	0353	11,	CC2!2000	/ANY COORDS	1536		/	
1443	1100	0470	AZE I	/COLLECTED BY	1537			/	
1441	1101	7111	JAP T2	/CURRENT CAM CHAN?	1540			LMODE	
1442	1102	0011	CLR	/YES, CLEAR CURRENT	1541			/SUBROUTINE TO INITIALIZE PASS 3	
1443	1103	1040	STA	/CAM CHAN COORD	1542			*1200	
1444	1104	2053	12,	CC2!2000	/COUNTERS	1543		PRODE	
1445	1105	1040	STA		1544	3200	0000	SINIT4, 0000	
1446	1106	2052	13,	CC1!2000		1545	3201	6141	LINC
1447	1107	0002	PDP		1546			LMODE	
1450			PRODE		1547	1202	0011	CLR	
1451	3113	5667	JMP I SINIT3		1550	1203	5267	STC NR1	
1452			LMODE		1551	1204	4002	STC NR	
1453	1111	1000	12,	LDA	/CHECK LOW ORDER	1552	1205	1020	LDA I
1454	1112	2052	14,	CC1!2000	/HALF OF CNTR	1553	1206	0650	LLF 10
1455	1113	0451	APJ	/>2000 PTS ?	1554	1207	5232	STC A1	
1456	1114	7165	JAP RET	/YES, RETURN AND	1555	1210	1000	LDA	
1457	1115	1120	ADA I	/PROCESS	1556	1211	0361	ITPLK	
1460	1116	3740	3740		1557	1212	0017	004	
1461	1117	0451	APJ	/>40 (OCTAL) PTS	1560	1213	2362	ADD TPLK	

1561	1214	0032	PDP		1657	1432	1320	LLA I	
1562			PMODE		1660	1433	3046	LIF 6	
1563	3215	1266	TAD KN4	/CALCULATE NO	1661	1434	5414	STC P1	
1564	3216	2267	ISZ NB1	/OF LINC FIELDS	1662	1435	0011	CLR	
1565	3217	7540	SZA SMA		1663	1436	3064	SET I RGL1	/DATA POINTER
1566	3220	5215	JMP --3		1664	1437	3777	3777	
1567	3221	7399	CLA CLL		1665	1410	0365	SET I RGL2	/LISP BUFFER
1570	3222	6141	LINC		1666	1411	3777	3777	/POINTER VC
1571			LMODE		1667	1412	3066	SET I RGL3	/LISP BUFFER
1572	1223	3267	ADD NB1		1673	1413	3777	3777	/POINTER VC
1573	1224	0317	COM	/SET LINC FIELD	1671	1414	3646	LIF 6	
1574	1225	4397	STC LFCNT	/COUNT	1672	1415	1024	LIA I RGD1	/VC
1575	1226	2361	ADD I TBLK		1673	1416	1463	SAE I	
1576	1227	1120	ADA I		1674	1417	7777	7777	
1577	1230	4901	4001	/SET INITIAL	1675	1420	0456	SKP	
1600	1231	5236	STC A2	/TAPE BLOCK NO	1676	1421	7472	JAP R4	/END OF DATA
1601	1232	0653	LIF 10		1677	1422	4537	STC VC	
1602	1233	0361	SET I TTC	/SET TO XFER	1700	1423	1024	LIA I RGL1	/AC
1603	1234	7773	-4	/4 BLOCKS	1701	1424	3341	SCR I	
1604	1235	0710	0710	/RDC I	1702	1425	4536	STC AC	
1605	1236	0000	0000	/MPLKNTBLK,	1703	1426	2537	ALL VC	
1606	1237	1820	LDA I		1704	1427	1120	ALA I	
1607	1240	1001	1001	/INC MBLK & TBLK	1705	1430	7337	-470	
1610	1241	1143	ADM		1706	1431	3471	APJ I	/OLL OR EVEN FIELD
1611	1242	1236	A2		1707	1432	7444	JMP P2	
1612	1243	0221	XSK I TTC	/4 BLOCKS?	1710	1433	1120	ALA I	
1613	1244	7235	JMP A2-1	/NO	1711	1434	3273	273	
1614	1245	0227	XSK I LFCNT	/YES, ANY MORE?	1712	1435	0117	COM	
1615	1246	0456	SKP	/YES	1713	1436	4537	STC VC	
1616	1247	7264	JAP A3	/NO, RETURN	1714	1437	1320	LIA I	
1617	1250	1320	LDA I		1715	1440	4333	4333	
1620	1251	0301	I		1716	1441	1143	ADM	/SET HC FOR DISPLAY
1621	1252	1140	ADM	/INC CURRENT	1717	1442	0536	IC	/ON CHANNEL 2
1622	1253	1232	A1	/LINC FIELD	1720	1443	7450	JMP R3	
1623	1254	1300	LFA		1721	1444	1120	ALA I	
1624	1255	1236	A2		1722	1445	7577	-243	
1625	1256	1120	ADA I	/RESET MELK	1723	1446	3917	COM	
1626	1257	7776	-1		1724	1447	4537	STC VC	/DISPLAY ON CHAN 1
1627	1260	1120	ALA I		1725	1450	3644	LIF 4	
1630	1261	4000	4000		1726	1451	1000	LIA	/STORE IN DISPLAY
1631	1262	5236	STC A2		1727	1452	3537	VC	/BUFFER
1632	1263	7232	JMP A1		1733	1453	1065	SIA I RGL2	
1633	1264	0032	PDP		1731	1454	0645	LIF 5	
1634			PMODE		1732	1455	1300	LIA	
1635	3265	5600	JMP I SINI4		1733	1456	3536	IC	
1636			/		1734	1457	1066	SIA I RGL3	
1637			/		1735	1460	3245	XSK RCL2	/LISP BUFFER FULL?
1640	3266	7774	KN4,	-4	1736	1461	3456	SKP	/NO
1641			/		1737	1462	7472	JAP R4	/YES, EXIT
1642			LMODE		1740	1463	3234	XSK RGL1	/END OF L FIELDS?
1643			/		1741	1464	7414	JAP P1	/NO
1644	1267	0000	NB1,	0	1742	1465	1320	LIA I	/YES, INC LIF
1645			/		1743	1466	0001	I	
1646			/		1744	1467	1143	ALA	
1647			/		1745	1470	1414	P1	/INC L FIELD
1650			/		1746	1471	7414	JAP P1	
1651				*1430	1747	1472	0000	P1,	P1P
1652				PMODE	1750			PMOIF	
1653				/SCALE DATA AND STORE IN DISPLAY BUFFER	1751	3473	5600	JMP I SSCLA	
1654	3400	0000	SSCLA,	0000	1752			/	
1655	3421	6141	LINC		1753			/	
1656			LMODE		1754			/	

2153	4255	1124	TAD KVI		2252	0346	0000	HC1.	0
2154	4256	7443	SZA	/VC=CVC-1	2253	0347	0000	HC2.	0
2155	4257	7413	SKP	/NO	2254	0350	0000	HC3.	0
2156	4260	5264	JAP CUIA	/YES	2255	0351	0000	HC4.	0
2157	4261	2011	ISZ FVC	/INC FVC	2256	0352	0000	HC5.	0
2161	4262	7333	CLA CLL		2257	0353	0000	HC6.	0
2161	4263	5241	JAP CUI	/GET NEXT VC	2260	0354	0000	HC7.	0
2162	4264	1411	TAD I FVC	/VC=CVC,NO	2261	0355	0000	HC10.	0
2163	4265	3343	LCA HHC	/COMPARE HC	2262	0356	0000	HC11.	0
2164	4266	1343	TAL HHC		2263	0357	0000	HC12.	0
2165	4267	7041	CIA		2264	0360	0000	HC13.	0
2166	4273	1344	TAD HHC1	/HHC1-HHC	2265	0361	0000	HC14.	0
2167	4271	7510	SPA	/+VE!	2266	0362	0000	HC15.	0
2170	4272	7041	CIA	/NO	2267	0363	0000	HC16.	0
2171	4273	1336	TAD KVI5	/-15	2273	0364	0000	HC17.	0
2172	4274	7730	SMA CLA	/IIFE >I15I	2271	0365	0000	HC20.	0
2173	4275	5241	JAP CUI	/YES,GET NEXT VC	2272			/	
2174	4276	2305	ISZ HHC	/NO,COUNT HC	2273			/	
2175	4277	7300	CLA CLL		2274			/	
2175	4300	1343	TAD HHC		2275			/	
2177	4301	3346	LCA HHC1	/STORES HC FOR	2276				*400
2200	4302	2301	ISZ CUI	/EACH POINT IN	2277				PAJLE
2201	4303	1011	TAD FVC	/HC1,HC2,ETC	2300			/	
2202	4304	1124	TAD KVI	/FVC-1	2301			/	
2203	4305	3311	LCA FVC	/REPLACES HC WITH 0	2302			/	
2204	4306	3411	LCA I FVC	/TO INDICATE IT HAS	2303			/	
2205	4307	1340	TAD CVC1		2304	4403	0000	SREL.	0000
2206	4310	7301	CIA		2305	4401	6141	LINC	
2207	4311	1341	TAD CVC		2306			LXODE	
2213	4312	7443	SZA		2307	0402	0011	CLR	
2211	4313	2341	ISZ CVC	/BEEN PROCESSED	2310	0403	4521	SIC HHC	/HHC SET TO 0
2212	4314	7000	NOP		2311	0404	1000	LDA	
2213	4315	1343	TAD HHC		2312	0405	0340	HHC	
2214	4316	3344	LCA HHC1		2313	0406	0317	CUI	
2215	4317	5241	JAP CUI		2314	0407	4520	SIC HHC	
2216	4320	4534	JMS I HHC	/REDUCE DATA	2315	0410	2345	ADD HHC	
2217	4321	4535	JMS I STOR	/STORE DATA	2316	0411	1460	SAF I	/ONLY 1 PAIR OF
2220	4322	7300	CLA CLL		2317	0412	0001	I	/COORDINATES?
2221	4323	1325	TAL +-2		2320	0413	0456	SKP	/NO,REDUCE
2222	4324	7410	SKP		2321	0414	6516	JAP R3	/YES,STORE
2223	4325	3346	LCA HHC1		2322	0415	1100	ALA I	
2224	4326	3341	LCA CUI		2323	0416	7767	-10	
2225	4327	5213	JAP CUI		2324	0417	0451	APJ	/> 10 HC COORDS
2226	4328	7240	SIA		2325	0420	6406	JAP +-6	/NO
2227	4331	3337	LCA HHC		2326	0421	1000	LLA I	/YES,MAKE HHC=10
2231	4332	7240	SIA		2327	0422	0410	I0	
2231	4333	3340	LCA HHC		2330	0423	1040	SIA	
2232	4334	4535	JMS I STOR		2331	0424	0345	HHC	
2233	4335	5600	JAP I SCJMP		2332	0425	6430	JAP +-3	
2234			/		2333	0426	1120	ALA I	
2235			/		2334	0427	0110	I0	
2236			/		2335	0430	1120	ALA I	
2237	4336	7763	KVI5.	-15	2336	0431	2520	2400+RIAR-2	/FORM ALL INST
2243			/		2337	0432	4430	SIC +-1	/TO GET APPROPRIATE
2241				LXODE	2340	0433	0316	NOP	/RECIPROCAL
2242	0337	0000	RVC.	0	2341	0434	4465	SIC RLIV	
2243	0340	0000	HHC.	0	2342	0435	2345	ADD HHC	
2244	0341	0000	CVC.	0	2343	0436	0317	CUI	
2245	0342	0000	CVC1.	0	2344	0437	4011	SIC CTR	/SAF HC COUNTER
2246	0343	0000	HHC.	0	2345	0440	0201	AVE.	
2247	0344	0000	HHC1.	0	2346	0441	0456	SKP	
2250	0345	4000	HHC.	0	2347	0442	6455	JAP R2	/DIFF SUMMED
2251			/		2350	0443	1000	LIA	/CALCULATE SUM

2351	0444	0346	R1,	HCI	/OF DIFFERENCES
2352	0445	2520		ADL HMC	
2353	0446	1143		ALM	
2354	0447	0521		AHC	
2355	0450	1020		LIA I	
2356	0451	0301		I	
2357	0452	1143		ADM	
2360	0453	0444		KI	/INC RI
2361	0454	6440		JAP AVE.	
2362	0455	1000	R2,	LIA	
2363	0456	0521		AHC	
2364	0457	0241		RUC I	/K10 (BINARY)
2365	0460	1563		BCL I	
2366	0461	0301		I	
2367	0462	1240		MUL	/FRACTIONALY
2370	0463	4465		..+4002	/MULTIPLY
2371	0464	0456		SKP	
2372	0465	0400	RDIV,	0000	/MULTIPLIER
2373	0466	0321		RJR I 1	/DIV BY 10
2374	0467	0451		APJ	/-VE JR +VE?
2375	0470	6476		JAP ..+6	/-VE, ROUND DOWN
2376	0471	0472		LZE I	/+VE, ROUND UP?
2377	0472	6502		JAP ..+10	
2400	0473	1120		ADA I	
2401	0474	0301		I	
2402	0475	6502		JAP ..+5	
2403	0476	0452		LZE	/ROUND DOWN?
2404	0477	6502		JAP ..+3	/NO
2405	0500	1120		ALA I	
2406	0501	7776		-1	
2407	0502	1100		ALA	
2410	0503	0340		HAC	
2411	0504	4340		STC HMC	/AVERAGED HC
2412	0505	2337		ADL HVC	
2413	0506	0317		CJA	
2414	0507	2341		ADL CVC	/CVC-HVC
2415	0510	1240		MIL	/DIV BY 2
2416	0511	4513		..+4002	
2417	0512	0456		SKP	
2420	0513	2300		0000	
2421	0514	2307		ADL HVC	
2422	0515	4337		STC HVC	
2423	0516	0302	R3,	PDP	
2424				PAJDE	
2425	4517	5600		JAP I SREF	
2426			/		
2427			/		
2430				LADJE	
2431	0520	0000	HMC,	0	
2432	0521	0300	HMC,	0	
2433			/		
2434	0522	2000	RTAB,	2000	/RECIPROCAL OF 2
2435	0523	1252		1252	/3
2436	0524	1000		1000	/4
2437	0525	0631		0631	/5
2440	0526	0526		0526	/6
2441	0527	0444		0444	/7
2442	0530	0400		0400	/10
2443			/		
2444			/		
2445			/		
2446			/		
2447			/		

2450				PMOLE	
2451	4531	0000	SSTJR,	0000	
2452	4532	6141		LINC	
2453				LADJE	
2454	0533	0646		LIF 6	
2455	0534	1000		LIA	
2456	0535	0307		RVC	
2457	0536	1070		STA I SGD	
2460	0537	1000		LIA	
2461	0540	0340		HMC	
2462	0541	1070		STA I SGD	
2463	0542	1020		LIA I	
2464	0543	0301		I	
2465	0544	4345		STC HMC	
2466	0545	1020		LIA I	
2467	0546	0346		HCI	
2470	0547	4444		STC RI	
2471	0550	0002		PDP	
2472				PAJDE	
2473	4551	5731		JMP I SSTJR	
2474			/		
2475			/		
2476				LADJE	
2477				*600	
2500				PAJDE	
2501			/		
2502			/		
2503			/		
2504	4600	0000		SMTA,	0000
2505	4601	7240		STA	
2506	4602	3010		LCA GD	
2507	4603	6221		CLF 20	
2510	4604	3131		LCA TEMP	
2511	4605	1136		IAD K377	
2512	4606	3410		LCA I GD	
2513	4607	2131		ISZ TEMP	
2514	4610	5205		JXP --3	
2515	4611	7240		STA	
2516	4612	3131		LCA TEMP	
2517	4613	7240		STA	/SET UP MIX FOR
2520	4614	3010		LCA MIXV	/VC
2521	4615	1141		IAD K37	/SET UP MIX FOR
2522	4616	3011		LCA MIXH	/HC
2523	4617	6141		LINC	
2524				LADJE	
2525	0620	0070		SFT I SGD	
2526	0621	3777		3777	
2527	0622	0646	MTI,	LIF 6	
2530	0623	1000		LIA I SGD	
2531	0624	1400		SAF I	/END OF DATA?
2532	0625	7777		7777	
2533	0626	0456		SKP	/NO
2534	0627	6751		JAP MTI	/YES, MIX BUILT
2535	0630	4761		SIC VPI	
2536	0631	1000		LIA I SGD	
2537	0632	1460		SAF I	/ALREADY PROCESSED?
2540	0633	0300		0	
2541	0634	0456		SKP	/NO
2542	0635	6622		JAP MTI	/YES
2543	0636	4763		STC HPI	
2544	0637	1000		LIA	
2545	0640	0310		SGD	
2546	0641	1040		STA	

2547	0642	0765	AFIR		2646	0741	1300	LIA	/YES
2550	0643	0312	SIC FCO		2647	0742	0764	HF2	
2551	0644	1032	LIA I FCO	MT2	2650	0743	4763	SIC HF1	
2552	0645	1463	SAF I		2651	0744	2762	ADD VF2	
2553	0646	7777	7777		2652	0745	4761	SIC VF1	
2554	0647	0000	SKP	/NO	2653	0746	2312	ADD FCO	
2555	0650	6674	JAP MT3	/YES	2654	0747	4765	SIC ADDR	
2556	0651	1000	SIA		2655	0750	6705	JAP MT3+11	
2557	0652	0762	VF2		2656	0751	0002	PDP	MT4
2560	0653	3117	CJM		2657			PAJIE	
2561	0654	2761	ADD VF1		2660	4752	5600	JAP I SMIX	
2562	0655	1120	ALA I		2661			LADIE	
2563	0656	0310	IS		2662	0753	1000	LDA	MT5
2564	0657	3411	APJ I	/<VF1+10?	2663	0754	0761	VF1	
2565	0660	6663	JMP ++3	/YES	2664	0755	5057	SIC VE	
2566	0661	0232	XSK I FCO	/NO	2665	0756	2763	ADD HF1	
2567	0662	6604	JAP MT2		2666	0757	5060	SIC HF	
2570	0663	1032	LIA I FCO		2667	0760	7300	JAP MT6	
2571	0664	1463	SAF I	/ALREADY PROCESSED	2670			/	
2572	0665	0000	0		2671			/	
2573	0666	0456	SKP	/NO	2672			/	
2574	0667	6604	JAP MT2	/YES	2673	0761	0000	VF1	0
2575	0670	4763	SIC HF1		2674	0762	0000	VF2	0
2576	0671	0762	ADD VF2		2675	0763	0000	HF1	0
2577	0672	4761	SIC VF1		2676	0764	0000	HF2	0
2578	0673	6604	JAP MT2	/NO	2677	0765	0000	ADDR	0
2601	0674	1000	LDA	MT3	2700			/	
2602	0675	0000	SGD		2701			*1000	
2603	0676	1120	ALA I		2702	1000	0002	MT6	
2604	0677	7775	-2		2703			PDP	
2605	0700	1600	PSF I		2704	5001	7000	PAJIE	
2606	0701	2000	0000		2705	5002	2131	CLA CLL	
2607	0702	4704	SIC ++2		2706	5003	7410	ISZ TFMP	/FIRST COORD?
2610	0703	0072	SEI I FCO		2707	5004	5213	SAP	
2611	0704	0000	0000		2710	5005	1257	JAP MT7	/YES
2612	0705	1032	LIA I FCO		2711	5006	7041	TAD VE	
2613	0706	1463	SAF I		2712	5007	1261	CIA	
2614	0707	7777	7777		2713	5010	1262	TAD LVE	
2615	0710	0456	SKP		2714	5011	7510	TAD K6	
2616	0711	6750	JAP MT5	/END OF DATA	2715	5012	5237	SPA	/START NEW ROW?
2617	0712	1000	SIA		2716	5013	7000	JAP MT10	/YES
2620	0713	0762	VF2		2717	5014	6201	CLA CLL	
2621	0714	0017	CJM		2718	5015	1257	CIF 20	
2622	0715	2761	ADD VF1		2720	5016	1257	TAD VE	
2623	0716	1120	ALA I		2721	5016	3413	DCA I MIXU	
2624	0717	0000	IS		2722	5017	1260	TAD HF	
2625	0720	0471	APJ I	/<VF1+10?	2723	5020	3411	DCA I STAR	
2626	0721	6704	JMP ++3	/YES	2724	5021	1257	TAD VE	
2627	0722	0232	XSK I FCO	/NO	2725	5022	3261	LCA LVE	
2630	0723	6705	JAP MT3+11		2726	5023	6141	LINC	
2631	0724	1032	LIA I FCO		2727			LADIE	
2632	0725	1463	SAF I	/ALREADY PROCESSED?	2730	1024	0646	LLF 6	
2633	0726	0000	0		2731	1025	1000	LIA	
2634	0727	0456	SKP	/NO	2732	1026	4765	ADDR	
2635	0730	6705	JAP MT3+11	/YES	2733	1027	1120	ADA I	
2636	0731	1000	SIA		2734	1030	7775	-2	
2637	0732	4764	HF2		2735	1031	1600	PSF I	
2640	0733	0017	CJM		2736	1032	6000	6000	
2641	0734	2763	ADD HF1		2737	1033	0312	SIC FCO	
2642	0735	1120	ADA I		2740	1034	1072	SIA I FCO	
2643	0736	0015	IS		2741	1035	1072	SIA I FCO	
2644	0737	0451	APJ	/<HF1+15?	2742	1036	6620	JAP MT1-2	
2645	0740	6705	JAP MT3+11	/NO	2744	5037	7000	PAJIE	
								CLA CLL	MT10

2745	5043	1313		TAD MTXV	3044	1116	0002		PDP
2746	5041	6141		LINC	3045				PMODE
2747				LMODE	3046	5117	6221		CLF 20
2750	1042	1560		RCL I	3047	5120	7200		CLA
2751	1043	0077		0077	3050	5121	1411		TAL I MIXH
2752	1044	0002		PLP	3051	5122	6141		LINC
2753				PMODE	3052				LMODE
2754	5045	1142		TAL K77	3053	1123	0646	CH3.	LDF 6
2755	5046	3010		LCA MIXV	3054	1124	1460		SAE I
2756	5047	1011		TAD MTXH	3055	1125	3777		3777
2757	5050	6141		LINC	3056	1126	0456		SKP
2760				LMODE	3057	1127	7142		JMP CH4
2761	1051	1560		RCL I	3060	1130	1070		STA I SGD
2762	1052	0077		0077	3061	1131	0210		XSK SGD
2763	1053	0002		PDP	3062	1132	7140		JMP .+6
2764				PMODE	3063	1133	1020		LIA I
2765	5054	1143		TAL K137	3064	1134	0001		I
2766	5055	3011		LCA MIXH	3065	1135	1143		AIM
2767	5056	5213		JMP M17	3066	1136	1110		CH2
2770			/		3067	1137	5123		STC CH3
2771			/		3070	1140	0002		PDP
2772			/	LMODE	3071				PMODE
2773			/		3072	5141	5304		JMP CH1
2774	1357	0000	VE.	0	3073				LMODE
2775	1360	0000	HE.	0	3074	1142	0002	CH4.	PDP
2776	1361	0000	LVE.	0	3075				PMODE
2777	1362	0006	K6.	6	3076	5143	7200		CLA
3000			/		3077	5144	1010		TAL MTXV
3001			/		3078	5145	6141		LINC
3002			/		3101				LMODE
3003			/		3102	1146	1560		RCL I
3004			/		3103	1147	0077		0077
3005			/		3104	1150	0002		PEP
3006	5063	0000	SCHICK.	0000	3105				PMODE
3007	5064	7300		CLA CLL	3106	5151	1142		TAL K77
3010	5065	1141		TAL K37	3107	5152	3010		LCA MIXV
3011	5066	3011		LCA MIXH	3110	5153	1011		TAD MTXH
3012	5067	7200		STA	3111	5154	6141		LINC
3013	5070	3010		LCA MIXV	3112				LMODE
3014	5071	1373		TAL K027	3113	1155	1560		RCL I
3015	5072	3131		LCA 1E4P	3114	1156	0077		0077
3016	5073	6141		LINC	3115	1157	0002		PLP
3017				LMODE	3116				PMODE
3020	1074	0070		SPT I SGD	3117	5160	1143		TAL K137
3021	1075	3777		3777	3120	5161	3011		LCA MIXH
3022	1076	1020		LIA I	3121	5162	2131		ISZ TEMP
3023	1077	0646		LDF 6	3122	5163	5304		JMP CH1
3024	1100	1000		STA	3123	5164	6141		LINC
3025	1101	1110		CH2	3124				LMODE
3026	1102	5123		STC CH3	3125	1165	1020		LIA I
3027	1103	0002		PEP	3126	1166	7777		7777
3030				PMODE	3127	1167	1070		STA I SGD
3031	5104	7200	CH1.	CLA	3130	1170	1070		STA I SGD
3032	5105	6221		CLF 20	3131	1171	0002		PDP
3033	5106	1410		TAD I MIXV	3132				PMODE
3034	5107	6141		LINC	3133	5172	5663		JMP I SCHICK
3035				LMODE	3134				/
3036	1110	0646	CH2.	LDF 6	3135				/
3037	1111	1460		SAE I	3136				/
3040	1112	3777		3777	3137	5173	7751	K027.	-27
3041	1113	0456		SKP	3140				/
3042	1114	0456		SKP	3141				/
3043	1115	1070		STA I SGD	3142				LMODE

3143			*1200	
3144			PMODE	
3145			/SUFRJUTINP TO STORE CALIBRATION MATRIX /ON TAPE.	
3146			/	
3147	5233	0000	STAPE, 0000	
3150	5201	6141		
3151			LINC	
3152	1232	1020	LMODE	
3153	1233	0650	LDA I	
3154	1204	5216	LLF 10	
3155	1205	0071	STC TA1	
3156	1206	7771	SFI I CTR	
3157	1207	0072	-6	
3160	1210	7773	SFI I FCO	
3161	1211	1030	-4	
3162	1212	1270	LLA	
3163	1213	1120	CTBLK	
3164	1214	4001	ADA I	
3165	1215	5220	4001	
3166	1216	0650	STC TA2	
3167	1217	0714	LLF 10	
3170	1220	0000	0714	/WRC 1
3171	1221	1020	0000	/MPLK\IBLK.
3172	1222	1001	LLA I	
3173	1223	1140	1001	
3174	1224	1220	AL4	
3175	1225	0232	TA2	
3176	1226	7244	KSK I FCO	
3177	1227	1020	JAP TA3	
3200	1230	0001	LDA I	
3201	1231	1140	I	
3202	1232	1216	ADM	
3203	1233	0072	TA1	
3204	1234	7773	SFI I FCO	
3205	1235	1000	-4	
3206	1236	1220	LDA	
3207	1237	1120	TA2	
3210	1240	7776	ADA I	
3211	1241	1120	-1	
3212	1242	4000	ADA I	
3213	1243	5220	4000	
3214	1244	0231	STC TA2	
3215	1245	7216	KSK I CTR	
3216	1246	1020	JAP TA1	
3217	1247	0650	LLA I	
3220	1250	5216	LLF 10	
3221	1251	3220	STC TA1	
3222	1252	1120	ADL TA2	
3223	1253	7776	ADA I	
3224	1254	1560	-1	
3225	1255	7000	FCL I	
3226	1256	1040	7000	
3227	1257	1270	STA	
3230	1260	1120	CTPLK	
3231	1261	7707	ADA I	
3232	1262	3451	-70	
3233	1263	3456	APU	
3234	1264	0000	SKP	
3235	1265	0011	HLT	
3236	1266	0002	CLR	
3237			PDP	
3240	5267	5600	PMODE	
3241			JAP I STAPE	

3242	/			
3243				LMODE
3244	1270	0017	CTBLK,	17
3245	/			
3246	/			
3247				*10
3250	0010	3777	SGL,	3777
3251	0011	0000	CTR,	0
3252	0012	0000	FCU,	0
3253	/			
3254	/			
3255	/			

NO ERRORS

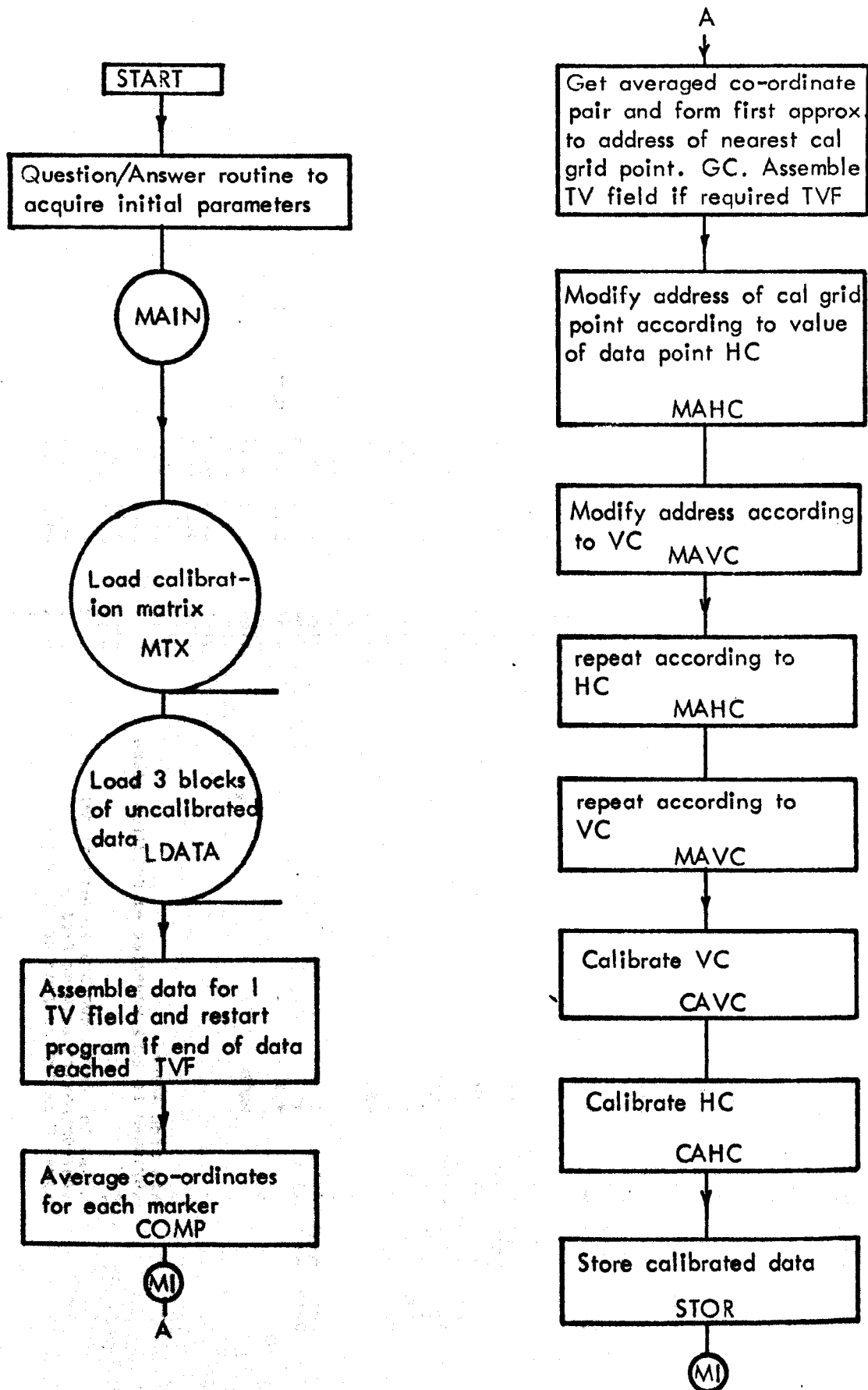
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ADDR	4765
AHC	4521
AVE	4440
A1	3232
A2	3236
A3	3264
REGIN	0120
REGIN1	0232
REGIN2	0266
P1	3414
P2	3444
P3	3450
P4	3472
CA	1104
CAD	0102
CAGU	6303
CAGUFR	6305
CAL	0121
CAM	0122
CAMCO	2541
CAMCO1	0111
CC	0117
CCAFU	6306
CC1	3052
CC2	0053
CLRF	6347
CLRI	6344
CLRM	6342
CFA	0103
CFC	6315
CHC	4343
CHC1	4344
CHECK	0140
CH1	5104
CH2	5110
CH3	5123
CH4	5142
CLAD	6334
CLCAL	6314
CTHC	2751
COIF	0110
CO4P	0133
COENT	2005
COJ	4210
CO1	4241
CO1A	4264
CO2	4301

CJ3 4323
CJ4 4330
CS4 6316
CTBLK 5270
CTP 4311
CVC 4341
CVC1 4342
CVTEST 2633
C1 3536
C2 3514
C3 3527
DF4 6341
DFD 2635
DFL1 2357
DISCG 6322
DISP 3123
DR 3337
EFLAG1 2772
EVAL 6331
EVC 6321
EVL 2656
EVLPI 6343
ENL1 2677
ENFAD 6332
ENFC 6313
ENFON 6301
ENS4 6314
FCVT 2645
FCJ 4312
FCOUNT 2346
FVC 3311
GL 3313
HC 2536
HCERR 2773
HCJT 2773
HCP1 2472
HC1 4346
HC13 4355
HC11 4356
HC12 4357
HC13 4363
HC14 4361
HC15 4362
HC16 4363
HC17 4364
HC2 4347
HC23 4365
HC3 4350
HC4 4351
HC5 4352
HC6 4353
HC7 4354
HF 5363
HE1 4763
HE2 4764
IALLR 2623
INIT1 3132
INIT2 3135
INIT3 3116
INIT4 3133
IPLK 2361
I1 3777
I2 3134

I3 3106
I4 3112
I5 3123
I6 3167
K41 2125
KV1 0124
KV15 4336
KV27 5173
KV4 3266
K1 0113
K137 0143
K2952 0112
K37 0141
K3777 0136
K6 5062
K77 0142
LDCA 6333
LICAM 6302
LIFC 6311
LILC 6312
LFCNT 2237
LHC 2501
LVE 5061
MAIN1 4231
MHC 4523
MTAPE 3003
M1X 0137
M1K4 0211
M1K5 0313
M11 4622
MT13 5337
MT2 4644
MT3 4674
MT4 4751
M15 4753
M16 5333
M17 5313
M1 2417
M2 2424
M3 3322
VPI 3267
VHC 4345
VLF 2355
VMS 3127
PASS2 2663
PASS3 0335
PHC 0107
RCA 6335
RCHA 6336
PLIV 4465
RFD 4134
RF4 2356
RFT 3165
RFTURN 3266
RGI 0243
RGL1 3004
RGL2 0095
RGL3 3006
RHC 4343
RTAP 4522
RVC 4337
R1 4444
R2 4455

R3 4516
SCAC 6337
SCAL 0230
SCPH 6345
SCLA 0126
SCHECK 5263
SCJLE 2512
SCJ4P 4230
SHSP 3474
SFC1 3130
SGP 4210
SINIT1 2233
SINIT2 2100
SINIT3 3267
SINIT4 3235
SLCT 0131
S41X 4633
SPHC 2434
SR 2540
SPFI 4400
SSCLA 3400
SSTOP 4531
SSTORE 2542
STAD 2031
STAPF 5233
START 3115
START1 2430
STOR 3135
STOPF 0114
SUF 6346
TAPF 0144
TAL 5216
TAR 5223
TAS 5224
TPLK 2360
TFL 2343
TCNT 2432
TMAP 3131
TMS10 2762
TIC 4301
TVC 2774
TVCNT 2334
T2 3111
VC 2537
VCHRC 2771
VCTEST 0136
VF 5357
VF1 4761
VF2 4762
XFER1 3341
XFER2 3365

Flow Chart for calibration program (CAL)



0379			*29	0376			LMODE
0381			/CAL - CALIBRATES DATA.	0377			SEGMENT 0
0382			/	0100			*400
0383			/16-6-75	0101	0430	0641	MAIN, LDF 1
0384			/	0102	0401	0011	CLR
0385			/	0103	0402	1043	STA
0386			/M.O.JARRETT.	0104	0403	2531	FLAG!2000
0387			/	0105	0404	0902	PDP
0388			/	0106			PMODE
0389			/	0107	0405	4475	JMS I MTK /LOAD CAL MTK
0390			/TAPE WITH CALIBRATION MATRIX AND	0110	0406	4473	JMS I LDATA /LOAD 1K OF DATA
0391			/UNACALIBRATED DATA ON UNIT 3.	0111	0407	4467	JMS I TVF /ASSEMBLE TV-FIELD
0392			/TAPE TO ACCEPT CALIBRATED DATA ON UNIT 2.	0112	0410	4466	JMS I COMP /REDUCE DATA
0393			/	0113	0411	4476	M1, JMS I GC /GET CORR PAIR
0394			/	0114	0412	4504	JMS I MAHC /MODIFY CAL ADDR
0395			/PROGRAM IS SELF STARTING AT LOCATION 06020	0115	0413	4503	JMS I MAVC /NOE FOR VC
0396			/IN LINC MODE.	0116	0414	4504	JMS I MAHC /REPEAT
0397			/	0117	0415	4503	JMS I MAVC /REPEAT FOR VC
0398			/	0120	0416	4477	JMS I CAVC /CALIBRATE DATA VC
0399			/	0121	0417	4500	JMS I CAHC /CALIBRATE DATA HC
0400			/	0122	0420	4501	JMS I STOR /STORE CALIBRATED DATA
0401			/	0123	0421	5211	JMP M1
0402			/	0124			LMODE
0403			PMODE	0125	0422	0641	END, LDF 1
0404			*10	0126	0423	1040	STA /SET FN,VC,HC TO 7777
0405	0310	0200	PTR, 0	0127	0424	2507	FN!2000
0406	0311	0200	FD, 0	0130	0425	0642	LDF 2
0407			/	0131	0426	1040	STA
0408			/	0132	0427	2656	CALVC!2000
0409	0050	0031	K1, 1	0133	0433	1040	STA
0410	0051	7777	KV1, -1	0134	0431	2657	CALHC!2000
0411	0052	0024	K24, 24	0135	0432	1000	LDA
0412	0053	7754	KV24, -24	0136	0433	3114	CDTELK!2000
0413	0054	0030	K30, 30	0137	0434	1120	ADA I
0414	0055	7753	KV30, -30	0140	0435	0003	3
0415	0056	0040	K40, 40	0141	0436	4446	STC M3
0416	0057	0100	K100, 100	0142	0437	0002	M2, PLP
0417	0060	7700	KV100, -100	0143			PMODE
0418			/	0144	0440	4501	JMS I STOR
0419			/	0145	0441	6141	LINC
0420	0061	0200	VC, 0	0146			LMODE
0421	0062	0000	HC, 0	0147	0442	0642	LDF 2
0422	0063	0000	ADDR, 0	0150	0443	1000	LLA
0423	0064	3141	SRD, SSRD	0151	0444	3114	CDTELK!2000
0424	0065	3020	FFL, SRFD	0152	0445	1460	SAE I
0425	0066	2600	COMP, SCOMP	0153	0446	0000	M3, 2000
0426	0067	2400	TVF, STVF	0154	0447	6437	JMP M2
0427	0070	2051	LPATA, SLDATA	0155	0450	0603	LIF 3
0428	0071	4000	VCC, SVCC	0156	0451	6020	JMP 20
0429	0072	4304	HCC, SHCC	0157			/
0430	0073	4400	DIV, SDIV	0160			/
0431	0074	0000	DIF, 0	0161			/
0432	0075	2000	MTX, SMTX	0162			LMODE
0433	0076	2310	CC, SEC	0163			SEGMENT 1
0434	0077	4600	CAVC, SCAVC	0164			/
0435	0100	4633	CAHC, SCAHC	0165			/SUBROUTINE TO LOAD CALIBRATION MATRIX.
0436	0101	5000	STOR, SSTOR	0166			*200
0437	0102	5024	TAPE, STAPE	0167			/
0438	0103	3433	MAVC, SMAVC	0170			PMODE
0439	0104	3400	MAHC, SMAHC	0171	2200	0000	SMTX, 0000
0440			/	0172	2201	6141	LINC
0441			/	0173			LMODE
0442			/	0174	0200	1020	LIA I

0175	0233	0331	1			0274	0267	0330	LDAT1,	0330	/MBLK\TFLK
0176	0234	0331	AXJ			0275	0270	1020	LDA I		
0177	0235	0362	SET I CTR			0276	0271	1001	1001		
0233	0236	7771	-6			0277	0272	1140	ADA		
0231	0237	3363	SET I FCTR			0333	0273	0267	LDAT1		
0232	0213	7773	-4			0301	0274	0223	ASK I FCTR	/DATA LOADED?	
0233	0211	1033	LDA			0332	0275	6266	JMP LDAT1-1	/NO	
0234	0212	0253	CTBLK	/START BLOCK FOR		0333	0276	1003	LDA		
0235	0213	1123	ADA I	/CAL MTX		0304	0277	0267	LDAT1		
0236	0214	4000	4000			0305	0300	1120	ADA I		
0237	0215	4223	STC MTX2			0336	0301	7776	-1		
0212	0216	1020	LDA I			0307	0302	4307	STC DTBLK	/SAVE START BN	
0211	0217	3653	LDF 10			0310	0303	0062	SET I CTR		
0212	0220	4221	STC MTX1			0311	0304	2377	2377		
0213	0221	3653	LDF 13			0312	0305	0302	PDP		
0214	0222	0710	0710	/RDC 1		0313			PMODE		
0215	0223	0710	0200	/MBLK\TBLK		0314	2306	5651	JMP I SLDATA		
0216	0224	1000	LDA I			0315			/		
0217	0225	1001	1001	/INC MBLK\TBLK		0316			/		
0220	0226	1140	ALM			0317			/		
0221	0227	0223	MTX2			0320	0307	0200	DTBLK,	200	
0222	0230	0223	KSK I FCTR	/END OF L-FIELD?		0321			/		
0223	0231	6244	JAP MTX3	/NO		0322			/		
0224	0232	1323	LDA I	/YES, INC L-DATA		0323			/SUBROUTINE TO GET COORDINATE PAIR AND		
0225	0233	0331	1	/FIELD		0324			/CALCULATE CALIBRATION ADDRESS.		
0226	0234	1140	ALM			0325			PMODE		
0227	0235	0221	MTX1			0326	2310	0000	SGC,	0000	
0230	0236	0063	SET I FCTR			0327	2311	6141	LINC		
0231	0237	7773	-4			0330			LMJDE		
0232	0240	1023	LDA I			0331	3312	0651	LDF 11		
0233	0241	3777	3777			0332	0313	1024	LDA I RD	/VC	
0234	0242	1140	ADA			0333	0314	1460	SAE I	/END OF CURRENT	
0235	0243	0223	MTX2			0334	0315	7777	7777	/TV FIELD?	
0236	0244	0222	KSK I CTR	/END OF CAL MTX?		0335	0316	6321	JMP +3	/NO	
0237	0245	6221	JMP MTX1	/NO		0336	0317	6512	JMP SNFLD	/YES LOAD NEXT FIELD	
0240	0246	0002	PDP	/YES, EXIT		0337	0320	6312	JMP -6		
0241			PMODE			0340	0321	0640	LDF 0		
0242	2247	5600	JAP I SMIX			0341	0322	1040	STA		
0243			/			0342	0323	2061	VC!2000		
0244			LMJDE			0343	0324	0345	SCR 5	/LEFT HALF OF ADDR	
0245			/			0344	0325	1340	STH		
0246	0250	0020	CTBLK,	20		0345	0326	2063	ADDR!2000		
0247			/			0346	0327	0651	LDF 11		
0250			/			0347	3330	1024	LDA I RD		
0251			/			0350	0331	0640	LDF 0		
0252			/SUBROUTINE TO LOAD 3 BLOCKS OF DATA			0351	0332	1040	STA		
0253			/			0352	0333	2062	HC!2000		
0254			PMODE			0353	0334	0345	SCR 5	/RIGHT HALF OF ADDR	
0255	2251	0233	SLDATA,	3000		0354	0335	1340	STH		
0256	2252	6141	LINC			0355	0336	6063	ADDR!6000		
0257			LMODE			0356	0337	0011	CLR		
0260	0253	0063	SET I FCTR			0357	0340	0302	PDP		
0261	0254	7774	-3			0360			PMODE		
0262	0255	1033	LDA	/CURRENT 1K START		0361	2341	5710	JMP I SGC		
0263	0256	0307	DTBLK	/BLOCK NO.		0362			/		
0264	0257	1120	ADA I			0363			/		
0265	0260	5000	5000			0364			/		
0266	0261	4267	STC LDAT1			0365			LMODE		
0267	0262	1023	LDA I			0366			/SUBROUTINE TO GET 1 TV FIELD OF DATA		
0273	0263	0031	1			0367			/		
0271	0264	0331	AXJ			0370			*400		
0272	0265	0653	LDF 13			0371			PMODE		
0273	0266	0710	0710	/RDC 1		0372	2400	0000	STVF,	0000	

0373	2401	6141	LINC		0472				LMODE
0374			LMODE		0473	0473	0002	TVF2.	PDP
0375	0402	0653	LDF 13		0474				PMODE
0376	0403	1022	LDA I CTR	/FN	0475	2471	6221		CDF 20
0377	0404	1460	SAF I	/END OF DATA?	0476	2472	7243		STA
0400	0405	7777	7777		0477	2473	3410		LCA I PTR
0401	0406	6411	JMP ++3		0500	2474	7243		STA
0402	0407	4531	STC FLAG		0501	2475	3410		LCA I PTR
0403	0413	6470	JMP TVF2	/RETURN & PROCESS	0502	2476	6141		LINC
0404	0411	4537	STC FN	/SAVE TV FIELD NO.	0503				LMODE
0405	0412	1022	LDA I CTR		0504	0477	0064		SET I RD
0406	0413	4510	STC TVC		0505	0507	2777		2777
0407	0414	1022	LDA I CTR		0506	0501	1022		LDA I
0410	0415	4511	STC THC		0507	0502	7776		-1
0411	0416	0202	XSK CTR	/END OF DATA?	0510	0503	1140		AD4
0412	0417	6423	JMP ++4	/NO	0511	0504	0002		CTR
0413	0420	0002	PLP		0512	0535	0002		PDP
0414			PAJDE		0513				PMODE
0415	2421	4470	JMS I LDATA	/YES, LOAD MORE DATA	0514	2506	5600		JMP I STVF
0416	2422	7410	SKP		0515				
0417			LMODE		0516				LMODE
0420	0423	0002	PDP		0517				/
0421			PMODE		0520	0537	0000	FN.	0
0422	2424	6221	CDF 20		0521	0510	0000	TVC.	0
0423	2425	1227	TAD ++2		0522	0511	0000	THC.	0
0424	2426	7413	SKP		0523				/
0425	2427	2777	2777		0524				/SUBROUTINE TO ASSEMBLE NEW TV FIELD.
0426	2430	3310	LCA PTR	/SET PTR TO STORE	0525	0512	1000	SNFLD.	LDA
0427	2431	1310	TAD TVC	/CURRENT TV FIELD	0526	0513	0000		0
0430	2432	3413	LCA I PTR		0527	0514	4526		STC NF1
0431	2433	1311	TAD THC		0530	0515	2531		ADD FLAG
0432	2434	3410	LCA I PTR		0531	0516	1460		SAF I
0433	2435	6141	LINC		0532	0517	7777		7777
0434			LMODE		0533	0520	0456		SKP
0435	0436	0653	LDF 13		0534	0521	6527		JMP NF1+1
0436	0437	1322	LDA I CTR		0535	0522	0002		PLP
0437	0440	1460	SAF I	/END OF DATA?	0536				PMODE
0440	0441	7777	7777		0537	2523	4467		JMS I TVF
0441	0442	6445	JMP ++3	/NO	0540	2524	4466		JMS I COMP
0442	0443	4531	STC FLAG	/YES	0541	2525	6141		LINC
0443	0444	6470	JMP TVF2		0542				LMODE
0444	0445	1440	SAF.	/CURRENT TV FIELD?	0543	0526	0000	NF1.	0000
0445	0446	0507	FI		0544	0527	0600		LIF 0
0446	0447	6470	JMP TVF2	/NO, RETURN	0545	0532	6422		JMP END
0447	0450	1322	LDA I CTR		0546				/
0450	0451	4510	STC TVC		0547				/
0451	0452	1322	LDA I CTR		0550	0531	0000	FLAG.	0
0452	0453	4511	STC THC		0551				/
0453	0454	0002	PDP		0552				/SUBROUTINE TO FIND COORDINATES BELONGING
0454			PMODE		0553				/TO THE SAME MARKER.
0455	2455	6221	CDF 20		0554				*610
0456	2456	1310	TAD TVC		0555				PMODE
0457	2457	3410	LCA I PTR		0556	2600	0000	SCOMP.	0000
0460	2460	1311	TAD THC		0557	2601	7300		CLA CLL
0461	2461	3410	LCA I PTR		0560	2602	1204		TAD ++2
0462	2462	6141	LINC		0561	2603	7410		SKP
0463			LMODE		0562	2604	3353		LCA H01
0464	0463	0202	XSK CTR	/END OF DATA FIELD?	0563	2605	3302		LCA C03
0465	0464	6406	JMP TVF1	/NO	0564	2606	1210		TAD ++2
0466	0465	0002	PDP		0565	2607	7410		SKP
0467			PAJDE		0566	2610	2777		2777
0470	2466	4470	JMS I LDATA	/YES, LOAD MORE DATA	0567	2611	3010		LCA PTR
0471	2467	5235	JMP TVF1-1		0570	2612	6141		LINC

0571			LMODE	0670	2710	1347	TAD CVC1	
0572	0613	0364	SET I RD	0671	2711	7941	CIA	
0573	0614	3377	3377	0672	2712	1346	TAD CVC	
0574	0615	0302	PDP	0673	2713	7443	SZA	
0575			PMODE	0674	2714	7413	SKP	
0576	2616	6221	CO1, CDF 20	0675	2715	2346	ISZ CVC	
0577	2617	1410	TAD I PTR	0676	2716	1350	TAD CHC	
0600	2620	7343	CIA	0677	2717	3351	DCA CHC1	/REPLACE COMP HC
0601	2621	7440	SZA	0700	2720	5242	JMP CO2	
0602	2622	7410	SKP	0701	2721	4465	JMS I RED	
0603	2623	5331	JMP CO5	0702	2722	4464	JMS I SRD	
0604	2624	7343	CIA	0703	2723	7303	CLA CLL	
0605	2625	3344	DCA RVC	0704	2724	1326	TAD ++2	
0606	2626	1410	TAD I PTR	0705	2725	7410	SKP	
0607	2627	7443	SZA	0706	2726	3353	DCA HC1	
0610	2630	7410	SKP	0707	2727	3302	DCA CO3	
0611	2631	5216	JMP CO1	0710	2730	5216	JMP CO9	
0612	2632	3345	DCA RHC	0711	2731	7240	STA	
0613	2633	1345	TAD RHC	0712	2732	3344	DCA RVC	
0614	2634	3351	DCA CHC1	0713	2733	7240	STA	
0615	2635	1374	TAD RVC	0714	2734	3345	DCA RHC	
0616	2636	1350	TAD K1	0715	2735	4464	JMS I SRD	/PUTS 7777 AT END
0617	2637	3346	DCA CVC	0716	2736	6141	LINC	/OF FIELD
0620	2640	1313	TAD PTR	0717			LMODE	
0621	2641	3311	DCA FD	0720	0737	0064	SET I RD	
0622	2642	1411	CO2, TAD I FD	0721	0740	3377	3377	
0623	2643	3347	DCA CVC1	0722	0741	0302	PLP	
0624	2644	1347	TAD CVC1	0723			PMODE	
0625	2645	7343	CIA	0724	2742	5600	JMP I SCOMP	
0626	2646	7443	SZA	0725			/	
0627	2647	7410	SKP	0726			/	
0630	2650	5321	JMP CO4	0727	2743	7743	KN35, -35	
0631	2651	7001	IAC	0730			/	
0632	2652	1346	TAD CVC	0731			LMODE	
0633	2653	7440	SZA	0732	0744	0000	RVC, 0	
0634	2654	7410	SKP	0733	0745	0000	RHC, 0	
0635	2655	5265	JMP CO2A	0734	0746	0000	CVC, 0	
0636	2656	1351	TAD KN1	0735	0747	0000	CVC1, 0	
0637	2657	7443	SZA	0736	0750	0000	CHC, 0	
0640	2660	7413	SKP	0737	0751	0000	CHC1, 0	
0641	2661	5065	JMP CO2A	0740	0752	0031	CHC, 1	
0642	2662	2011	ISZ FD	0741			/	
0643	2663	7303	CLA CLL	0742	0753	0000	HC1, 0	
0644	2664	5242	JMP CO2	0743	0754	0000	HC2, 0	
0645	2665	1411	CO2A, TAD I FD	0744	0755	0000	HC3, 0	
0646	2666	3353	DCA CHC	0745	0756	0000	HC4, 0	
0647	2667	1350	TAD CHC	0746	0757	0000	HC5, 0	
0650	2673	7941	CIA	0747	0760	0000	HC6, 0	
0651	2671	1351	TAD CHC1	0750	0761	0000	HC7, 0	
0652	2672	7510	SPA	0751	0762	0000	HC10, 0	
0653	2673	7041	CIA	0752	0763	0000	HC11, 0	
0654	2674	1343	TAD KN35	0753	0764	0000	HC12, 0	
0655	2675	7703	SZA CLA	0754	0765	0000	HC13, 0	
0656	2676	5242	JMP CO2	0755	0766	0000	HC14, 0	
0657	2677	2352	ISZ VHC	0756	0767	0000	HC15, 0	
0660	2733	7303	CLA CLL	0757	0770	0000	HC16, 0	
0661	2731	1350	TAD CHC	0760	0771	0000	HC17, 0	
0662	2732	3353	CO3, DCA HC1	0761	0772	0000	HC20, 0	
0663	2733	2302	ISZ CO3	0762			/	
0664	2744	1311	TAD FD	0763			/	
0665	2735	1351	TAD KN1	0764			/	
0666	2736	3311	DCA FD	0765			/	
0667	2737	3411	DCA I FD	0766			/	
							/SUBROUTINE TO REDUCE DATA TO ONE	
							/COORDINATE PAIR PER GRID POINT	
			/REPLACE HC WITH 0					
			/END OF TV-FIELD?					
			/NO					
			/YES					
			/ALREADY PROCESSED?					
			/NO					
			/YES					
			/END OF DATA?					
			/NO					
			/YES, GO REDUCE DATA					
			/VC=CVC?					
			/VC=CVC-1					
			/GET NEXT VC					
			/CHC1-CHC					
			/+VE?					
			/NO, MAKE +VE					
			/-35					
			/DIFF>1351?					
			/YES, GET NEXT VC					
			/NO, COUNT HC					

3767				*1000		1066	1074	3301		I	
3770				PMODE		1067	1075	7102		JMP ++5	
3771	3330	8930	SRED,	0000		1070	1076	0452		LZE	/ROUND DOWN?
3772	3301	6141		LINC		1071	1377	7132		JMP ++3	/NO
3773				LMODE		1072	1100	1120		ADA I	
3774	1022	2011		CLR		1073	1101	7776		-1	
3775	1033	5121		STC AHC	/AHC SET TO 0	1074	1102	1130		ALA	
3776	1334	1330		LDA		1075	1103	0745		RHC	
3777	1025	0745		RHC		1076	1104	4745		STC RHC	/AVERAGED HC
1438	1006	3317		CUM		1077	1105	2744		ADD RVC	
1071	1007	5120		STC MHC		1100	1106	0317		CUM	
1032	1313	2752		ADD NHC		1101	1107	2746		ADD CVC	/CVC-RVC
1033	1311	1460		SAE I	/ONLY 1 PAIR OF	1102	1110	1240		MUL	/DIV BY 2
1304	1012	0321		I	/COORDINATES?	1103	1111	5113		++4002	
1305	1013	0456		SKP	/NO, REDUCE	1104	1112	0456		SKP	
1006	1014	7116		JMP R3	/YES, STORE	1105	1113	2000		2000	
1307	1015	1120		ALA I		1106	1114	2744		ADD RVC	
1310	1016	7757		-20		1107	1115	4744		STC RVC	
1311	1317	0451		APJ	/> 20 HC COORDS	1110	1116	0302	R3,	PDP	
1012	1020	7026		JMP ++6	/NJ	1111				PMODE	
1313	1321	1320		LDA I	/YES, MAKE NHC=20	1112	3117	5600		JMP I SRED	
1014	1022	0320		20		1113				/	
1315	1023	1043		STA		1114				/	
1316	1024	0752		NHC		1115				LMODE	
1317	1025	7030		JMP ++3		1116	1120	0300	MHC,	0	
1023	1026	1120		ADA I		1117	1121	0330	AHC,	0	
1021	1027	0329		20		1120			/		
1022	1030	1120		ADA I		1121	1122	2000	RTAB1,	2000	/RECIPROCAL OF 2
1323	1031	3120		2000*RTAB1-2	/FORM ADD INST	1122	1123	1252		1252	/3
1324	1032	5033		STC ++1	/TO GET APPROPRIATE	1123	1124	1000		1000	/4
1025	1033	0316		NJP	/RECIPROCAL	1124	1125	0631		0631	/5
1026	1034	5065		STC RDIV		1125	1126	0526		0526	/6
1027	1035	2752		ALD NHC		1126	1127	0444		0444	/7
1030	1036	0317		CUM		1127	1130	0400		0400	/10
1031	1037	4035		STC HCTR	/SET HC COUNTER	1130	1131	0343		0343	/11
1032	1040	0225	AVE,	KSK I HCTR		1131	1132	0314		0314	/12
1033	1041	0456		SKP		1132	1133	0272		0272	/13
1034	1042	7055		JMP R2	/DIFF SUMMED	1133	1134	0252		0252	/14
1035	1043	1030		LDA	/CALCULATE SUM	1134	1135	0235		0235	/15
1036	1044	0753	R1,	HCI	/OF DIFFERENCES	1135	1136	0222		0222	/16
1037	1045	3120		ADD MHC		1136	1137	0210		0210	/17
1040	1046	1140		ADM		1137	1140	0200		0200	/20
1041	1047	1121		AHC		1140				/	
1042	1050	1020		LDA I		1141				/	
1043	1051	0001		I		1142				/SUBROUTINE TO STORE REDUCED DATA	
1044	1052	1140		ADM		1143				PMODE	
1045	1053	1044		R1	/INC R1	1144	3141	0000	SSRD,	0000	
1046	1054	7040		JMP AVE		1145	3142	6141		LINC	
1047	1055	1030	R2,	LDA		1146				LMODE	
1050	1056	1121		AHC		1147	1143	0651		LDF 11	
1051	1057	0241		RCL I	/X10 (BINARY)	1150	1144	1000		LDA	
1052	1060	1560		RCL I		1151	1145	0744		RVC	
1053	1061	0331		I		1152	1146	1064		STA I RD	
1054	1062	1240		MUL	/FRACTIONALY	1153	1147	1300		LDA	
1055	1063	5065		++4002	/MULTIPLY	1154	1150	0745		RHC	
1056	1064	0456		SKP		1155	1151	1364		STA I RD	
1057	1065	0300	RDIV,	0300	/MULTIPLIER	1156	1152	1320		LDA I	
1060	1066	0321		RJR I 1	/DIV BY 10	1157	1153	0301		I	
1061	1067	0451		APJ	/-VE OR +VE?	1160	1154	4752		STC NHC	
1062	1070	7076		J4P ++6	/-VE, ROUND DOWN	1161	1155	1320		LDA I	
1063	1071	2472		LZE I	/+VE, ROUND UP?	1162	1156	0753		HCI	
1064	1072	7132		JMP ++10		1163	1157	5344		STC R1	
1065	1073	1120		ADA I		1164	1160	0002		PDP	

1165			PMODE	1264	3456	1763	TAD ADDR
1166	3161	5741	JMP I SSRD	1265	3457	1057	TAL K100
1167	/	/		1266	3460	3963	LCA ADDR
1170	/	/		1267	3461	5234	JMP SMAVC+1 /REPEAT
1171	/	/		1270			
1172			LMODE	1271			
1173			/SUBROUTINE TO MODIFY ADDR ACCORDING TO	1272			LMODE
1174			/VALUE OF DATA HC-	1273			*2
1175			*1400	1274	0002	0000	CTR, 0
1176			PMODE	1275	0003	0000	FCTR, 0
1177	3430	0000	SMAHC,	1276	0004	0000	RD, 0
1200	3431	6221	CDF 20	1277	0005	0000	HCTR, 0
1201	3432	7330	CLA CLL	1300			/
1202	3433	1063	TAD ADDR	1301			/
1203	3434	1356	TAL K40	1302			SEGMENT 2
1204	3435	3232	LCA MAHC1	1303			/
1205	3436	1362	TAD HC	1304			LMODE
1206	3437	7041	CIA	1305			/SUBROUTINE TO CALCULATE CORRECTION FACTOR
1207	3438	1632	TAD I MAHC1	1306			/FOR VERTICAL COORDINATE
1210	3411	7510	SPA	1307			*200
1211	3412	5222	JMP MAHC2	1310			PMODE
1212	3413	1055	TAD K30	1311	4200	0000	SVCC,
1213	3414	7710	SPA CLA	1312	4201	6221	CDF 20
1214	3415	5600	JMP I SMAHC	1313	4202	7300	CLA CLL
1215	3416	1063	TAL ADDR	1314	4203	1061	TAD VC
1216	3417	1351	TAL K1	1315	4204	7041	CIA
1217	3420	3363	LCA ADDR	1316	4205	1463	TAD I ADDR /VC(CAL)-VC(DATA)
1220	3421	5201	JMP SMAHC+1	1317	4206	3074	LCA DIF
1221	3422	1054	MAHC2,	1320	4207	1074	TAD DIF
1222	3423	7710	SPA CLA	1321	4210	7710	SPA CLA /VC(DATA)<VC(CAL)?
1223	3424	7410	SKP	1322	4211	5237	JMP VCC2 /NO
1224	3425	5600	JMP I SMAHC	1323	4212	1363	TAD ADDR /YES
1225	3426	1363	TAL ADDR	1324	4213	6141	LINC
1226	3427	1350	TAL K1	1325			LMODE
1227	3433	3363	LCA ADDR	1326	0214	1560	RCL I
1230	3431	5201	JMP SMAHC+1	1327	0215	0077	0077
1231	/	/		1330	0216	1460	SAE I
1232	/	/		13310	0217	0000	0
1233	/	/		1332	0220	0456	SKP
1234	3432	0000	MAHC1, 0	1333	0221	6261	JMP VCC3 /TAKE YGV1-YGV3
1235	/	/		1334	0222	1440	SAF
1236	/	/		1335	0223	0341	CALIM2
1237			/SUBROUTINE TO MODIFY ADDR ACCORDING TO	1336	0224	0456	SKP
1240			/VALUE OF DATA VC-	1337	0225	6261	JMP VCC3 /TAKE YGV14-YGV13
1241	3433	0000	SMAVC,	1340	0226	0002	VCC1,
1242	3434	6221	CDF 20	1341			PMODE
1243	3435	7300	CLA CLL	1342	4227	7200	CLA CLL
1244	3436	1061	TAD VC	1343	4228	1063	TAD ADDR
1245	3437	7041	CIA	1344	4231	1360	TAD K100
1246	3440	1463	TAD I ADDR	1345	4232	3277	LCA ADDR2
1247	3441	7510	SPA	1346	4233	1677	TAD I ADDR2
1250	3442	5252	JMP MAVC1	1347	4234	7041	CIA
1251	3443	1053	TAL K24	1350	4235	1463	TAD I ADDR /YGVN-YGV(N-1)
1252	3444	7710	SPA CLA	1351	4236	5600	JMP I SVCC
1253	3445	5633	JMP I SMAVC	1352	4237	1363	TAD ADDR
1254	3446	1363	TAL ADDR	1353	4240	6141	LINC
1255	3447	1360	TAL K100	1354			LMODE
1256	3450	3363	LCA ADDR	1355	0241	1560	RCL I
1257	3451	5234	JMP SMAVC+1	1356	0242	0077	0077
1260	3452	1352	MAVC1,	1357	0243	1440	SAE
1261	3453	7710	SPA CLA	1360	0244	0300	CALIM1
1262	3454	7410	SKP	1361	0245	0456	SKP
1263	3455	5633	JMP I SMAVC	1362	0246	6271	JMP VCC4 /TAKE YGV12-YGV11

1363	0247	1440	S4E			1462	0327	7700	7700
1364	0253	9302	CALIM3			1463	0330	1460	SAE I
1365	0251	0456	SKP			1464	0331	0900	0
1366	0252	6271	JMP VCC4	/TAKE YGN25-YGN24		1465	0332	0456	SKP
1367	0253	0032	PDP			1466	0333	6360	JMP HCC3
1370			PMODE			1467	0334	0302	PDP
1371	4254	7300	CLA CLL			1470			PMODE
1372	4255	1074	TAD DIF			1471	4335	7300	CLA CLL
1373	4256	1051	TAD KNI	/MAKE ONES COMP		1472	4336	1372	TAD HADDR
1374	4257	3374	DCA DIF			1473	4337	1051	TAD KNI
1375	4263	7419	SKP			1474	4340	3371	DCA HADDR2
1376			LMODE			1475	4341	1771	TAD I HADDR2
1377	0261	0032	PDP			1476	4342	7041	CIA
1400			PMODE			1477	4343	1772	TAD I HADDR
1401	4262	1063	TAD ADDR			1500	4344	5704	JMP I SHCC
1402	4263	1057	TAD K100			1501	4345	1074	TAD DIF
1403	4264	3277	DCA ADDR2			1502	4346	7041	CIA
1404	4265	1463	TAD I ADDR			1503	4347	3074	DCA DIF
1405	4266	7041	CIA			1504	4350	1063	TAD ADDR
1406	4267	1677	TAD I ADDR2	/YGD(N+1)-YGDN		1505	4351	6141	LINC
1407	4273	5600	JMP I SVCC			1506			LMODE
1410			LMODE			1507	0352	1560	PCL I
1411	0271	0332	PDP			1510	0353	7700	7700
1412			PMODE			1511	0354	1440	SAE
1413	4272	7300	CLA CLL			1512	0355	0303	CALIM4
1414	4273	1074	TAD DIF			1513	0356	0456	SKP
1415	4274	1051	TAD KNI			1514	0357	6334	JMP HCC1
1416	4275	3374	DCA DIF			1515	0360	0302	PDP
1417	4276	5230	JMP VCC1+2			1516			PMODE
1420			/			1517	4361	7300	CLA CLL
1421			/			1520	4362	1372	TAD HADDR
1422			LMODE			1521	4363	1050	TAD K1
1423			/			1522	4364	3371	DCA HADDR2
1424	0277	0000	ADDR2, 0			1523	4365	1772	TAD I HADDR
1425	0300	1200	CALIM1, 1200			1524	4366	7041	CIA
1426	0301	1300	CALIM2, 1300			1525	4367	1771	TAD I HADDR2
1427	0302	2500	CALIM3, 2500			1526	4370	5704	JMP I SHCC
1430	0303	0031	CALIM4, 31			1527			/
1431			/			1530			/
1432			/			1531			LMODE
1433			/SUBROUTINE TO CALCULATE CORRECTION FACTOR			1532	0371	0000	HADDR2, 0
1434			/FOR HORIZONTAL COORDINATE.			1533	0372	0000	HADDR, 0
1435			PMODE			1534			/
1436	4304	0330	SHCC, 0000			1535			/
1437	4305	6021	CDF 20			1536			/SUBROUTINE TO COMPUTE AND SCALE CORRECTION
1440	4306	7300	CLA CLL			1537			/FACTORS.
1441	4307	1363	TAD ADDR			1540			*400
1442	4310	1056	TAD K43			1541			PMODE
1443	4311	3372	DCA HADDR			1542	4400	0000	SDIV, 0000
1444	4312	1062	TAD NO			1543	4401	6141	LINC
1445	4313	7041	CIA			1544			LMODE
1446	4314	1772	TAD I HADDR	/HC(CAL)-HC(DATA)		1545	0402	1120	ADA I
1447	4315	3374	DCA DIF			1546	0403	2425	2000+RIAR-25
1450	4316	1074	TAD DIF			1547	0404	4405	STC +1
1451	4317	7710	SPA CLA	/HC(DATA)<HC(CAL)		1550	0405	0000	0000
1452	4320	5345	JMP HCC2	/NO		1551	0406	4435	STC DIV3
1453	4321	1074	TAD DIF	/YES		1552	0407	0011	CLR
1454	4322	7040	CMA			1553	0410	0640	LDF 0
1455	4323	3374	DCA DIF	/ONES COMP		1554	0411	1000	LDA
1456	4324	1063	TAD ADDR			1555	0412	2074	DIF!2000
1457	4325	6141	LINC			1556	0413	0451	APJ
1460			LMODE			1557	0414	6423	JMP DIV1
1461	0326	1560	BCL I			1560	0415	1020	LDA I

1561	0416	0316	VOP
1562	0417	4447	STC DIV4
1563	0420	1030	LDA
1564	0421	2074	DIF12000
1565	0422	6431	JMP DIV2
1566	0423	1020	LDA I
1567	0424	0317	COM
1570	0425	4447	STC DIV4
1571	0426	1430	LDA
1572	0427	2074	DIF12000
1573	0430	0317	COM
1574	0431	0246	ROL 6
1575	0432	1240	MUL
1576	0433	4435	..+4002
1577	0434	0456	SKP
1633	0435	0030	0000
1631	0436	1240	MUL
1632	0437	4441	..+4002
1633	0440	0456	SKP
1634	0441	0620	0620
1635	0442	0321	ROR I 1
1636	0443	0472	LZE I
1637	0444	6447	J4P ..+3
1613	0445	1120	ADA I
1611	0446	0331	I
1612	0447	0316	VOP
1613	0450	0002	PDP
1614			PMODE
1615	4451	5600	J4P I SDIV
1616			/
1617			/
1620			LMODE
1621			/
1622	0452	3030	RTAB, 3030
1623	0453	2721	/RECIPROCAL OF 25
1624	0454	2054	2721
1625	0455	2525	2054
1626	0456	2436	2525
1627	0457	2354	2436
1630	0460	2075	2354
1631	0461	2222	2075
1632	0462	2151	2222
1633	0463	2104	2151
1634	0464	2041	2104
1635	0465	2000	2041
1636	0466	1740	2000
1637	0467	1703	1740
1640	0470	1650	1703
1641	0471	1616	1650
1642	0472	1565	1616
1643	0473	1536	1565
1644	0474	1510	1536
1645	0475	1463	1510
1646	0476	1437	1463
1647	0477	1414	1437
1650	0500	1372	1414
1651	0501	1350	1372
1652	0502	1330	1350
1653	0503	1310	1330
1654	0504	1271	1310
1655	0505	1252	1271
1656			1252
1657			/

1660			/SUBROUTINE TO CALIBRATE VERTICAL COORDINATE
1661			*630
1662			P40LE
1663	4600	0000	SCAUC, 0000
1664	4601	7300	CLA CLL
1665	4602	1063	TAD ADDR
1666	4603	6141	LINC
1667			LMODE
1670	0604	0346	SCR 6
1671	0605	1120	ADA I
1672	0606	7765	-12
1673	0607	0471	APJ I
1674	0610	6615	JMP ..+5
1675	0611	1120	ADA I
1676	0612	0305	5
1677	0613	0017	COM
1700	0614	6620	JMP ..+4
1701	0615	1120	ADA I
1702	0616	7771	-6
1703	0617	0017	COM
1704	0620	1260	MUL I
1705	0621	0144	0144
1706	0622	4656	STC CALVC
1707	0623	0002	PDP
1710			PMODE
1711	4624	4471	JMS I VCC
1712	4625	4473	JMS I DIV
1713	4626	6141	LINC
1714			LMODE
1715	0627	1140	ALC
1716	0630	0656	CALVC
1717	0631	0002	PLP
1720			PMODE
1721	4632	5600	JMP I SCAUC
1722			/
1723			/
1724			/
1725			/SUBROUTINE TO CALIBRATE HORIZONTAL COORDINATE
1726	4633	0000	SCAHC, 0000
1727	4634	7300	CLA CLL
1730	4635	1063	TAD ADDR
1731	4636	6141	LINC
1732			LMODE
1733	0637	1564	RCL I
1734	0640	7700	7700
1735	0641	1120	ADA I
1736	0642	7762	-15
1737	0643	1260	MUL I
1740	0644	0144	0144
1741	0645	4657	STC CALHC
1742	0646	0002	PLP
1743			PMODE
1744	4647	4472	JMS I HCC
1745	4650	4473	JMS I DIV
1746	4651	6141	LINC
1747			LMODE
1750	0652	1140	ALC
1751	0653	0657	CALHC
1752	0654	0002	PDP
1753			PMODE
1754	4655	5633	JMP I SCAHC
1755			/
1756			/

1757				LMODE		2056	1053	1001	1001
1760	0656	0000	CALVC,	0		2057	1054	5156	STC .+2
1761	0657	0000	CALHC,	0		2060	1055	0706	WRI
1762			/			2061	1056	0000	0000
1763			/			2062	1057	1000	LDA
1764			/			2063	1060	1056	.-2
1765			/SUBROUTINE TO STORE CALIBRATED DATA			2064	1061	1120	ADA I
1766			*1000			2065	1062	0001	1
1767			PMOUL			2066	1063	1560	PCL I
1770	5000	0000	SSTOR,	0000		2067	1064	7000	7000
1771	5001	6141	LINC			2070	1065	5103	STC TA4
1772			LMODE			2071	1066	1000	LDA
1773	1002	0641	LDF 1			2072	1067	1114	CDTRLK
1774	1003	1000	LDA			2073	1070	5072	STC .+2
1775	1004	2507	FV12000			2074	1071	0707	CHK
1776	1005	0652	LDF 12			2075	1072	0000	TA3,
1777	1006	1062	STA I CBUF	/TV-FIELD NO		2076	1073	1460	SAE I
2000	1007	1000	LDA			2077	1074	7777	7777
2001	1010	0656	CALVC			2078	1075	7007	JMP TA1-1
2002	1011	1000	STA I CRUF	/CALIBRATED VC		2079	1076	1020	LDA I
2003	1012	1000	LDA			2080	1077	0001	1
2004	1013	0657	CALHC			2083	1100	1140	AIM
2005	1014	1062	STA I CBUF	/CALIBRATED HC		2084	1101	1072	TA3
2006	1015	0000	KSK CRUF	/BUFFER FULL?		2085	1102	1460	SAE I
2007	1016	7000	JAP .+4			2086	1103	0000	TA4,
2008	1017	0000	PDP			2087	1104	7071	JMP TA3-1
2009			PMODE			2088	1105	1000	LDA
2010	5000	4500	JMS I TAPE	/PUT ON TAPE		2089	1106	1100	TA4
2011	5001	7410	SKP			2090	1107	5114	STC CDTRLK
2012			LMODE			2091	1110	0062	SFT I CRUF
2013	1002	0000	PEP			2092	1111	2077	2077
2014			PMODE			2093	1112	0000	PEP
2015	5000	5600	JMP I SSTOR			2094	5113	5624	JMP I STAPE
2016			/			2095			/
2017			/			2096			/
2018			/			2097			/
2019			/			2098			/
2020			/SUBROUTINE TO PUT CALIBRATED DATA ON TAPE			2099			/
2021			/ON UNIT 2			2100	1114	0000	CDTRLK,
2022	5000	0000	STAPE,	0000		2101			LMODE
2023	5001	6141	LINC			2102			000
2024			LMODE			2103			*2
2025	1000	1000	LDA			2104	0000	2077	CDTRLK,
2026	1001	1114	CDTRLK	/START TAPE DN		2105			/
2027	1002	1100	ALA I			2106			/
2028	1003	1100	S000			2107			/
2029	1004	5000	STC TA1			2108			/
2030	1005	1000	LDA I	/SET EXTENDED UNIT		2109			/
2031	1006	0001	1	/BIT 11		2110			/
2032	1007	0652	AKO			2111	0000	0076	
2033	1008	0700	LDF 12			2112	0001	6000	SFT I 16
2034	1009	0700	WRI I			2113	0002	7000	.-+6001
2035	1010	0000	TA1,			2114	0003	0600	JMP QAINIT
2036	1011	1000	LDA			2115	0004	0500	MESS1
2037	1012	1000	TA1			2116	0005	7000	ANSWER
2038	1013	1100	ADA I			2117	0006	0500	JMP QARFSH
2039	1014	1001	1001			2118	0007	0070	SFT I 10
2040	1015	5000	STC .+2			2119	0008	61000	ANSWER
2041	1016	0700	WRI I			2120	0009	61000	JMP PM
2042	1017	0000	0000			2121	0010	1000	LDA
2043	1018	1000	LDA			2122	0011	0175	OCTAC
2044	1019	1100	ADA I			2123	0012	1100	ALA I
2045	1020	0000	0000			2124	0013	7001	-776
2046	1021	0000	0000			2125	0014	0471	APU I
2047	1022	0000	0000			2126	0015	6170	JMP XIT
2048	1023	0000	0000			2127	0016	1000	LDA
2049	1024	0000	0000			2128	0017	1000	LDA
2050	1025	0000	0000			2129	0018	1000	LDA
2051	1026	0000	0000			2130	0019	1000	LDA
2052	1027	0000	0000			2131	0020	1000	LDA
2053	1028	0000	0000			2132	0021	1000	LDA
2054	1029	0000	0000			2133	0022	1000	LDA
2055	1030	0000	0000			2134	0023	1000	LDA

2155	0043	0175	JCTAC		2254	0137	1330	PM1,	LDA I 10
2156	0041	0641	LDF 1		2255	0140	1420		SHD I
2157	0042	1040	STA		2256	0141	7400		7400
2160	0043	2250	DTBLK!2000		2257	0142	6015		JMP 15
2161	0044	0076	SET I 16		2260	0143	1420		SHD I
2162	0045	6046	+6001		2261	0144	3400		3400
2163	0046	7000	JMP QAINIT		2262	0145	6015		JMP 15
2164	0047	0625	MESS2	/DTBLK	2263	0146	1420		SHD I
2165	0050	0500	ANSWER		2264	0147	0000		0
2166	0051	7053	JMP QARFSH		2265	0150	6137		JMP PM1
2167	0052	0070	SET I 10		2266	0151	1120		ALA I
2170	0053	0500	ANSWER		2267	0152	7717		-60
2171	0054	6133	JMP PM		2270	0153	1340		STA
2172	0055	1030	LDA		2271	0154	0176		NUM
2173	0056	0175	JCTAC		2272	0155	1120		ALA I
2174	0057	1120	ADA I		2273	0156	0001		1
2175	0060	2031	-776		2274	0157	0451		APD
2176	0061	0471	APD I		2275	0160	6174		JMP KIT
2177	0062	6174	JMP KIT		2276	0161	1120		ADA I
2200	0063	1000	LLA		2277	0162	7767		-10
2201	0064	0175	JCTAC		2300	0163	0471		APD I
2202	0065	0641	LDA I		2301	0164	6174		JMP KIT
2203	0066	1040	STA		2302	0165	1300		LLA
2204	0067	2307	DTBLK!2000		2303	0166	0175		JCTAC
2205	0070	0076	SET I 16		2304	0167	1260		MUL I
2206	0071	6072	+6001		2305	0170	0010		10
2207	0072	7030	JMP QAINIT		2306	0171	2176		ADD NUM
2210	0073	0652	MESS3	/CDTBLK	2307	0172	4175		STC JCTAC
2211	0074	0500	ANSWER		2310	0173	6137		JMP PM1
2212	0075	7053	JMP QARFSH		2311	0174	6016	XIT,	JMP 16
2213	0076	0070	SET I 10		2312			/	
2214	0077	0500	ANSWER		2313			/	
2215	0100	6133	JMP PM		2314	0175	0000	OCTAC,	0
2216	0101	1000	LDA		2315	0176	0000	NUM,	0
2217	0102	0175	JCTAC		2316			/	
2220	0103	1120	ADA I		2317				*500
2221	0104	7031	-776		2321	0500	0000	ANSWER,	0
2222	0105	0471	APD I		2321			/	
2223	0106	6174	JMP KIT		2322			/	
2224	0107	1000	LDA		2323				*600
2225	0110	0175	JCTAC		2324	0600	0640		
2226	0111	2042	LDF 2		2324	0601	0001		
2227	0112	1040	STA		2324	0602	1440		
2230	0113	3114	CDTBLK!2000		2324	0603	1524		
2231	0114	0076	SET I 16		2324	0634	3040		
2232	0115	6116	+6001		2324	0605	2324		
2233	0116	7000	JMP QAINIT		2324	0606	0122		
2234	0117	0700	MESS4	/PRESS S TO START	2324	0637	2440		
2235	0120	0500	ANSWER		2324	0610	0214		
2236	0121	7053	JMP QARFSH		2324	0611	1703		
2237	0122	0070	SET I 10		2324			MESS1,	TEXT ZF CAL MTK START BLOCK
2240	0123	0500	ANSWER		2325	0612	1343		
2241	0124	1300	LDA I 10		2325				
2242	0125	1420	SHD I		2326	0613	4740		
2243	0126	2300	R300	/AN S?	2326	0614	4347		
2244	0127	0456	SKP	/YES	2326				
2245	0130	6174	JMP KIT	/NO REPEAT MESS	2327	0615	4043		
2246	0131	0600	LDF 0		2327	0616	0640		
2247	0132	6400	JMP 400		2327	0617	4040		
2250	0133	0050	SET 15		2327	0620	4033		
2251	0134	0000	0		2327	0621	2402		
2252	0135	0011	CLR		2327	0622	1413		
2253	0136	4175	STC OCTAC		2327	0623	4074		

2327 0624 6334
 2327
 2330 0625 0640
 2330 0626 0401
 2330 0627 2401
 2330 0630 4023
 2330 0631 2401
 2330 0632 2224
 2330 0633 4032
 2330 0634 1417
 2330
 2331 0635 0313
 2331 0636 4347
 2331
 2332 0637 4043
 2332
 2333 0640 4740
 2333 0641 4347
 2333
 2334 0642 4043
 2334 0643 0640
 2334 0644 4043
 2334 0645 4304
 2334 0646 2402
 2334 0647 1413
 2334 0650 4374
 2334 0651 6334
 2334
 2335 0652 0640
 2335 0653 0301
 2335 0654 1440
 2335 0655 3431
 2335 0656 2401
 2335 0657 4003
 2335 0660 2401
 2335 0661 2224
 2335 0662 4032
 2335 0663 1417
 2335
 2336 0664 0313
 2336 0665 4347
 2336
 2337 0666 4043
 2337
 2340 0667 4740
 2340 0670 4306
 2340 0671 4740
 2340 0672 4043
 2340 0673 3334
 2340 0674 2402
 2340 0675 1413
 2340 0676 4374
 2340 0677 6334
 2340
 2341 0700 3624
 2341 0701 3120
 2341 0702 3540
 2341 0703 2340
 2341 0704 2417
 2341 0705 4023
 2341 0706 2401
 2341 0707 2224
 2341 0710 4074

F CTBLK <3\Z

MESS2, TEXT ZF DATA START BLOCK

F DTBLK <3\Z

MESS3, TEXT ZF CAL DATA START BLOCK

F CDTBLK <3\Z

2341 0711 6134
 2341
 2342
 2343
 2344
 2345
 2346
 2347
 2350
 2351
 2352
 2353
 2354
 2355 1000 1020
 2356 1001 0202
 2357 1002 2000
 2360 1003 1060
 2361 1004 0000
 2362 1005 3200
 2363 1006 4001
 2364 1007 1001
 2365 1010 3264
 2366 1011 5057
 2367 1012 1021
 2370 1013 5052
 2371 1014 4006
 2372 1015 0043
 2373 1016 1052
 2374 1017 0044
 2375 1020 1057
 2376
 2377 1021 0041
 2400 1022 0004
 2401 1023 7270
 2402 1024 0016
 2403 1025 1324
 2404 1026 7231
 2405 1027 7035
 2406 1029 7050
 2407 1031 1460
 2410 1032 0043
 2411 1033 7026
 2412 1034 7021
 2413
 2414 1035 1343
 2415 1036 1324
 2416 1037 1120
 2417 1040 7717
 2420 1041 0317
 2421 1042 4006
 2422 1043 1363
 2423 1044 0226
 2424 1045 7043
 2425 1046 1323
 2426 1047 7026
 2427
 2430 1050 1343
 2431 1051 0064
 2432 1052 0000
 2433

MESS4, TEXT ZFTYPE S TO START <1\Z

/QANDA SUBROUTINE FOR THE
 /PDP-12
 /REMOVE *1000 BELOW IF
 /INSERTING SOURCE DIRECTLY
 /INTO YOUR PROGRAM SOURCE
 *1000 /REMOVE, IF DESIRED

/TO HERE TO INITIALIZE THE ROUTINE

GAINIT, LDA I /SAVE JMP RETURN
 2
 ADD 0
 STA I
 QAB, 0 /JMP +3
 ADD QAL+3
 STC I /PTR TO FIRST PARAM
 LDA I /GET FIRST PARAM
 ADD QAG+1 /PTR TO HALFWORD-1
 STC QAG-3
 LDA I 1
 STC QARF5H-1
 STC 6 /XR6 USED AS A SWITCH. =0

IF NO ANSWER FIELD, =1777 IF YES

QACA, SET 3 /XR3 TO PTR TO ANSWERS
 QARF5H-1
 SET 4 /XR4 TO PTR TO QUESTIONS
 QAG-3
 /TO HERE IF FIRST TIME TH.

OUGH JR FOLLOWING A CR

SET 1
 4
 JMP QAT
 NOP /F
 LDH I 4 /H. BUMP PTR IF H OR F
 QAD, JMP QAO
 JMP +6 /74
 JMP QAE /34
 SAE I /CR?
 43
 JMP QAD /NO
 JMP QACA+4 /EXAMINE NEXT CHAR
 /INITIALIZE ANSWER BUFR
 /74 TO ANSWERS
 /NEXT HALFWORD

STH 3
 LDH I 4
 ADA I
 -60
 COE
 STC 6
 STH I 3 /0 IN AC
 XSK I 6
 JAP -2
 LDH I 3 /BUMP PTR. TO ANSWERS
 JMP QAD
 /ANSWER BUFR IS INITIATED

QAE, STH 3
 SET I 4 /XR4 TO PTR TO LAST TYPED
 CHAR IN ANSWER BUFR
 0
 /----RE-ENTER HERE TO REPR

2434	1053	1323	QARFSH, LDA I	/INITIAL Y POSITION	2520	1136	7521	QAJ, ER	JMP GETRBD	/TO HERE IF DISPLAYED BUFR
2435	1054	0277	277		2521	1137	0470		AZE I	
2436	1055	5113	STC QAH-1		2522	1147	7004		JMP QAB /NOTHING TYPED - EXIT	
2437	1056	0063	SET I 3	/XR3 TO PTR TO HALFWORD QU	2523	1141	0062		SET I 2	
			ESTIONS-1		2524	1142	1412		QAY	
2443	1057	0003	0		2525	1143	1402		SHD 2	/LF?
2441	1063	0045	SET 5	/XR5 TO PTR TO LAST DISPLA	2526	1144	7311		JMP QAK+4	/YES- EXIT
			YED CHAR IN ANSWER BUFR		2527	1145	1422		SHD I 2	/CR?
2442	1061	1052	QARFSH-1		2530	1146	7223		JAP QAV	
2443	1062	0341	QAG, SET I		2531	1147	0206		XSX 6	/IS THERE AN ANSWER FIELD?
2444	1063	0023	3		2532	1150	7053		JAP QARFSH	
2445	1064	7870	JMP QAT		2533	1151	1422		SHD I 2	/<?
2446	1065	7874	JAP .+7	/F	2534	1152	7175		JAP QAL	
2447	1066	1323	LDM I 3	/H. BUMP PTR	2535	1153	1422		SHD I 2	/>?
2450	1067	1320	LDA I	/NEITHER- ASSUME HALF SIZE	2536	1154	7305		JAP QAK	
2451	1070	1560	DCL I		2537	1155	1422		SHD I 2	/ALT?
2452	1071	5103	STC QAM+2	/SET INSTR TO CLEAR FF FOR	2540	1156	7015		JAP QACA /REINITIALIZE	
			HALF SIZE		2541	1157	1422		SHD I 2	/BACK SLASH?
2453	1072	3512	ADD QAV	/NOP IN AC	2542	1160	7053		JMP QARFSH	/IGNORE
2454	1073	7101	JMP QAM		2543	1161	1422		SHD I 2	/RUBOUT?
2455	1074	1323	LDM I 3	/BUMP PTR	2544	1162	7175		JAP QAL	/IGNORE
2456	1075	1029	LDA I		2545	1163	1422		SHD I 2	/TAB?
2457	1076	1620	RSE I		2546	1164	7053		JMP QARFSH	/IGNORE
2463	1077	5103	STC QAM+2	/SET INSTR TO SET FF FOR F	2547	1165	5172		STC .+5	/ACCEPTABLE CHAR
			ULL SIZE		2550	1166	7231		JMP QAO	/TEST NEXT CHAR
2461	1100	3513	ADD QAL+1	/ADD 90 IN AC	2551	1167	7263		JMP QAO	/74 BACK PTR UP BY 1
2462	1101	5245	QAG, STC QAP+3		2552	1170	7263		JAP QAO	/34
2463	1102	0024	MSC I 4	/LEAD CONTROL REGISTER	2553	1171	1020		LDA I	/OK- STORE IT
2464	1103	1620	RSE I	/THIS INSTR CHANGES- EITHER	2554	1172	0000		0	
			R RSE & UP DCL &		2555	1173	1344		SHD 4	
2465	1104	0020	200		2556	1174	7053		JMP QARFSH	/REDISPLAY
2465	1105	0004	MSC 4	/AC TO CONTROL REGISTER	2557	1175	1304		LDM 4	/TO HERE IF RUBOUT OR <
2467	1106	0061	SET I 1	/XRI TO INITIAL X POSITION	2560	1176	7232		JMP QAO+1	
2473	1107	0100	100		2561	1177	7353		JMP QARFSH	/74 IGNORE
2471	1110	1024	LDA I	/Y COORDINATE MULTIPLE	2562	1200	1775		-6002	
2472	1111	7737	-43		2563	1201	1302		LDM 2	/TEST THE CHAR
2473	1112	1160	ADM I	/Y COORDINATE	2564	1202	1460		SAE I	/RUBOUT?
2474	1113	0430	0		2565	1203	0037		37	
2475	1114	1323	QAH, LDM I 3		2566	1204	7263		JMP QAO	/NO- BACK PTR UP BY 1
2476	1115	7232	JAP QAO+1		2567	1205	0045		SET 5	
2477	1116	7301	JAP QAZ	/74 BUMP PTR TO NEXT CHAR	2570	1206	0004		4	
			PUT 40 IN AC		2571	1207	0043		SET 3	
2503	1117	7136	JAP QAJ	/34	2572	1210	0004		4	
2501	1120	1420	SHD I	/NEITHER	2573	1211	7213		JAP .+2	
2502	1121	4300	4300		2574	1212	1325		LDM I 5	/BUMP PTR
2503	1122	7362	JAP QAG	/CR- MOVE X AND Y COORDINA	2575	1213	1323		LDM I 3	/GET NEXT CHAR
			TE		2576	1214	7232		JMP QAO+1	
2530	1123	7242	JMP QAP	/ISPLAY CHAR	2577	1215	0016		NOP	/IF 74 OR 34, REPLACE CURS
2535	1124	7114	JAP QAH	/PICK UP NEXT CHAR					ENT CHAR WITH 0	
2536	1125	7242	JAP QAP	/TO HERE IF DISPLAYING ANS	2600	1216	0011		CLR	
			WER BUFR		2601	1217	1345		SHD 5	
2507	1126	1520	SHD I	/SWITCH TO DISPLAY CURSOR- EITHER	2602	1220	0450		AZE	/WAS IT 74 OR 34?
			0000 OR 7777		2603	1221	7212		JAP .-7	/NO- CONTINUE
2513	1127	0000	0	/IFXR4=XRS, THEN SWITCH=77	2604	1222	7263		JAP QAO	/BACK PTR UP BY 1
			77		2605					/TO HERE IF CR
2511	1130	7516	JMP QAF		2606	1223	0006		QAG, XSX 6	/EXIT ROUTINE IF NO ANSWER
2512				/QUESTION MODE	2607	1224	7311		JAP QAK+4	
2513	1131	1325	QAI, LDM I 5						FIELD	
2514	1132	7232	JMP QAO+1		2610	1225	7231		JAP QAO	
2515	1133	7114	JAP QAH	/74	2611	1226	7053		JMP QARFSH	/74 MOVE PTR TO NEXT BUFR
2516	1134	7114	JAP QAH	/34					ION FIELD	
2517	1135	7125	JAP QAI-4	/NEITHER- DISPLAY IT	2612	1227	7051		JAP QAE+1	/34 END OF BUFR- MOVE PTR

2613	1230	7225		J4P QAN+2		2677	1307	7263		JMP QAQ	/YES. IGNORE
2614						2730	1310	7424		JMP QAK	/MOVE OUT FORWARD
2615	1231	1324	QAQ,	LDH I 4	/SNR	2701					/TO HERE TO EXIT WITH
2616	1232	1420		SHD I	/				SKIP		
				FIELD	/	2702	1311	1020		LDA I	
2617	1233	7400		7400	/	2703	1312	0001		1	
				FR	/	2704	1313	1140		ALM	
2620	1234	6000		JMP 0	/	2705	1314	1004		QAR	
				4 WJR 34	/	2706	1315	7004		JMP QAR	
2621	1235	1467		SAE I		2707					/CHARACTER PATTERNS
2622	1236	0334		34		2710	1316	0101	QAQ,	0101	/KBL 0, ILLEGAL. USEL
2623	1237	0220		XSK I 0					AS MARKER		
2624	1240	0220		XSK I 0		2711	1317	0101		0101	
2625	1241	6300		JMP 0		2712	1320	4477		4477	/1:A
2626					/SNR TO DISP LINC CHA	2713	1321	7744		7744	
				R IN AC		2714	1322	5177		5177	/2:B
2627	1242	0241	QAP,	RUL 1	/MULT BY 2 FOR INDEX	2715	1323	2651		2651	
				TO ADDRESS OF TABLE		2716	1324	4136		4136	/3:C
2630	1243	3430		ADD QAK+4		2717	1325	2241		2241	
2631	1244	4302		STC 2	/ADDRESS OF CHAR TO D	2718	1326	4177		4177	/4:D
				ISP IN XH2		2721	1327	3641		3641	
2632	1245	3506		ADD QAU	/THIS INSTR CHANGES	2722	1330	4577		4577	/5:E
				EITHER UP OR ADD 9U		2723	1331	4145		4145	
2633	1246	3536		ADD QAU		2724	1332	4477		4477	/6:F
2634	1247	2001		ADD I	/ADD 4 TO XRI TO SPAC	2725	1333	4044		4044	
				E CHAR		2726	1334	4136		4136	/7:G
2635	1250	4001		STC 1		2727	1335	2645		2645	
2636	1251	2035		ADD 5	/GET ADDRESS OF ANSWE	2730	1336	1077		1077	/10:H
				R BUFR		2731	1337	7710		7710	
2637	1252	0317		COM		2732	1340	7741		7741	/11:I
2640	1253	2304		ALL 4		2733	1341	0041		0041	
2641	1254	0450		AZE		2734	1342	4142		4142	/12:J
2642	1255	0011		CLR		2735	1343	4076		4076	
2643	1256	5127		STC QAI-2	/SWITCH=0 OR 7777	2736	1344	1077		1077	/13:K
2644	1257	3113		ADD QAH-1	/Y COORDINATE IN AC	2737	1345	4324		4324	
2645	1267	1742		ESC 2		2740	1346	0177		0177	/14:L
2646	1261	1762		LSC I 2	/DISPLAY CHAR	2741	1347	0301		0301	
2647	1262	6333		J4P 0		2742	1350	3077		3077	/15:M
2650	1263	1320	QAQ,	LDA I	/BACK UP PTR BY 1	2743	1351	7730		7730	
2651	1264	3777		-4000		2744	1352	3077		3077	/16:N
2652	1265	1140		ALM		2745	1353	7706		7706	
2653	1266	0334		4		2746	1354	4177		4177	/17:O
2654	1267	7353		J4P QANFSH	/REDISPLAY	2747	1355	7741		7741	
2655					/	2750	1356	4477		4477	/20:P
2656	1270	1321	QAI,	LH I 1	/SNR	2751	1357	3344		3344	
2657	1271	1420		SHL I	/	2752	1360	4276		4276	/21:Q
2660	1272	0600		0600	/	2753	1361	0376		0376	
2661	1273	6000		JMP 0	/	2754	1362	4477		4477	/22:R
2662	1274	1460		SAE I	/	2755	1363	3146		3146	
2663	1275	0010		10		2756	1364	5121		5121	/23:S
2664	1276	0202		XSK I 0		2757	1365	4651		4651	
2665	1277	0223		XSK I 0		2760	1366	0040		0040	/24:T
2666	1300	6000		J4P 0		2761	1367	4077		4077	
2667					/	2762	1370	0177		0177	/25:U
2670	1301	1323	QAZ,	LDH I 3		2763	1371	7701		7701	
2671	1302	1320		LDA I		2764	1372	0176		0176	/26:V
2672	1333	0340		40		2765	1373	7402		7402	
2673	1334	7125		JMP QAI-4		2766	1374	0677		0677	/27:W
2674					/TO HERE IF >	2767	1375	7701		7701	
2675	1305	1324	QAK,	LDH I 4		2770	1376	1463		1463	/30:X
2676	1306	0470		AZE I	/IS CURRENT CHAR BLAN	2771	1377	6314		6314	
				X?		2772	1400	0770		0770	/31:Y
						2773	1401	7007		7007	

2774	1402	4543	4543	/32:Z	3267	1471	0651	0651	
2775	1403	6151	6151		3270	1472	1526	1526	/66:6
2776	1404	4177	4177	/33:/	3271	1473	4225	4225	
2777	1405	0300	0300		3272	1474	4443	4443	/67:7
3221				/34:BACKSLASH IGNORED	3273	1475	6050	6050	
					3274	1476	5126	5126	/70:8
					3275	1477	2651	2651	
3221	1406	0000	0	/NOT USED	3276	1500	5122	5122	/71:9
3222	1407	0000	0	/NOT USED	3277	1501	3651	3651	
3223	1412	0000	0000	/35:J	3100	1532	2200	2200	/72::
3224	1411	7741	7741		3101	1533	0000	0000	
3225				/CODES 36:ALT, 37:RUB	3102	1504	4631	4631	/73:3
					3103	1535	0000	0000	
					3104				/CODE 74:<NOT DISPLAY
3226	1412	4543	4543	/LF,CR					
3227	1413	7476	7476	/,>					
3228	1414	3634	3634	/ALT, BACKSLASH					
3229	1415	3747	3747	/RUBOUT, TAB	3105	1506	0002	0	/CONSTANT
3230	1416	0000	0000	/40:SPACE	3106	1507	0000	0	/NOT USED
3231	1417	0000	0000		3107	1510	1212	1212	/75:*
3232	1418	7500	7500	/41:K!	3110	1511	1212	1212	
3233	1421	0000	0000		3111				/CODE 76:> NOT DISPLA
3234	1422	7000	7000	/42:"					
3235	1423	0000	0000		3112	1512	0016	YED	
3236				/CODES 43:, 44:, 45:L	3113	1513	3506	QAW, NOP	
					3114	1514	4000	ADD QAW	/77:?
					3115	1515	2055	4000	
					3116			2055	
					3117	1516	1760	/	
					3120	1517	6000	QAF, DSC I	
					3121	1520	7131	6000	
					3122			JMP QAI	
					3123				
					3124				/END Q+A
					3125				
					3126				
					3127				
					3130				/KEYBOARD INPUT ROUTINE
					3131				
					3132				
					3133			QAKRP=6036	/PEP-8 IOT KBL
					3134			QATSF=6041	/TSF
					3135			QATLS=6046	/TLS
					3136	1521	1000	/	
					3137	1522	0000	GETKBD, LDA	
					3140	1523	5643	0	
					3141	1524	2001	STC QAKXIT+6	/SAVE RETURN
					3142	1525	5643	ADD 1	/SAVE XRS 1 AND 2
					3143	1526	2002	STC QAKXIT+3	
					3144	1527	5642	ADD 2	
					3145	1528	5636	STC QAKXIT+5	
					3146	1530	5636	STC QAKXIT+1	
					3147	1531	0415	KST	/WAS SOMETHING TYPED?
					3150	1532	6000	JMP 0	/NO: EXIT
					3151	1533	0000	IOR	
					3152	1534	6000	QAKRB	/GET TTY CHAR, CLEAR FLAG
					3153	1535	1060	STA I	/SAVE IT
					3154	1536	0000	QATY, J	
					3155	1537	1120	ADA I	
					3156	1540	7540	-237	
					3157	1541	0451	AND	/BETWEEN 233 AND 237?
						1542	7624	JMP QACNTR	/CONTROL CHAR. CHECK P
								OR CR,LF,TAB	
					3160			/	
					3161	1543	0061	SET I 1	/NO
					3162	1544	1654	QACIAR-1	

3163	1545	0062	SET I 2		3261	1634	7637	JMP QAEKIT+2	/EXIT, DONT ECHO
3164	1546	7773	-7		3262				
3165	1547	1333	LDA		3263	1635	1020	QAEKIT, LDA I	/GET 6-BIT ASCII
3166	1550	1536	QATY		3264	1636	0000	0	
3167	1551	1461	SAE I 1		3265	1637	0061	SET I 1	/RESTORE XRS
3170	1552	7554	JMP +2		3266	1640	0000	0	
3171	1553	7635	JMP QAEKIT	/ILLEGAL CHAR. DONT EC	3267	1641	0062	SFT I 2	
					3270	1642	0000	0	
					3271	1643	6000	JMP	/EXIT SXR GETKRD
3172	1554	0222	XSK I 2	/CHECKED THEM ALL?	3272			/SXR TO PRINT CCAC)	
3173	1555	7551	JMP --4		3273	1644	0500	QATPE, IOR	
3174					3274	1645	6046	QATLS	/PDP-8 IOT TLS
3175	1556	1120	ADA I		3275	1646	1000	LDA	
3176	1557	7440	-337		3276	1647	0000	0	
3177	1560	0451	APU	/BETWEEN 240 AND 337?	3277	1650	5654	STC +4	/SAVE RETURN
3203	1561	7575	JMP QALEGL	/YES. LEGAL CHAR	3300	1651	0500	IOR	
3201					3301	1652	6041	QATSF	/WAIT FOR FLAG
3202	1562	1461	SAE I 1	/NO. CHECK FURTHER.	3302	1653	7651	JMP --2	
3203	1563	7572	JMP +7		3303	1654	6000	JMP	/EXIT
3224	1564	1000	LLA I	/RUBOUT	3304				
3205	1565	0334	334		3305	1655	0243	QACHAR, 243	/HASH
3206	1566	7644	JMP QATPE	/ECHO BACKSLASH	3306	1656	0244	244	/DOLLAR SIGN
3207	1567	1020	LDA I		3307	1657	0245	245	/PER CENT
3210	1570	0307	37		3310	1660	0247	247	/APOSTROPHE
3211	1571	7637	JMP QAEKIT+2	/LEGAL EXIT	3311	1661	0330	300	/AT SIGN
3212					3312	1662	0336	336	/UP ARROW
3213	1572	1461	SAE I 1		3313	1663	0337	337	/BACK ARROW
3214	1573	7635	JMP QAEKIT	/ILLEGAL	3314	1664	0340	40	/RUBOUT
3215					3315	1665	0036	36	/ALT
3216	1574	7637	JMP QAEKIT+2	/EXIT, DONT ECHO	3316				/END OF SXR GETKRD
3217									
3220	1575	1000	QALEGL, LDA						
3221	1576	1536	QATY						
3222	1577	7644	JMP QATPE	/ECHO CHAR					
3223	1600	3536	ADD QATY						
3224	1601	1560	RCL I	/STRIP IT TO 6-BIT					
3225	1602	7700	7700						
3226	1603	7637	JMP QAEKIT+2						
3227									
3230	1604	1460	/TO HERE IF CONTROL CHAR						
3231	1605	7755	QACNTR, SAE I						
3232	1606	7621	7755						
3233	1607	1323	JMP QACKLF						
3234	1608	0343	LDA I	/CR					
3235	1611	5636	43						
3236	1612	1020	SIC QAEKIT+1						
3237	1613	0215	LDA I						
3240	1614	7644	215						
3241	1615	1020	JMP QATPE						
3242	1616	0212	LDA I						
3243	1617	7644	212						
3244	1620	7635	JMP QATPE						
3245			JMP QAEKIT						
3246	1621	1460							
3247	1622	7752	QACKLF, SAE I						
3250	1623	7627	JMP --4						
3251	1624	1020	LLA I	/LF					
3252	1625	0045	45						
3253	1626	7611	JMP QACNTR+5						
3254	1627	1460	SAE I						
3255	1630	7751	7751						
3256	1631	7635	JMP QAEKIT	/ILLEGAL					
3257	1632	1323	LDA I						
3260	1633	0347	47						

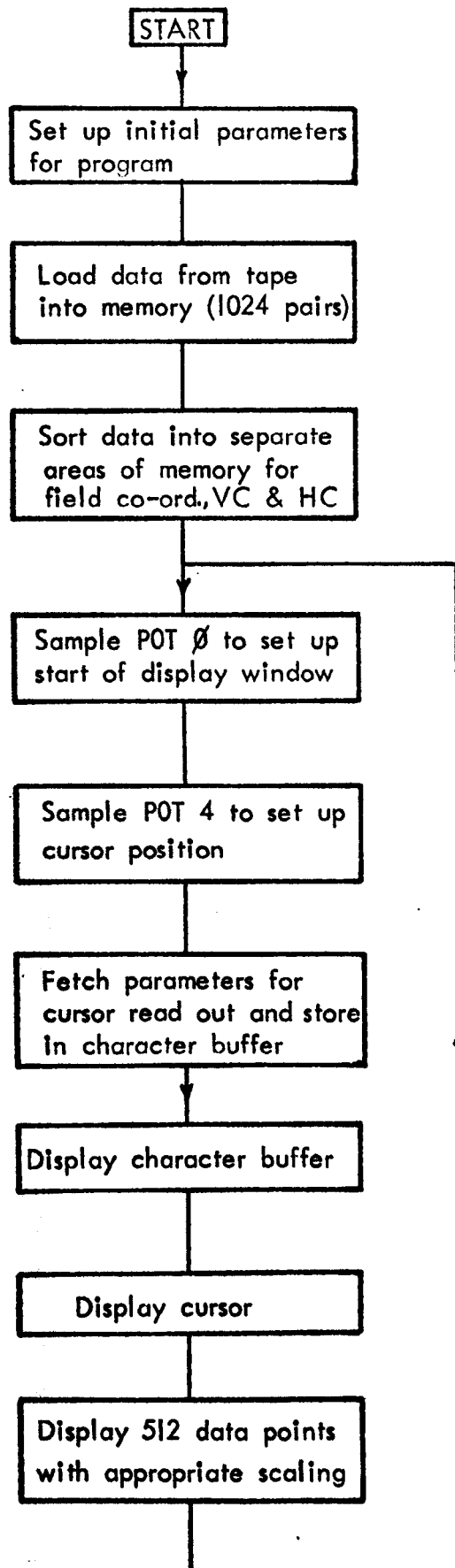
NO ERRORS

ADDR 0063
 AMIF2 4277
 AHC 4121
 ANSWER 6500
 AVF 3040
 CAHC 3100
 CALHC 4657
 CALIM1 4330
 CALIM2 4301
 CALIM3 4302
 CALIM4 4303
 CALVC 4656
 CAVC 0077
 CRUF 4002
 CTRPLK 5114
 CHC 2753
 CHC1 2751
 CUP 0006
 CUI 2616
 C02 2642
 C0PA 2665
 C03 2702
 C04 2721
 C05 2731
 CTRPLK 2250
 CTP 2002
 CVC 2746
 CVC1 2747
 DIF 0374
 DIV 0073

DIV1 4423
DIV2 4431
DIV3 4435
DIV4 4447
DTPLK 2307
END 0422
FCTR 2003
FD 0911
FLAG 2531
FN 2537
GC 0076
GETMPD 7501
HADDF 4372
HADDF2 4371
HC 0362
HCC 0072
HCC1 4334
HCC2 4345
HCC3 4360
HCTR 2005
HC1 2753
HC10 2762
HC11 2763
HC12 2764
HC13 2765
HC14 2766
HC15 2767
HC16 2770
HC17 2771
HC2 2754
HC20 2772
HC3 2755
HC4 2756
HC5 2757
HC6 2760
HC7 2761
K1 0051
K100 0060
K101 0053
K102 0055
K103 2743
K1 0050
K100 0052
K101 0054
K102 0056
LIATA 0070
LIAT1 0267
MHC 0104
MHC1 3432
MHC2 3422
MHN 0430
MVC 0103
MVC1 4452
MSS1 6600
MSS2 6625
MSS3 6652
MSS4 6700
MC 3120
MX 3375
MX1 2221
MX2 2223
MX3 2244

M1 0411
M2 0437
M3 0446
NF1 2526
NHC 2752
NUM 6176
OCTAC 6175
PM 6133
PM1 6137
PTR 0010
QAP 7004
QACA 7015
QACHAR 7655
QACKLF 7621
QACVTR 7634
QAD 7026
QAE 7050
QAEEXIT 7635
QAF 7516
QAG 7062
QAH 7114
QAI 7131
QAINIT 7030
QAJ 7136
QAK 7305
QAKRR 6036
QAL 7175
QALEGL 7575
QAM 7101
QAN 7223
QAO 7231
QAP 7242
QAO 7263
QARFSH 7053
QAT 7270
QATLS 6146
QATPE 7644
QATSF 6141
QATY 7536
QAU 7536
QAV 7316
QAW 7512
QAX 7424
QAY 7412
QAZ 7301
ED 2034
EDIV 3365
ED 0065
EIC 2745
ETAR 4452
ETARI 3122
EVC 2744
E1 3344
E2 3055
E3 3116
SCAHC 4633
SCAVC 4600
SCJMP 2600
SDIV 4400
SGC 2312
SHCC 4304
SLDATA 2251
SMAHC 3400

SMAVC 3433
SMTX 2200
SPLD 2512
SRD 0064
SRD 3000
SSRD 3141
SSTOR 5000
STAPE 5024
STOR 0101
STVF 2400
SUCC 4200
TAPE 0132
TA1 5040
TA3 5072
TA4 5103
THC 2511
TVC 2510
TVF 0067
TVF1 2436
TVF2 2470
VC 0061
VCC 0071
VCC1 4226
VCC2 4237
VCC3 4261
VCC4 4271
XIT 6174



```

3111 *23
3111 /DISPLAY CALIBRATED DATA WITH CURSOR -
3112 /DISC-CAL
3113 /
3114 /
3115 /16-6-75
3116 /
3117 /
3118 /
3119 /K.J. JARRETT-
3120 /
3121 /DISPLAYS DATA FROM TAPE CONTAINING
3122 /CALIBRATED DATA ON UNIT 2-A DISPLAY
3123 /OF CAMERA CHANNEL NUMBER 10 FIELD
3124 /NUMBER, VERTICAL COORDINATE, HORIZONTAL
3125 /COORDINATE AND AN INDICATION AS TO
3126 /WHETHER THE FOCUS PLATE (FP) WAS SAMPLED
3127 /IS ALSO PROVIDED.
3128 /
3129 /
3130 /DATA TO BE DISPLAYED IS PLACED IN A
3131 /1324 POINT BUFFER, AND A MOVING WINDOW
3132 /TO DISPLAY THE DATA IS CONTROLLED BY
3133 /PJT 3. A CURSOR IS CONTROLLED BY PJT 4
3134 /TO PROVIDE A READOUT OF DATA AS DESCRIBED
3135 /ABOVE.
3136 /
3137 /
3138 /8-MJLF I/O PRESENT, START 20 TO START
3139 /
3140 /
3141 /LOCATION 0436 CONTAINS INITIAL TAPE BLOCK
3142 /NUMBER. 12 BLOCKS ARE LOADED.
3143 /
3144 /RE-STARTING THE PROGRAM LOADS THE NEXT 12
3145 /BLOCKS.
3146 /
3147 /
3148 /
3149 /
3150 /
3151 /
3152 /
3153 /
3154 /
3155 /
3156 /
3157 /
3158 /
3159 /
3160 /
3161 /
3162 /
3163 /
3164 /
3165 /
3166 /
3167 /
3168 /
3169 /
3170 /
3171 /
3172 /
3173 /
3174 /
3175 /

```

```

0320 6141
3121 6233
3123 0433
3131 0437
3132 3633
3133 0224
3223 1020
3201 0650
3232 1040
3203 2441
3234 1340
3235 2445

```

```

PMJLF
*23
LINC
LMJLF
JMP MAIN
PMJLF
*100
SLATA
SISORT
SISIP
LSP
LMJLF
*233
LIA I
LIF 10
SIA
LSI
SIA
LS2

```

```

3176 0236 1040
3177 0207 0451
3178 0210 4410
3179 0211 3264
3180 0212 3777
3181 0213 0332
3182
3183 0214 4530
3184 0215 4531
3185 0216 4530
3186 0217 4531
3187 0220 4533
3188 0221 4531
3189 0222 4533
3190 0223 4531
3191 0224 4522
3192 0225 5224
3193
3194
3195
3196
3197
3198
3199
3200
3201
3202
3203
3204
3205
3206
3207 0400 0300
3208 0401 6141
3209
3210
3211
3212 3422 0367
3213 3423 7774
3214 3424 1203
3215 3425 0436
3216 0426 1120
3217 0427 5033
3218 0410 4416
3219 0411 1020
3220 0412 0331
3221 0413 0331
3222 0414 0650
3223 0415 3733
3224 0416 0300
3225 0417 1320
3226 0420 1301
3227 0421 1140
3228 0422 0416
3229 0423 0227
3230 0424 6415
3231 0425 1010
3232 0426 0416
3233 0427 1120
3234 0430 7776
3235 0431 4436
3236 0432 0366
3237 0433 2377
3238 0434 0332
3239
3240 0435 5600
3241
3242
3243
3244 0436 0200
3245
3246
3247

```

```

STA
LS3
SIC LLAT1
SFI I LPIR
3777
PEP
PMJLF
JMS I LLATA
JMS I LSJHI
JMS I LLATA
JMS I LSJHI
JMS I LLATA
JMS I LSJHI
JMS I LLATA
JMS I LSJHI
JMS I LISP
JMP --1
/
/
/
/
/SUBROUTINE TO LOAD 3 BLOCKS OF DATA
/
LMJLF
*433
PMJLF
SLATA, 0400
LINC
LMJLF
SFI I FCTR
-3
LLA
LIFLK
ALA I
5033
SIC LLAT2
LLA I
I
AKJ
LEAT1, LIF 10
RIC
LDAT2, 0400
LIA I
1001
ALC
LLAT2
KSK I FCTR
JMP LLAT1+1
LLA
LLAT2
ALA I
-1
SIC LIFLK
SFI I TPIR
2377
PEP
PMJLF
JMP I SLATA
/
/
/
/
LMJLF
LIFLK, 200
/
/
/

```

```

0175 /SUBROUTINE TO SORT DATA INTO FIELDS OF
0176 /10 FIELD NUMBERS, VERTICAL AND HORIZONTAL
0177 /COORDINATES
0231 /
0232 SLSORT, PNODE
0233 0437 0000 LINC
0234 0443 6141 LNODE
0235 0441 0650 LSI, LIF 10
0236 0442 1026 LLA 1 TPTR
0237 0443 0644 LIF 4
0238 0444 1064 STA 1 LPTR
0239 0445 0650 LSI, LIF 10
0240 0446 1026 LLA 1 TPTR
0241 0447 0645 LIF 5
0242 0450 1344 STA LPTR
0243 0451 0653 DS3, LIF 10
0244 0452 1026 LLA 1 TPTR
0245 0453 0646 LIF 6
0246 0454 1044 STA LPTR
0247 0455 0206 ASK TPTR
0248 0456 6441 JMP LSI
0249 0457 0000 PNODE
0250 0460 5637 JMP I SLSORT
/
/
/
/SUBROUTINE TO DISPLAY 512 POINTS AS A
/MOVING WINDOW OVER 1024 POINTS, WITH CURSOR
/
0234 +600
0235 0600 0300 SLISP, 0
0236 0631 6141 LINC
0237 LNODE
0238 0632 4100 NLISP, SA4 3 /SET UP START
0239 0633 1123 ADA I /LOCATION OF MOVING
0240 0634 1000 1000 /WINDOW
0241 0635 0341 SCR 1
0242 0636 3450 AZF
0243 0637 6612 JMP .+3
0244 0610 1623 PSF I
0245 0611 1777 1777
0246 0612 1623 PSE I
0247 0613 2300 2300
0248 0614 1040 STA
0249 0615 7334 LPTR
0250 0616 0134 SAM 4 /CURSOR
0251 0617 1123 ALA I
0252 0620 1300 1000
0253 0621 0341 SCR 1
0254 0622 2034 ADD LPTR
0255 0623 1623 PSE I
0256 0624 2033 2033
0257 0625 4335 STC CURS
0258 0626 0646 LIF 6
0259 0627 1335 LLA CURS
0260 0630 3061 SFI I 1
0261 0631 1136 LCI
0262 0632 7021 JMP P11
0263 0633 0363 SFT I 3
0264 0634 1324 LCRUF+47
0265 0635 7116 JMP GLCS

```

```

0274 0636 0645
0275 0637 1335
0276 0640 0061
0277 0641 1106
0278 0642 7021
0279 0643 0363
0280 0644 1302
0281 0645 7116
0282 0646 0644
0283 0647 1035
0284 0650 1560
0285 0651 7400
0286 0652 0361
0287 0653 1106
0288 0654 7021
0289 0655 0063
0290 0656 1260
0291 0657 7116
0292 0660 0363
0293 0661 1260
0294 0662 1005
0295 0663 0310
0296 0664 1560
0297 0665 7770
0298 0666 1120
0299 0667 1170
0300 0670 4672
0301 0671 1000
0302 0672 0000
0303 0673 1063
0304 0674 1000
0305 0675 0670
0306 0676 1120
0307 0677 0000
0308 0700 4702
0309 0701 1000
0310 0702 0000
0311 0703 1063
0312 0704 1005
0313 0705 0261
0314 0706 0472
0315 0707 6725
0316 0710 0363
0317 0711 1340
0318 0712 0362
0319 0713 1044
0320 0714 0361
0321 0715 7772
0322 0716 1022
0323 0717 1063
0324 0720 0021
0325 0721 6716
0326 0722 0376
0327 0723 7706
0328 0724 6727
0329 0725 0376
0330 0726 7714
0331 0727 0024
0332 0730 1563
0333 0731 0000
0334 0732 0000
0335 0733 0077
0336 0734 1000

```

```

LIF 5
LIA CURS
SFI I 1
LCI
JMP P11
SFT I 3
LCRUF+25
JMP GLCS
LIF 4
LIA CURS
PCL I
7400
SFT I 1
LCI
JMP P11
SFT I 3
LCRUF+3
JMP GLCS
SFI I 3
LCRUF+3
LIA CURS
RJR 10
PCL I
7770
ALA I
CIAP+20
SIC .+2
LLA
0000
SIA I 3
LIA
.-3
ALA I
40
SIC .+2
LIA
J000
STA I 3
LIA CURS
RJR I 1
LZF I
JMP I11
SFI I 3
LCRUF+63
SFI I 2
CIARI-1
SFI I 1
-5
LIA I 2
SIA I 3
ASK I 1
JAP .-3
SFI I 16
-71
JMP .+3
SFI I 16
-63
SFA
PCL I
200
PSF
SFI I 17
LCRUF-1

```

D11.

0373	0735	0361	SHI I 1
0374	0736	0300	0
0375	0737	1020	LDA I
0376	0740	0377	377
0377	0741	1777	LSC I 17
0403	0742	0236	KSK I 16
0401	0743	6741	J4P --2
0402	0744	6751	J4P DCUR
0403	0745	6777	J4P LD41
0404	0746	6751	J4P DCUR
0405	0747	0302	PDP
0406			P4J1F
0407	0750	0600	J4P I S11SP
0408			/
0409			/
0410			LMJDF
0411			LDA
0412			0
0413	0751	1000	LDA
0414	0752	0300	0
0415	0753	0776	SIC LCU1
0416	0754	0646	LIF 6
0417	0755	1005	LIA CURS
0418	0756	0343	SCR 3
0419	0757	1120	ADA I
0420	0760	0400	400
0421	0761	0300	SIC 1
0422	0762	0377	SHI I 17
0423	0763	0773	-4
0424	0764	0645	LDF 5
0425	0765	1005	LIA CURS
0426	0766	0342	SCR 2
0427	0767	1120	ALA I
0428	0770	0300	4
0429	0771	0141	LIS 1
0430	0772	1120	ALA I
0431	0773	0300	1
0432	0774	0237	KSK I 17
0433	0775	6771	JMP --4
0434	0776	6300	JMP 0
0435			ICUI,
0436			/
0437			/
0438			/
0439			LD41,
0440	0777	1000	LDA
0441	1000	0300	0
0442	1001	0400	SIC LIA2
0443	1002	0377	SHI I 17
0444	1003	0777	0777
0445	1004	0646	LIF 6
0446	1005	1000	LIA I LPTR
0447	1006	0343	SCR 3
0448	1007	1120	ALA I
0449	1008	0400	400
0450	1009	0300	SIC 1
0451	1010	0645	LIF 5
0452	1011	1000	LIA LPTR
0453	1012	0342	SCR 2
0454	1013	0141	LIS 1
0455	1014	0237	KSK I 17
0456	1015	0734	JMP DL41
0457	1016	0300	JMP 0
0458			LIA2,
0459			/
0460			/
0461			/

0471			0471
0472			0472
0473			0473
0474			0474
0475			0475
0476			0476
0477			0477
0500			0500
0501			0501
0502			0502
0503			0503
0504			0504
0505			0505
0506			0506
0507			0507
0508			0508
0509			0509
0510			0510
0511			0511
0512			0512
0513	1021	0361	0513
0514	1022	0261	0514
0515	1023	0471	0515
0516	1024	0317	0516
0517	1025	0665	0517
0518	1026	0200	0518
0519	1027	0101	0519
0520	1030	1000	0520
0521	1031	0540	0521
0522	1032	0452	0522
0523	1033	0306	0523
0524	1034	1361	0524
0525	1035	0362	0525
0526	1036	1101	0526
0527	1037	0354	0527
0528	1040	0450	0528
0529	1041	0250	0529
0530	1042	0301	0530
0531	1043	1341	0531
0532	1044	0452	0532
0533	1045	0306	0533
0534	1046	1361	0534
0535	1047	0407	0535
0536	1050	0377	0536
0537	1051	1361	0537
0538	1052	0354	0538
0539	1053	0350	0539
0540	1054	1000	0540
0541	1055	0300	0541
0542	1056	0375	0542
0543	1057	0263	0543
0544	1061	0222	0544
0545	1062	0365	0545
0546	1063	0223	0546
0547	1064	1361	0547
0548	1065	0300	0548
0549	1066	1102	0549
0550	1067	0451	0550
0551	1070	0263	0551
0552	1071	1000	0552
0553	1072	0300	0553
0554	1073	1500	0554
0555	1074	0567	0555

```

/
/
/PRINTC 1
/FORM PDP-12
/D-J. NICHOLS
/UNIV. WISCONSIN
/
/SIGNED 12-BIT BINARY TO DECIMAL CONVERSION
/
/ENTER VIA: JMP PDI
/ WITH BINARY NUMBER IN ACCUMULATOR
/ AND IR 1 PRESET
/ DECIMAL 6-BIT ASCII CODE STORED VIA IR1
/ LEADING ZEROS SUPPRESSED
/ RIGHT JUSTIFIED
/ NON-INTERLUPTABLE
/USES IR 1-3
/
PDI, SCR I 1 /SIGN TO LINK BIT
ROL I 1
APJ I /MAKE NEGATIVE
CJA
SIC P9PDI /SAVE II
ALL 0 /SAVE RETURN
SIC P9PDI
LIA I /LOAD POSSIBLE SIGNS
K9PDI, 5540 /"- "
LZF /GET CORRECT SIGN
ROR 6
SHI I 1 /AND STORE II
SET I 2 /SET ADDRESS POINTER
P9PDI-1
JMP K9PDI /CONVERT A DIGIT
AZF /IS IT A LEADING ZERO?
JMP L9PDI /NO
ALL K9PDI /YES:MOVE SIGN RIGHT 1 PLACE
SHI I /STORE A SPACE OVER THE SIGN
LZF /GET THE CORRECT SIGN
ROR 6
SHI I 1 /AND STORE II
JMP --10 /THEN CONTINUE CONVERTING
L9PDI, ALL P9PDI-2 /MAKE ASCII
SHI I 1 /STORE A DIGIT
JMP K9PDI /CONVERT NEXT DIGIT
JMP L9PDI /AND STORE II
/LITTLE SUBROUTINE TO DO THE CONVERSION
M9PDI, LDA /SAVE RETURN
0
SIC P9PDI-4
SHI I 3 /SET DIGIT COUNTER
1777
KSK I 2 /MOVE ADDRESS POINTER
ADD M9PDI /LOAD THE VALUE
KSK I 3 /PUMP COUNTER
SHI I /SAVE REMAINING PART
M9PDI, 0
ADA 2 /INITIAL ADD
APJ /STILL NEGATIVE?
JMP --5 /YES:COUNT AND ADD AGAIN
LDA /NO: GET THE COUNTER
3
SHI I /LAST DIGIT?
3567

```

0570	1075	0000	JMP 0	/NO: BACK TO THE SUBROUTINE
0571	1076	1600	PSF I	/YES:MAKE ASCII
0572	1077	0000	0000	
0573	1100	1361	STH I 1	/STORE IT
0574	1101	0000	P9RL1, JMP 0	/AND RETURN TO CALLING PGM
0575			/ADDEVD TABLE	
0576	1102	1750	Q9RL1, 1750	
0577	1103	0144	144	
0600	1104	0312	12	
0601	1105	0001	1	
0602			/END PINDEX 1	
0603			/	
0604			/	
0605	1106	0000	DCI, 0	
0606	1107	0000	0	
0607	1110	0000	0	
0610	1111	0000	0	
0611	1112	0000	0	
0612	1113	0000	0	
0613	1114	0000	0	
0614	1115	0000	0	
0615			LMODE	
0616			/	
0617			/	
0620	1116	1000	GLCS, LDA	
0621	1117	0000	0	
0622	1120	0147	SIC GD2	
0623	1121	0001	SET I 1	
0624	1122	1106	DCI	
0625	1123	0002	SET I 2	
0626	1124	7772	-5	
0627	1125	0011	CLR	
0630	1126	1001	GD1, LDH I 1	
0631	1127	1100	ADA I	
0632	1131	1110	CTAB-40	
0633	1131	0100	SIC ++2	
0634	1132	1000	LLA	
0635	1133	0000	0000	
0636	1134	1000	STA I 3	
0637	1135	1000	LLA	
0640	1136	1100	--3	
0641	1137	1100	ADA I	
0642	1140	0000	40	
0643	1141	0140	SIC ++2	
0644	1142	1000	LLA	
0645	1143	0000	0000	
0646	1144	1000	STA I 3	
0647	1145	0000	ASK I 2	
0650	1146	7126	JMP GD1	
0651	1147	0000	GD2, JMP 0	
0652			/	
0653			/	
0654			/	
0655	1153	0000	CTAB, 0	
0656	1151	0000	0	
0657	1152	0000	0	
0660	1153	0000	0	
0661	1154	0000	0	
0662	1155	0000	0	
0663	1156	0000	0	
0664	1157	0000	0	
0665	1160	0000	0	
0666	1161	0000	0	

0667	1162	0000	0	
0670	1163	0000	0	
0671	1164	0000	0	
0672	1165	0004	0004	/-
0673	1166	0000	0	
0674	1167	0000	0	
0675	1170	4136	4136	/0
0676	1171	2101	2101	/1
0677	1172	4503	4503	/2
0700	1173	4122	4122	/3
0701	1174	2414	2414	/4
0702	1175	5172	5172	/5
0703	1176	1506	1506	/6
0704	1177	4443	4443	/7
0705	1200	5126	5126	/8
0706	1201	5122	5122	/9
0707	1202	0000	0	
0710	1203	0000	0	
0711	1204	0000	0	
0712	1205	0000	0	
0713	1206	0000	0	
0714	1207	0000	0	
0715	1210	0000	0	
0716	1211	0000	0	
0717	1212	0000	0	
0720	1213	0000	0	
0721	1214	0000	0	
0722	1215	0000	0	
0723	1216	0000	0	
0724	1217	0000	0	
0725	1220	0000	0	
0726	1221	0000	0	
0727	1222	0000	0	
0730	1223	0000	0	
0731	1224	0000	0	
0732	1225	0004	0004	/-
0733	1226	0000	0	
0734	1227	0000	0	
0735	1230	3641	3641	/0
0736	1231	0177	0177	/1
0737	1232	2151	2151	/2
0740	1233	2651	2651	/3
0741	1234	0477	0477	/4
0742	1235	0651	0651	/5
0743	1236	4225	4225	/6
0744	1237	6050	6050	/7
0745	1240	2651	2651	/8
0746	1241	3651	3651	/9
0747	1242	0000	0	
0750	1243	0000	0	
0751	1244	0000	0	
0752			/	
0753			/	
0754	1245	4477	CTAB1, 4477	
0755	1246	4044	4044	/F
0756	1247	0000	0000	
0757	1250	4477	4477	
0760	1251	3044	3044	/P
0761	1252	0000	0	
0762	1253	0000	0	
0763	1254	0000	0	
0764	1255	4477	DCBUF, 4477	
0765	1256	4044	4044	/F

0766	1257	0330	0
0767	1260	0330	0
0770	1261	0330	0
0771	1262	0330	0
0772	1263	0330	0
0773	1264	0330	0
0774	1265	0330	0
0775	1266	0330	0
0776	1267	0330	0
0777	1270	0330	0
1030	1271	0330	0
1031	1272	0330	0
1032	1273	0330	0
1033	1274	0330	0
1034	1275	0330	0
1035	1276	0330	0
1036	1277	0176	0176
1037	1300	7402	7402
1010	1301	0330	0
1011	1302	0330	0
1012	1303	0330	0
1013	1304	0330	0
1014	1305	0330	0
1015	1306	0330	0
1016	1307	0330	0
1017	1310	0330	0
1020	1311	0330	0
1021	1312	0330	0
1022	1313	0330	0
1023	1314	0330	0
1024	1315	0330	0
1025	1316	0330	0
1026	1317	0330	0
1027	1320	0330	0
1030	1321	1077	1077
1031	1322	7710	7710
1032	1323	0330	0
1033	1324	0330	0
1034	1325	0330	0
1035	1326	0330	0
1036	1327	0330	0
1037	1330	0330	0
1040	1331	0330	0
1041	1332	0330	0
1042	1333	0330	0
1043	1334	0330	0
1044	1335	0330	0
1045	1336	0330	0
1046	1337	0330	0
1047	1340	0330	0
1050	1341	0330	0
1051	1342	0330	0
1052	1343	0330	0
1053	1344	0330	0
1054	1345	0330	0
1055	1346	0330	0
1056	1347	0330	0
1057	1350	0330	0
1060	1351	0330	0
1061	1352	0330	0
1062	1353	0330	0
1063			
1064			

/V

/H

1065				
1066				
1067				*4
1070	0004	0000	LPTR	0
1071	0005	0000	CURS	0
1072	0006	0000	TPTR	0
1073	0007	0000	FCTR	0
1074				*42

NO ERRORS

FDI	1021
CTAB	1150
CTARI	1245
CURS	1005
LCPUF	1255
DCT	1106
DCUR	0751
DCU1	0776
LDAT	0777
DLA1	1004
DLA2	1020
DISP	0102
DII	0725
EPTR	0004
DSORT	0101
DSP	0004
DSPL	0103
ES1	0441
ES2	0445
ES3	0451
L1PLK	0436
FCIR	0007
GDGS	1116
GD1	1126
GL2	1147
K9FD1	1031
LLATA	0100
LLAT1	0414
LLAT2	0416
L9RL1	1050
MAIN	0200
M9FD1	1054
VDISP	0602
V9PL1	1065
P9FD1	1101
Q9PL1	1102
SDISP	0600
SLSORT	0437
SLLA1A	0400
TPTR	0006

0175	0437	0000	SDSORT,	0000	0274	0641	1777	DSC I 17
0176	0440	6141		LINC	0275	0642	0236	XSK I 16
0177				LMODE	0276	0643	6641	JMP --2
0200	0441	0650	DS1,	LDF 10	0277	0644	0356	SET 16
0201	0442	1026		LDA I TPTR	0300	0645	0004	DPTR
0202	0443	0644		LDF 4	0301	0646	0375	SET I 15
0203	0444	1064		STA I DPTS	0302	0647	2776	2776
0204	0445	0650	DS2,	LDF 10	0303	0650	0644	LDF 4
0205	0446	1326		LDA I TPTR	0304	0651	0235	DI1, XSK I 15
0206	0447	0645		LDF 5	0305	0652	0456	SKP
0207	0453	1344		STA DPTR	0306	0653	6665	JMP DI2
0210	0451	0653	DS3,	LDF 10	0307	0654	1036	LDA I 16
0211	0452	1326		LDA I TPTR	0310	0655	1563	RCL I
0212	0453	0646		LDF 6	0311	0656	7400	7400
0213	0454	1044		STA DPTR	0312	0657	1440	SAI
0214	0455	0206		XSK TPTR	0313	0660	0005	CURS
0215	0456	6441		JMP DSI	0314	0661	6651	JMP DI1
0216	0457	0002		PDP	0315	0662	6679	JMP DCUR
0217				Pmode	0316	0663	0644	LDF 4
0209	0460	5637		JMP I SDSORT	0317	0664	6651	JMP DI1
0221			/		0320	0665	6716	DI2, J4P DDAT
0222			/		0321	0666	0002	PLP
0223			/		0322			Pmode
0224			/SUBROUTINE TO DISPLAY 512 POINTS AS A		0323	0667	5603	JMP I SDISP
0225			/MOVING WINDOW OVER 1024 POINTS, WITH CURSOR		0324			
0226			/		0325			
0227				LMODE				
0231				*600				
0232	0600	0000	SDISP,	Pmode				
0233	0601	6141		0000				
0234				LINC				
0235	0602	0311		LMODE				
0236	0603	0100		CLR				
0237	0604	1120		SAI 0				/SET UP START
0240	0605	1000		ADA I				/LOCATION OF MOVING
0241	0606	1620		1000				/WINDOW
0242	0607	2000		PSE I				
0243	0610	4034		2000				
0244	0611	3104		STC DPTR				/CURSOR
0245	0612	1120		SAI 4				
0246	0613	1000		ADA I				
0247	0614	0342		1000				
0250	0615	1040		SCR 2				
0251	0616	0005		STA				
0252	0617	0061		CURS				
0253	0620	1025		SET I 1				
0254	0621	6740		DCT				
0255	0622	0063		JMP RDI				
0256	0623	1177		SET I 3				
0257	0624	7035		DCRUF+3				
0209	0625	0004		JMP GDOS				
0261	0626	1560		SFA				
0262	0627	0200		PCL I				
0263	0630	0034		200				
0264	0631	0077		LSF				
0265	0632	1173		SET I 17				
0266	0633	0061		DCRUF-1				
0267	0634	0000		SET I 1				
0271	0635	0076		0				
0271	0636	7757		SET I 16				
0272	0637	1020		-20				
0273	0640	0377		LDA I				
				377				


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0175 /COORDINATES
0176 /
0177 PMODE
0200 SDSORT, 0000
0201 0437 0000 LINC
0202 0443 6141 LMODE
0203 LDF 10
0204 0440 1026 LLA I IPTR
0205 0443 0644 LDF 4
0206 0444 1064 STA I LPTR
0207 0445 0650 DS2, LDF 10
0208 0446 1026 LLA I TPTR
0209 0447 0645 LDF 5
0210 0450 1044 STA DPTR
0211 0451 0650 DS3, LDF 10
0212 0452 1026 LDA I IPTR
0213 0453 0646 LDF 6
0214 0454 1044 STA DPTR
0215 0455 0206 XSK IPTR
0216 0456 6441 JMP DS1
0217 0457 0000 PLP
0218 PMODE
0219 0460 5637 JMP I SDSORT
0220 /
0221 /
0222 /
0223 /SUBROUTINE TO DISPLAY 512 POINTS AS A
0224 /MOVING WINDOW OVER 1024 POINTS, WITH CURSOR
0225 /
0226 *600
0227 SLISP, 0
0228 0600 0000 LINC
0229 0601 6141 LMODE
0230 NDISP, SAM 0 /SET UP START
0231 SAM 0 /LOCATION OF MOVING
0232 ADA I /WINDOW
0233 1000
0234 SCR 1
0235 AZE
0236 JMP +-3
0237 RSE I
0238 1777
0239 RSE I
0240 2000
0241 STA
0242 DPTR
0243 SAM 4 /CURSOR
0244 ALA I
0245 1000
0246 SCR 1
0247 ADD LPTR
0248 RSE I
0249 2000
0250 STC CURS
0251 4005
0252 0626 0646 LDF 6
0253 0627 1005 LDA CURS
0254 0630 0361 SFT I 1
0255 0631 1122 LCT
0256 0632 7035 JMP BDI
0257 0633 0063 SET I 3
0258 0634 1340 DCRUF+47
0259 0635 7132 JMP GDCS
0260 0636 0645 LDF 5
0261 0637 1005 LLA CURS

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0274 0640 0061 SFT I 1
0275 0641 1122 LCT
0276 0642 7035 JMP BDI
0277 0643 0063 SET I 3
0278 0644 1316 DCRUF+25
0279 0645 7132 JMP GDCS
0280 0646 0644 LDF 4
0281 0647 1005 LLA CURS
0282 0650 1560 RCL I
0283 0651 7400 7400
0284 0652 0061 SFT I 1
0285 0653 1122 LCT
0286 0654 7035 JMP BDI
0287 0655 0063 SET I 3
0288 0656 1274 DCRUF+3
0289 0657 7132 JMP GDCS
0290 0660 0063 SET I 3
0291 0661 1274 DCRUF+3
0292 0662 1005 LLA CURS
0293 0663 0310 ROR 10
0294 0664 1560 RCL I
0295 0665 7770 7770
0296 0666 1120 ADA I
0297 0667 1204 CTAB+20
0298 0670 4672 STC +-2
0299 0671 1000 LLA
0300 0672 0000 0000
0301 0673 1063 STA I 3
0302 0674 1003 LLA
0303 0675 0672 +-3
0304 0676 1120 ADA I
0305 0677 0040 40
0306 0700 4702 STC +-2
0307 0701 1000 LLA
0308 0702 0000 0000
0309 0703 1063 STA I 3
0310 0704 1005 LLA CURS
0311 0705 0061 RCL I 1
0312 0706 0472 LZE I
0313 0707 6725 JMP DDI
0314 0710 0063 SFT I 3
0315 0711 1354 DCRUF+63
0316 0712 0062 SFT I 2
0317 0713 1260 CTAB1-1
0318 0714 0061 SFT I 1
0319 0715 7772 -5
0320 0716 1022 LDA I 2
0321 0717 1063 STA I 3
0322 0720 0221 XSK I 1
0323 0721 6716 JMP +-3
0324 0722 0076 SET I 16
0325 0723 7706 -71
0326 0724 6727 JMP +-3
0327 0725 0076 SFT I 16
0328 0726 7714 -E3
0329 0727 0024 SFA
0330 0730 1560 RCL I
0331 0731 0000 000
0332 0732 0004 EBF
0333 0733 0077 SFT I 17
0334 0734 1270 DCRUF-1
0335 0735 0061 SFT I 1
0336 0736 0000 0

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DI1,

0373	0737	1020	LLA I
0374	0740	0377	377
0375	0741	1777	DSC I 17
0376	0742	0236	XSK I 16
0377	0743	6741	JMP --2
0400	0744	6751	JMP DCUR
0401	0745	7005	JMP DLAT
0402	0746	6751	JMP DCUR
0403	0747	0202	PDP
0404			PMODE
0405	0750	5600	JMP I SDISP
0406			
0407			
0410			PMODE
0411			
0412	0751	1000	DCUR, LDA
0413	0752	0333	0
0414	0753	5004	STC DCU1
0415	0754	0646	LDF 6
0416	0755	1025	LDA CURS
0417	0756	0341	SCR I
0420	0757	4001	STC I
0421	0760	3077	SET I 17
0422	0761	7773	-4
0423	0762	0645	LDF 5
0424	0763	1005	LDA CURS
0425	0764	1120	ALA I
0426	0765	7307	-470
0427	0766	0471	APU I
0430	0767	6772	JMP ++3
0431	0770	1120	ADA I
0432	0771	0470	470
0433	0772	1120	ALA I
0434	0773	7577	-200
0435	0774	0317	CJM
0436	0775	1120	ADA I
0437	0776	0304	4
0440	0777	0141	DIS I
0441	1000	1120	ALA I
0442	1001	0001	1
0443	1002	0237	XSK I 17
0444	1003	6777	JAP --4
0445	1004	6000	JMP 0
0446			DCU1,
0447			/
0450			/
0451			/
0452			/
0453	1005	1000	DDAT, LDA
0454	1006	0300	0
0455	1007	5034	STC DDA2
0456	1010	0077	SH I 17
0457	1011	2777	COUNTR, 2777
0460	1012	0646	DDA1, LDF 6
0461	1013	1024	LDA I DPTR
0462	1014	0341	SCR I
0463	1015	4001	STC I
0464	1016	0645	LDF 5
0465	1017	1034	LDA DPTR
0466	1020	1120	ALA I
0467	1021	7307	-470
0470	1022	0471	APU I
0471	1023	7026	JAP ++3

0472	1024	1120	ADA I
0473	1025	0470	470
0474	1026	1120	ADA I
0475	1027	7577	-200
0476	1030	0017	CJM
0477	1031	0141	DIS I
0500	1032	0237	XSK I 17
0501	1033	7012	JMP DDA1
0502	1034	6000	JAP 0
0503			DDA2,

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0330 *20
0331 /DISPLAY DATA WITH FIELD CURSOR - DISF
0332 /
0333 /
0334 /18-5-76
0335 /
0336 /M.O.JARRETT.
0337 /
0338 /
0339 /DISPLAYS DATA FROM TAPE CONTAINING
0340 /CU-ORDINATE DATA ON UNIT 2-READ OUT
0341 /OF THE TV FIELD NUMBER IS PROVIDED.
0342 /CORRESPONDING TO CURSORS WHICH MARK
0343 /EVERY POINT OCCURRING IN THE TV FIELD
0344 /SELECTED.
0345 /
0346 /
0347 /
0348 /
0349 /
0350 /DATA TO BE DISPLAYED IS PLACED IN A
0351 /1024 POINT BUFFER, AND A MOVING WINDOW
0352 /TO DISPLAY THE DATA IS CONTROLLED BY
0353 /POT 3. CURSORS ARE CONTROLLED BY POT 4
0354 /TO PROVIDE A READOUT OF DATA AS DESCRIBED
0355 /ABOVE.
0356 /
0357 /
0358 /
0359 /
0360 /
0361 /PROGRAM STARTED BY I/O PRESET, START 20
0362 /IN 8 MODE
0363 /
0364 /
0365 /LOCATION 436 CONTAINS START BLK FOR DATA
0366 /TO BE DISPLAYED.
0367 /RE-STARTING PROGRAM LOADS NEXT 12 BLOCKS
0368 /OF DATA.
0369 /
0370 /
0371 /
0372 /
0373 /
0374 /
0375 /
0376 /
0377 /
0378 /
0379 /
0380 /
0381 /
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0076 0212 3777 3777
0077 0213 0002 PDP
0100 PMODE
0101 0214 4500 JMS I LLATA
0102 0215 4501 JMS I DSORT
0103 0216 4500 JMS I LDATA
0104 0217 4501 JMS I DSORT
0105 0220 4500 JMS I LLATA
0106 0221 4501 JMS I DSORT
0107 0222 4500 JMS I LDATA
0110 0223 4501 JMS I DSORT
0111 0224 4502 DSP, JMS I DISP
0112 0225 5224 JMP --1
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0114 /
0115 /
0116 /SUBROUTINE TO LOAD 3 BLOCKS OF DATA
0117 /
0120 LMODE
0121 *400
0122 PMODE
0123 0400 0000 SLDATA, 0000
0124 0401 6141 LINC
0125 LMODE
0126 0402 0067 SET I FCTR
0127 0403 7774 -3
0130 0404 1000 LDA
0131 0405 0436 DTPLK
0132 0406 1120 ADA I
0133 0407 5000 5000
0134 0410 4416 STC LDAT2
0135 0411 1020 LDA I
0136 0412 0001 1
0137 0413 0001 AKO
0140 0414 0653 LDAT1, LDF 10
0141 0415 0700 PDC
0142 0416 0000 LDAT2, 0000
0143 0417 1000 LDA I
0144 0420 1001 1001
0145 0421 1140 ADA
0146 0422 0416 LDAT2
0147 0423 0227 XSK I FCTR
0150 0424 6415 JMP LDAT1+1
0151 0425 1000 LDA
0152 0426 0416 LDAT2
0153 0427 1120 ADA I
0154 0430 7776 -1
0155 0431 4436 STC DTPLK
0156 0432 0066 SET I TPIR
0157 0433 2377 2377
0160 0434 0002 PDP
0161 PMODE
0162 0435 5600 JMP I SLDATA
0163 /
0164 LMODE
0165 /
0166 0436 0112 DTPLK, 112
0167 /
0170 /
0171 /SUBROUTINE TO SORT DATA INTO FIELDS OF
0172 /TV FIELD NUMBERS, VERTICAL AND HORIZONTAL
0173 /COORDINATES
0174 /

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0175          PMODE
0176      0437 0000 SDSORT, 0000
0177      0440 6141          LINC
0203          LMODE
0201      0441 0650 DS1,   LDF 10
0202      0442 1026          LDA I TPTR
0203      0443 0644          LDF 4
0204      0444 1064          STA I DPTR
0205      0445 0650 DS2,   LDF 10
0206      0446 1026          LDA I TPTR
0207      0447 0645          LDF 5
0210      0450 1044          STA DPTR
0211      0451 0650 DS3,   LDF 10
0212      0452 1026          LDA I TPTR
0213      0453 0646          LDF 6
0214      0454 1044          STA DPTR
0215      0455 0206          ASK TPTR
0216      0456 6441          JMP DS1
0217      0457 0002          PLP
0220          PMODE
0221      0460 5637          JMP I SDSORT
0222      /
0223      /
0224      /
0225          /SUBROUTINE TO DISPLAY 512 POINTS AS A
0226          /MOVING WINDOW OVER 1024 POINTS, WITH CURSOR
0227      /
0228      /
0231          *600
0232      0600 0000 SDISP,  0
0233      0601 6141          LINC
0234          LMODE
0235      0602 0100 NDISP,  SAM 0          /SET UP START
0236      0603 1120          ADA I          /LOCATION OF MOVING
0237      0604 1000          1000          /WINDOW
0243      0605 0341          SCR 1
0244      0606 0450          AZE
0242      0607 6612          JMP .+3
0243      0610 1620          PSE I
0244      0611 1777          1777
0245      0612 1620          DSE I
0246      0613 2000          2000
0247      0614 1040          STA
0251      0615 0004          DPTR
0251      0616 0104          SAM 4          /CURSOR
0252      0617 1120          ADA I
0253      0620 1000          1000
0254      0621 0342          SCR 2
0255      0622 1040          STA
0256      0623 0005          CURS
0257      0624 0061          SET I 1
0260      0625 1046          DCT
0261      0626 0761          JMP REI
0262      0627 0063          SET I 3
0263      0630 1020          DCRUF+3
0264      0631 7056          JMP GDCS
0265      0632 0024          SFA
0266      0633 1560          PCL I
0267      0634 0200          200
0274      0635 0004          HSF
0271      0636 0377          SET I 17
0272      0637 1214          DCRUF-1
0273      0640 0001          SET I 1

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0274      0641 0000          0
0275      0642 0076          SET I 16
0276      0643 7757          -R0
0277      0644 1020          LDA I
0300      0645 0377          377
0301      0646 1777          DSC I 17
0302      0647 0236          KSK I 16
0303      0650 6646          JMP .-2
0304      0651 0056          SET 16
0305      0652 0004          DPTR
0306      0653 0075          SET I 15
0307      0654 2777          2777
0310      0655 0644          LDF 4
0311      0656 0235          DI1,  KSK I 15
0312      0657 0456          SKP
0313      0660 6672          JMP DI2
0314      0661 1036          LDA I 16
0315      0662 1560          PCL I
0316      0663 7400          7400
0317      0664 1440          SAI
0320      0665 0005          CURS
0321      0666 6656          JMP DI1
0322      0667 6675          JMP LCUR
0323      0670 0644          LDF 4
0324      0671 6656          JMP DI1
0325      0672 6731          DI2,  JMP LLAT
0326      0673 0002          PLP
0327          PMODE
0330      0674 5600          JMP I SDISP
0331      /
0332      /

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APPENDIX A.4

Characteristics of "Scotchlite" Retro-reflective sheeting.

Table 1. Data from the manufacturer's literature.

Table 2. Data from the manufacturer's literature.

Table 3. Data from the manufacturer's literature.

Angle of View (degrees)	100°
Lighting Factor	100%
Viewing distance (feet)	1000

Table 4. Data from the manufacturer's literature.

Table 5. Data from the manufacturer's literature.

Since the retro-reflective sheeting is made of a plastic material, it is not as durable as other materials. The sheeting is made of a plastic material, and it is not as durable as other materials. The sheeting is made of a plastic material, and it is not as durable as other materials.

Optical Properties

"Scotchlite" Brand Reflective Sheetings High Gain # 7610 and # 7611 have the retro-reflectance values listed on the following tables. The values are expressed as a multiple of the brightness of a perfect diffuse white surface. This multiple is shown as the luminance factor. These values were obtained from retro-reflectance measurements of typical samples of #7610 and #7611 Sheetings.

Table I - Luminance Factor vs. Incidence Angle

Angle of Incidence	0°	10°	20°	30°	45°
Luminance Factor	590	595	620	660	710

(All readings were taken at an 0.5° divergence angle).

Table II - Luminance Factor vs. Divergence Angle

Angle of Divergence	0°	1/4°	1/3°	1/2°	3/4°	1°	1-1/2°
Luminance Factor	1610	1280	1090	590	195	115	55

(All readings were taken at a 0° incidence angle).

Table I data shows that retro-reflectance of # 7610/#7611 Sheetings remain high up to very oblique angles of incidence.

Table II shows that retro-reflective efficiency of #7610/#7611 Sheetings decrease rapidly as the angle between the incident light ray and the sensor or receiver is increased only slightly.

Since the retro-reflective surface of "Scotchlite" Brand Reflective Sheetings High Gain # 7610 and # 7611 is actually a continuous bond of exposed glass spheres, the result is a surface which is non-specular and therefore resistant to glare interference from ambient light.

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